### High Directivity

## **Monolithic Amplifier**

## MNA-2W+

 $50\Omega$  0.5 to 4.5 GHz





## **The Big Deal**

- Integrated matching, DC Blocks and bias circuits
- Excellent Active Directivity
- Operates over 2.8-5V

CASE STYLE: DQ849

#### **Product Overview**

MNA-2W+ is a wideband PHEMT based MMIC amplifier with high active Directivity. MNA integrates the entire matching network and majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the MNA amplifier extremely straightforward to use. This design operates on a single 2.8 to 5V supply, is well matched for  $50\Omega$  and comes in a tiny, low profile 3x3mm 8-lead MCLP package accommodating dense circuit board layouts.

## **Key Features**

Feature	Advantages	
Excellent Active Directivity (Isolation- Gain) 21-36 dB	Ideal for use as a buffer amplifier minimizing interaction of adjacent circuits	
Integrates DC blocks and RF choke	Minimizes external components, component count and circuit area.	
Single +2.8 to +5V operation	Amplifier can be used at low voltage such as +3V or standard +5V. +5V operation results in higher P1dB and OIP3.	
3 x 3mm 8-lead MCLP package	Tiny footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB.	

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## 0.5-4.5 GHz

#### **Product Features**

- Choice of supply voltage, +2.8V to +5V
- Micro-miniature size .120"X.120"
- Internal DC blocking at RF input and output
- High directivity, +21 dB typ. at 2.5 GHz and 5V
- Output power, +17.5 dBm typ. at 2.5 GHz and 5V
- Aqueous washable





## MNA-2W+

CASE STYLE: DQ849

+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

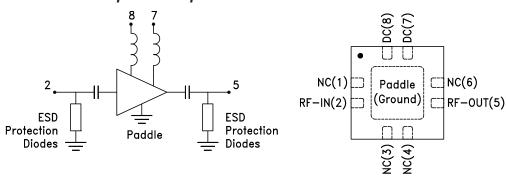
#### **Typical Applications**

- Buffer amplifier
- Cellular infrastructure
- Communications satellite
- Defense

#### **General Description**

MNA-2W+ is a wideband PHEMT based MMIC amplifier with high active Directivity. MNA integrates the entire matching network and majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the MNA amplifier extremely straightforward to use. This design operates on a single +2.8 to +5V supply, is well matched for  $50\Omega$  and comes in a tiny, low profile 3x3mm 8-lead MCLP package accommodating dense circuit board layouts.

#### simplified schematic and pad description



Function	Pad Number	Description (See Fig 1)	
RF IN	2	RF input pin	
RF-OUT	5	RF output pin	
DC	7,8	DC Bias pads 7,8. Pad 7 connected to ground via 1000 pF. Pad 8 connected to pad 7 via 33 ohms.	
NC	1,3,4,6	Not Connected, connect pad 3 and 4 to ground externally	
GND	Paddle	Ground	
OPTIONAL	1,6	No internal connection; recommended use: per PCB Layout PL-078	



### Electrical Specifications<sup>1</sup> at 25°C

Parameter	Condition (GHz)	Vs=5V			Vs=2.8V	Unite
raiailletei		Min.	Тур.	Max.	Тур.	Units
Frequency Range		0.5		4.5	0.5-4.5	GHz
Gain	0.5	_	14.1	_	12.4	dB
	1.0	_	15.0	_	13.0	
	2.0	13.5	15.0	16.5	12.6	
	2.5	_	14.7	_	12.3	
	3.5	_	12.8	_	10.6	
	4.5	9.0	9.7	11.5	7.8	
Input Return Loss	0.5		8		8	dB
	1.0		16		17	
	2.0		19		22	
	2.5		17		19	
	3.5		14		14	
	4.5		9		9	
Output Return Loss	0.5		14		13	dB
•	1.0		21		18	
	2.0		15		18	
	2.5		14		17	
	3.5		15		19	
	4.5		17		17	
Output Power at P1dB	0.5		19.2		10.3	dBm
output i onoi ut i iub	1.0		19.1		11.4	
	2.0		17.9		11.8	
	2.5		17.5		11.8	
	3.5		15.8		11.7	
	4.5		13.9		11.4	
Output IP3	0.5		32		22	dBm
Catpat II C	1.0		31		23	d Dill
	2.0		29		23	
	2.5		29		23	
	3.5		27		23	
	4.5		25		22	
Noise Figure (dB)	0.5		5.6		5.8	dB
Noise i igure (db)	1.0		5.3		5.4	ub ub
					1	
	2.0 2.5		5.3 5.4		5.5	
					5.6	
	3.5		5.6		5.7	
Discoult it.	4.5		6.3		6.5	-ID
Directivity (Isolation Gain)	0.5		33		36	dB
(Isolation-Gain)	1.0		29		28	
	2.0		22		22	
	2.5		21		21	
	3.5		21		21	
	4.5		24		23	
DC Current			84	104	79	mA
Device Current Variation vs. Temperature <sup>(2)</sup>			32		15	μΑ/°C
Device Current Variation vs Voltage			0.0013		0.0034	mA/mV
Thermal resistance at 85°C (Junction to Lead)			54		54	°C/W

<sup>(1)</sup> Measured on Mini-Circuits Characterization test board TB-186+. See Characterization Test Circuit (Fig. 1) (2) (Current at 85°C -Current at -45°C)/130 (3) (Current at 5.25V-Current at 3.9V)/1.35 (4) (Current at 3.9V-Current at 2.66V)/1.24

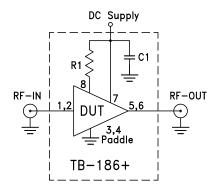
#### Absolute Maximum Ratings<sup>5</sup>

Absolute maximum ramige			
Parameter	Ratings		
Operating Temperature	-40°C to 85°C		
Storage Temperature	-55°C to 100°C		
DC Voltage	7 V at pin 7 (on TB-186+)		
	1V at pins 2 & 5		
Power Dissipation	800 mW		
Input Power	+11 dBm at Vs=+2.8V and		
	+16 dBm at+5V (continuous operation)		
	+23 dBm (5 minutes max)		

<sup>5.</sup> Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.



#### **Characterization & Application Test Circuit**



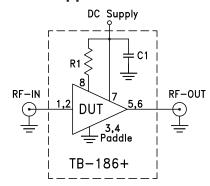
Component	Size	Value	Units
R1	0805	33.2	Ω
C1	0402	1000	ρF

Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-186+) Gain, Return loss, Output power at 1dB compression (P1 dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

#### Conditions:

- 1. Gain and Return loss: Pin= -25dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 0 dBm/tone at output.

#### **Recommended Application Circuit**



Component	Size	Value	Units
R1	0805	33.2	Ω
C1	0402	1000	ρ <b>F</b>

Fig 2. Test Board includes case, connectors, and components soldered to PCB

#### **Product Marking**



Additional Detailed Technical Information additional information is available on our dash board. To access this information click here		
	Data Table	
Performance Data	Swept Graphs	
	S-Parameter (S2P Files) Data Set (.zip file)	
Case Style	DQ849 3x3x0.9 mm MCLP Plastic package, exposed paddle lead finish: tin-silver over nickel	
Tape & Reel	F104	
Standard quantities available on reel	7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices	
Suggested Layout for PCB Design	PL-078	
Evaluation Board	TB-186-2W+	
Environmental Ratings	ENV08T1	

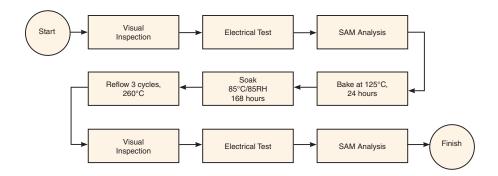
#### **ESD Rating**

Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (Pass 50V) in accordance with ANSI/ESD STM5.2-1999

#### **MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D



#### **Additional Notes**

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

