



AWU6615

HELP3™ Dual-band Cell & PCS WCDMA
3.4 V Linear Power Amplifier Module
Data Sheet - Rev 2.6

FEATURES

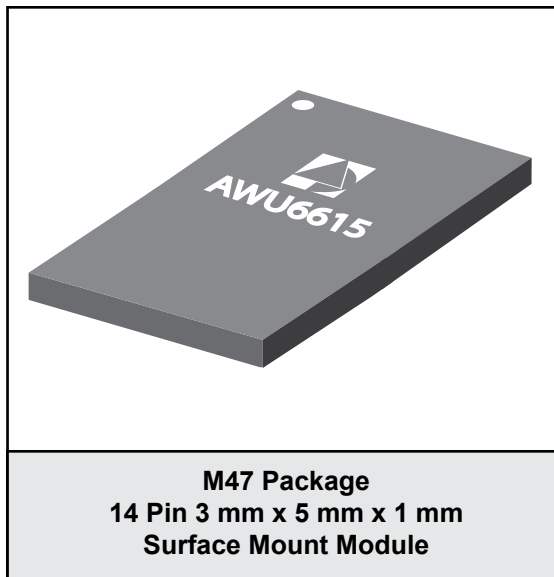
- InGaP HBT Technology
- High Efficiency:
 - 39 % @ $P_{OUT} = +28.7$ dBm
 - 22 % @ $P_{OUT} = +16.6$ dBm
 - 9 % @ $P_{OUT} = +8$ dBm
- Low Quiescent Current: 4.5 mA
- Internal Voltage Regulation
- Built-in Directional Coupler
- Common V_{MODE} Control Line
- Simplified V_{CC} Bus PCB routing
- Reduced External Component Count
- Low Profile Surface Mount Package: 1 mm
- RoHS Compliant Package, 260 °C MSL-3

APPLICATIONS

- Cell & PCS Dual-band Wireless Handsets and Data Devices for HSDPA/HSPA networks.

PRODUCT DESCRIPTION

AWU6615 addresses the demand for increased integration in dual-band handsets for WCDMA networks. The small footprint 3 mm x 5 mm x 1 mm surface-mount RoHS compliant package contains independent RF PA paths to ensure optimal performance in both frequency bands, while achieving a 25% PCB space savings compared with solutions requiring two single-band PAs. The package pinout was chosen to enable handset manufacturers to easily route bias to both power amplifiers and simplify control with common mode pins. The device is manufactured on an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. The AWU6615 is part of ANADIGICS' High-Efficiency-at-Low-Power (HELP™) family of WCDMA power amplifiers, which deliver low quiescent currents and significantly greater efficiency without the need of an external DC-DC converter. Through selectable bias modes, the AWU6615 achieves optimal efficiency, specifically at low- and mid-range power levels where the PA typically operates, thereby dramatically increasing handset talk-time. Its built-in voltage regulator eliminates the need for external



switches. This PA has built-in directional couplers for each band, with a common coupler output port CPL_OUT. These couplers provide high directivity and 23 dB Coupling. The 3 mm x 5 mm x 1 mm surface mount package incorporates matching networks optimized for output power, efficiency and linearity in a 50 Ω system.

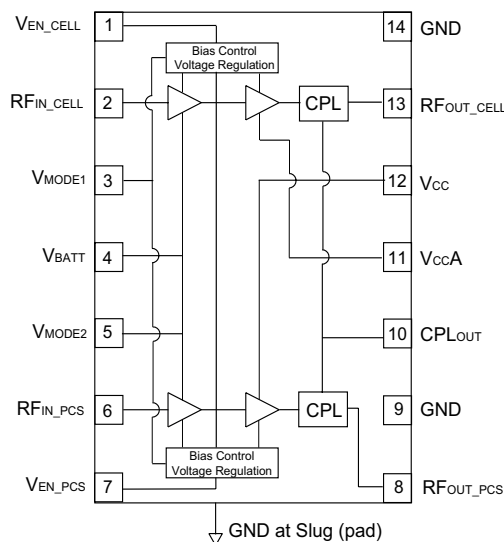


Figure 1: Block Diagram

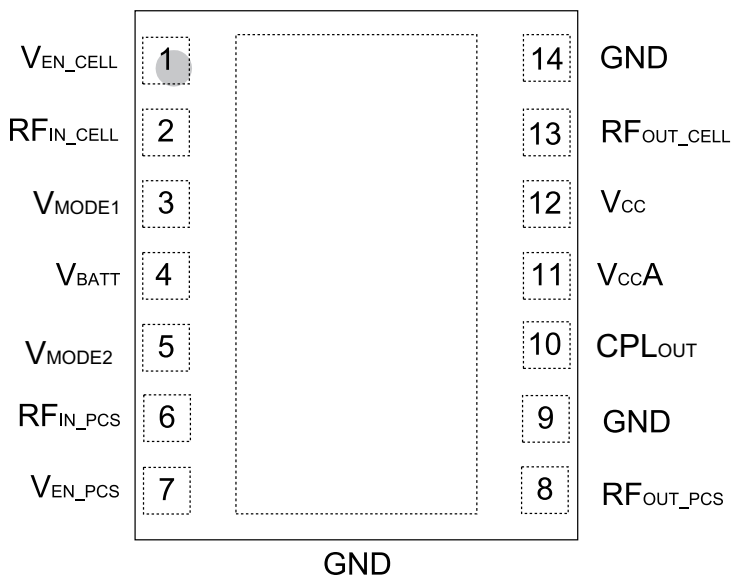


Figure 2: Pinout

Table 1: Pin Description

PIN	NAME	DESCRIPTION
1	V_{EN_CELL}	Enable Voltage for Cell Band
2	RF_{IN_CELL}	RF Input for Cell Band
3	V_{MODE1}	Mode Control Voltage for Cell and PCS Bands
4	V_{BATT}	Battery Voltage
5	V_{MODE2}	Mode Control Voltage for Cell
6	RF_{IN_PCS}	RF Input for PCS Band
7	V_{EN_PCS}	Enable Voltage for PCS Band
8	RF_{OUT_PCS}	RF Output for PCS Band
9	GND	Ground
10	CPL_{OUT}	Coupler Output Port
11	V_{CCA}	Battery Voltage A
12	V_{CC}	Supply Voltage
13	RF_{OUT_CELL}	RF Output for Cell Band
14	GND	Ground

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Supply Voltage (V_{BATT} , V_{CC} , V_{CCA})	0	+5	V
Mode Control Voltage (V_{MODE1} , V_{MODE2})	0	+3.5	V
Enable Voltage (V_{EN_CELL} , V_{EN_PCS})	0	+3.5	V
RF Input Power (PIN)	-	+10	dBm
Storage Temperature (T_{STG})	-40	+150	°C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Operating Frequency (f)	824 1850	-	849 1910	MHz	Cellular (Band 5) PCS (Band 2)
Supply Voltage (V_{CC} and V_{BATT})	+3.2	+3.4	+4.2	V	
Enable Voltage (V_{EN})	+1.35 0	+1.8 -	+3.1 +0.5	V	PA "on" PA "shut down"
Mode Control Voltage (V_{MODE0} , V_{MODE1})	+1.35 0	+1.8 -	+3.1 +0.5	V	Low Bias Mode High Bias Mode
RF Output Power, Band 5, UMTS R99 WCDMA, HPM HSPA (MPR = 0 dB), HPM R99 WCDMA, MPM HSPA (MPR = 0 dB), MPM R99 WCDMA, LPM HSPA (MPR = 0 dB), LPM	28.2 ⁽¹⁾ 27.2 ⁽¹⁾ 16.1 ⁽¹⁾ 15.1 ⁽¹⁾ 7.5 ⁽¹⁾ 6.5 ⁽¹⁾	28.7 27.7 16.6 15.6 8.0 7.0	- - - - - -	dBm	3GPP TS 34.121-1, REL8 Table C.11.1.3, Subtest 1
RF Output Power, Band 2, UMTS R99 WCDMA, HPM HSPA (MPR = 0 dB), HPM R99 WCDMA, MPM HSPA (MPR = 0 dB), MPM R99 WCDMA, LPM HSPA (MPR = 0 dB), LPM	28.3 ⁽¹⁾ 27.3 ⁽¹⁾ 16.5 ⁽¹⁾ 15.5 ⁽¹⁾ 8.0 ⁽¹⁾ 7.0 ⁽¹⁾	28.8 27.8 17.0 16.0 8.5 7.5	- - - - - -	dBm	3GPP TS 34.121-1, REL8 Table C.11.1.3, Subtest 1
Case Temperature (T_C)	-30	-	+90	°C	

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

(1) For operation at $V_{CC} = +3.2$ V, $T_C = +90$ °C P_{OUT} is derated by 0.5 dB.

Table 4: Electrical Specifications - Cellular Band (Band 5)
($T_C = +25\text{ }^\circ\text{C}$, $V_{BATT} = V_{CC} = +3.4\text{ V}$, $V_{EN_CELL} = +1.8\text{ V}$, $50\text{ }\Omega$ system, R99 uplink waveform)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS		
					P _{OUT}	V _{MODE1}	V _{MODE2}
Gain	25.5 14.5 9	27.5 17 11.5	30 20 14	dB	+28.7 dBm +16.6 dBm +8 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
ACLR1 @ 5 MHz Offset ⁽¹⁾	- - -	-42 -42 -41	-37 -37 -37	dBc	+28.7 dBm +16.6 dBm +8 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
ACLR2 @ 10 MHz Offset ⁽¹⁾	- - -	-58 -55 -54	-48 -48 -48	dBc	+28.7 dBm +16.6 dBm +8 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
Power-Added Efficiency ⁽¹⁾	35 18 6	39 22 9	- - -	%	+28.7 dBm +16.6 dBm +8 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
Quiescent Current (I _q)	-	4	6	mA	through V _{CC} pin, V _{MODE1} = +1.8 V, V _{MODE2} = +1.8 V		
Mode Control Current	-	0.1	0.15	mA	through V _{MODE} pins, V _{MODE1} = +1.8 V		
Battery Current	-	1.5	2.5	mA	through V _{BATT} pin, V _{MODE1} = +1.8 V, V _{MODE2} = +1.8 V		
Enable Current	-	0.15	0.25	mA	through V _{EN_CELL} pin, V _{MODE} = 1.8 V		
Noise in Receive Band	-	-133	-	dBm/Hz	869 MHz to 894 MHz		
Harmonics 2fo 3fo, 4fo	- - -	-40 -50	-30 -38	dBc	P _{OUT} < +28.7 dBm		
Input Impedance	-	-	2:1	VSWR			
Coupling Factor	-	23	-	dB			
Spurious Output Level (all spurious outputs)	-	-	-65	dBc	P _{OUT} < +28.7 dBm In-band Load VSWR < 5:1 Out-of-band Load VSWR < 10:1 Applies over all operating conditions		
Load mismatch stress with no permanent degradation or failure	8:1	-	-	VSWR	Applies over full operating conditions		

Notes:

(1) Efficiency and ACLR measured at 836.5 MHz.

Table 5: Electrical Specifications - PCS Band (Band 2)
(T_C = +25 °C, V_{BATT} = V_{CC} = +3.4 V, V_{EN_PCS} = +1.8 V, 50 Ω system, R99 uplink waveform)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS		
					P _{OUT}	V _{MODE1}	V _{MODE2}
Gain	25 12 8	27 15 11	30.5 18 13.5	dB	+28.8 dBm +17 dBm +8.5 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
ACLR1 @ 5 MHz Offset	- - -	-41 -40 -42	-37 -37 -37	dBc	+28.8 dBm +17 dBm +8.5 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
ACLR2 @ 5 MHz Offset	- - -	-52 -53 -56	-48 -48 -48	dBc	+28.8 dBm +17 dBm +8.5 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
Power-Added Efficiency ⁽¹⁾	35 18 6	39 21 8.5	- - -	%	+28.8 dBm +17 dBm +8.5 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V
Quiescent Current (I _q)	-	5	7	mA	through V _{CC} Pin, V _{MODE1} = +1.8 V, V _{MODE2} = +1.8 V		
Mode Control Current	-	0.1	0.15	mA	through V _{MODE} pins, V _{MODE1} = +1.8 V		
Battery Current	-	1.5	2.5	mA	through V _{BATT} pin, V _{MODE1} = +1.8 V, V _{MODE2} = +1.8 V		
Enable Current	-	0.15	0.25	mA	through V _{EN_CELL} pin, V _{MODE1} = V _{MODE2} = 1.8 V		
HBT Leakage Current (V _{CC})	-	<1	5	μA	V _{BATT} = +4.2 V, V _{CC} = +4.2 V, V _{EN_CELL} = 0 V, V _{MODE1} = 0 V, V _{MODE2} = 0 V		
Total Decoder Current on V _{BATT} (Shutdown Mode)	-	12	22	μA	V _{BATT} = +4.2 V, V _{CC} = +4.2 V, V _{EN} = 0 V, V _{MODE1} = V _{MODE2} = 0 V		
Noise in Receive Band	-	-134	-	dBm/Hz	1930 MHz to 1990 MHz		
Harmonics 2fo 3fo, 4fo	- - -	-42 -50	-30 -38	dBc	P _{OUT} < +28.8 dBm		
Input Impedance	-	-	2:1	VSWR			
Coupling Factor	-	22.5	-	dB			
Spurious Output Level (all spurious outputs)	-	-	-65	dBc	P _{OUT} < +28.8 dBm In-band Load VSWR < 5:1 Out-of-band Load VSWR < 10:1 Applies over all operating conditions		
Load mismatch stress with no permanent degradation or failure	8:1	-	-	VSWR	Applies over full operating conditions		

Notes:

1. Efficiency and ACLR measured at 1880 MHz.

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: <http://www.anadigics.com>

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the V_{ENABLE} voltage.

Bias Modes, Medium Bias Mode

The power amplifier may be placed in Low Bias mode or a High Bias mode by applying the appropriate logic level (see Operating Ranges table) to the V_{MODE1} and V_{MODE2} pins. The Bias Control table lists the recommended modes of operation for various applications.

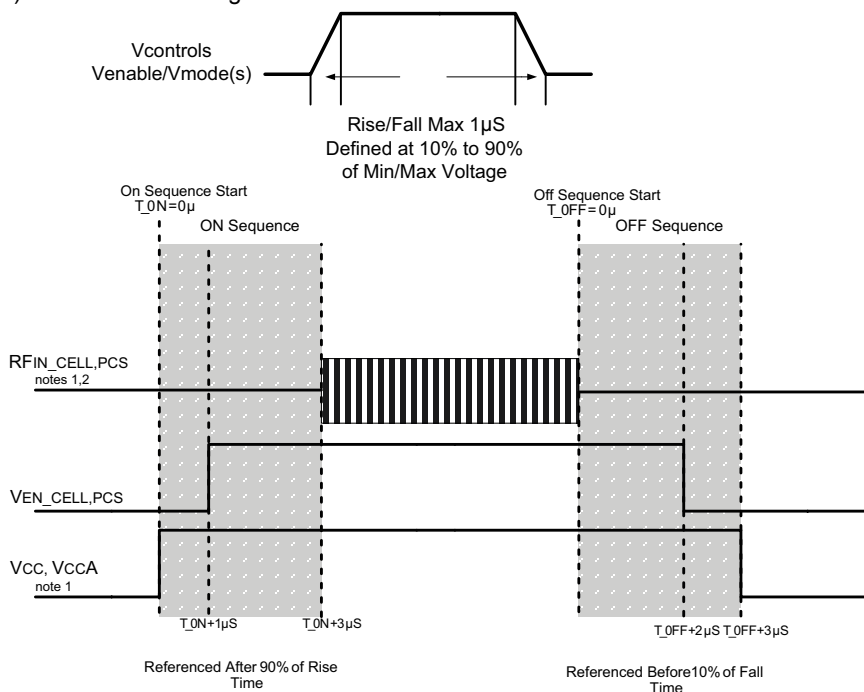


Figure 3: Minimum Recommended ON/OFF Timing Sequence

Notes:

- (1) Level might be changed after RF is ON.
- (2) RF OFF defined as $P_{IN} \leq -30$ dBm.
- (3) Switching simultaneously between V_{MODE} and V_{EN} is not recommended.

Table 6: Bias Control

APPLICATION	P_{OUT} LEVELS	BIAS MODE	V_{EN}	V_{MODE1}	V_{MODE2}	V_{CC}	V_{BATT}
WCDMA - low power (Low Bias Mode)	< +8 dBm	Low	+1.8 V	+1.8	+1.8 V	3.2 - 4.2 V	> 3.2 V
WCDMA - med power (Medium Bias Mode)	> +8 dBm < +16.5 dBm	Low	+1.8 V	+1.8 V	0 V	3.2 - 4.2 V	> 3.2 V
WCDMA - high power (High Bias Mode)	> +16.5 dBm	High	+1.8 V	0 V	0 V	3.2 - 4.2 V	> 3.2 V
Shutdown	-	Shutdown	0 V	-	-	3.2 - 4.2 V	> 3.2 V

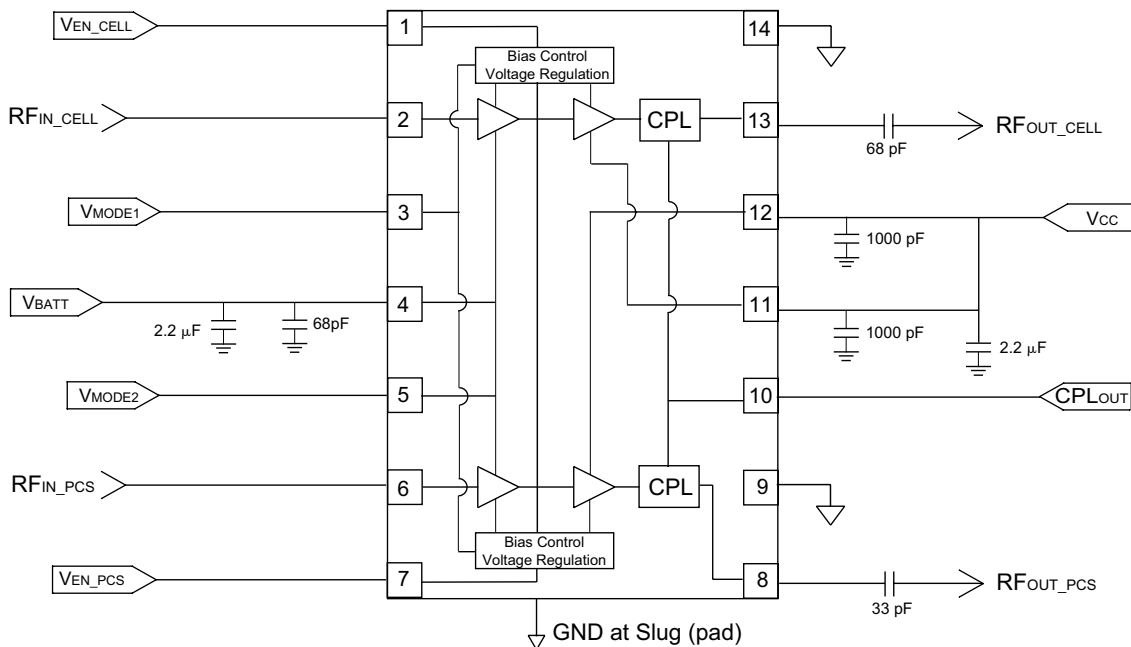
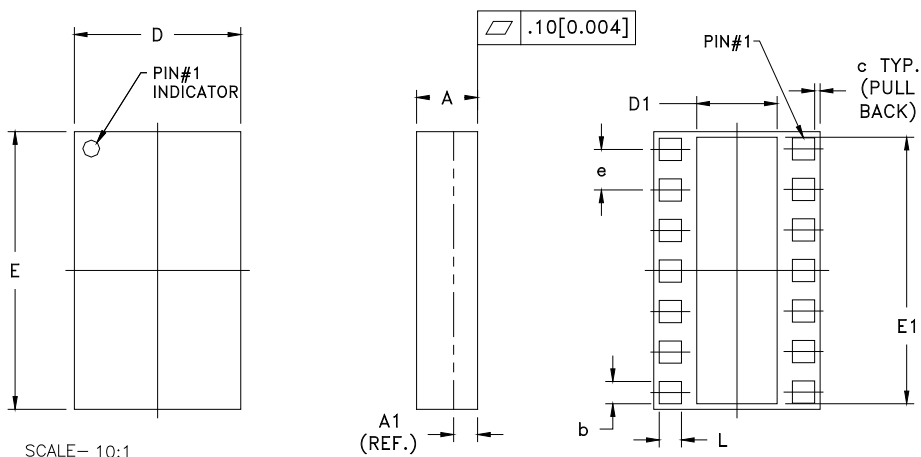


Figure 4: Application Circuit

PACKAGE OUTLINE



SYMBOL	MILLIMETERS			INCHES			NOTE
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
A	0.91	1.03	1.13	0.035	0.041	0.044	—
A1	PLEASE REFER TO LAMINATE CONTROL DRAWING						—
b	0.32	0.37	0.41	0.013	0.015	0.016	3
c	—	0.10	—	—	0.004	—	—
D	2.88	3.00	3.12	0.113	0.118	0.123	—
D1	1.45	1.50	1.57	0.057	0.059	0.062	3
E	4.88	5.00	5.12	0.192	0.197	0.202	—
E1	4.70	4.75	4.80	0.185	0.187	0.189	3
e	—	0.73	—	—	0.029	—	4
L	0.32	0.37	0.41	0.013	0.015	0.016	3

NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
3. PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.
4. PITCH MEASUREMENT (e) TAKEN CENTERLINE TO CENTERLINE OF SOLDER MASK OPENINGS.
5. UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN.

Figure 5: Package Outline - 14 Pin 3 mm x 5 mm x 1 mm Surface Mount Module

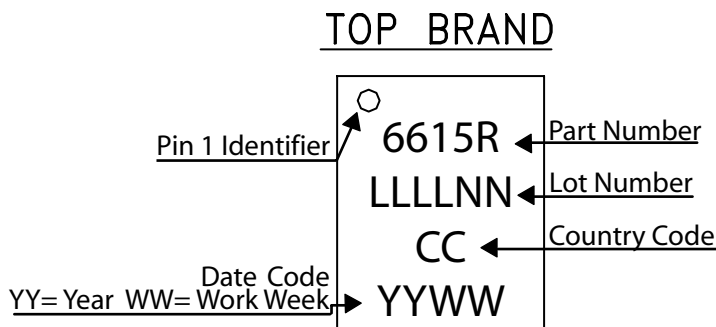


Figure 6: Branding Specification

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWU6615RM47Q7	-30 °C to +90 °C	RoHS Compliant 14 Pin 3 mm x 5 mm x 1 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel
AWU6615RM47P9	-30 °C to +90 °C	RoHS Compliant 14 Pin 3 mm x 5 mm x 1 mm Surface Mount Module	Partial Tape and Reel

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