

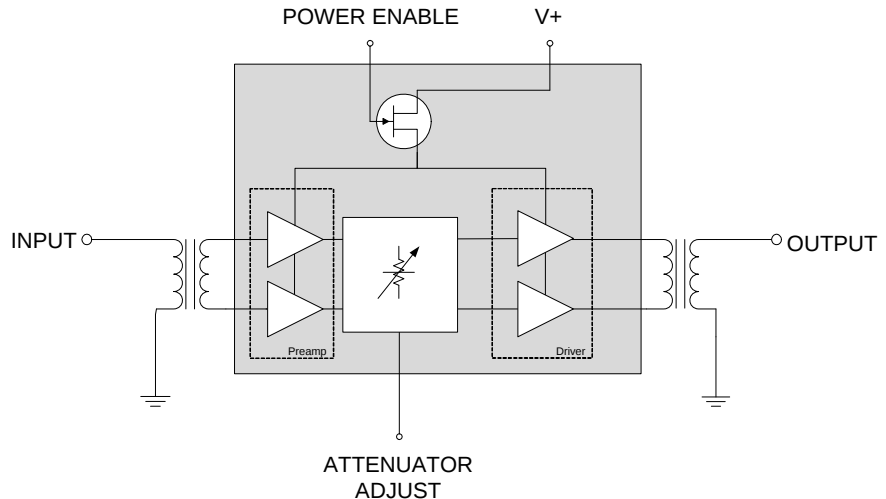


Features

- Excellent Linearity
- Extremely High Output Capability
- Voltage Controlled Attenuator
- Power Enable Feature
- Optimal Reliability
- Low Noise
- Unconditionally Stable Under all Terminations
- 27dB Typical Gain at 1003MHz
- 410mA Typical at 12V_{DC}

Applications

- 45MHz to 1003MHz Downstream Edge QAM RF Modulators
- Headend Equipment



Functional Block Diagram

Product Description

The RFAM2790 is an integrated edge QAM amplifier module. The part employs GaAs pHEMT die, GaAs MESFET die, a 20dB range variable attenuator and a power enable feature, has high output capability, and is operated from 45MHz to 1003MHz. It provides excellent linearity and superior return loss performance with low noise and optimal reliability.

Ordering Information

RFAM2790SB	Sample bag with 5 pieces
RFAM2790SR	7" Reel with 100 pieces
RFAM2790TR7	7" Reel with 250 pieces
RFAM2790TR13	13" Reel with 750 pieces
RFAM2790PCBA-410	Fully Assembled Evaluation Board

Optimum Technology Matching® Applied

☐ GaAs HBT	☐ SiGe BiCMOS	☒ GaAs pHEMT	☐ GaN HEMT
☒ GaAs MESFET	☐ Si BiCMOS	☐ Si CMOS	☐ BiFET HBT
☐ InGaP HBT	☐ SiGe HBT	☐ Si BJT	☐ SOI

Absolute Maximum Ratings

Parameter	Rating	Unit
V+ DC Supply Over-Voltage (5 minutes)	14	V
Storage Temperature	-40 to +100	°C
Operating Mounting Base Temperature	-30 to +100	°C
Power Enable Voltage	10	V



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

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RoHS (Restriction of Hazardous Substances): Compliant per EU Directive 2002/95/EC.

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall					V+ = 12V; T _{MB} = 30 °C; Z _S = Z _L = 75Ω; Attenuation = 0dB
Power Gain		27.0		dB	f = 45MHz
	26.0	27.0	28.0	dB	f = 1003MHz
Slope[1]	-0.5	0.0	1.0	dB	f = 45MHz to 1003MHz
Flatness of Frequency Response		0.5	1.0	dB	f = 45MHz to 1003MHz (Peak to Valley)
Input Return Loss	18	20		dB	f = 45MHz to 1003MHz
Output Return Loss	16	18		dB	
Noise Figure		4.0	5.0	dB	f = 50MHz to 1003MHz
Total Current Consumption (DC)		410.0	450.0	mA	
Distortion					V+ = 12V; T _{MB} = 30 °C; Z _S = Z _L = 75Ω; Attenuation = 0dB
Adjacent Channel Power Ratio (ACPR); N = 4 contiguous 256QAM channels			-58	dBc	Channel Power = 58dBmV; Adjacent channel up to 750kHz from channel block edge
			-60	dBc	Channel Power = 58dBmV; Adjacent channel (750kHz from channel block edge to 6MHz from channel block edge)
			-63	dBc	Channel Power = 58dBmV; Next-adjacent channel (6MHz from channel block edge to 12MHz from channel block edge)
			-65	dBc	Channel Power = 58dBmV; Third-adjacent channel (12MHz from channel block edge to 18MHz from channel block edge)
2nd Order Harmonic (HD2); N = 1 256QAM channel			-63	dBc	Channel Power = 66dBmV; In each of 2N contiguous 6MHz channels coinciding with 2nd harmonic components (up to 1000MHz);
3rd Order Harmonic (HD3); N = 1 256QAM channel			-63	dBc	Channel Power = 66dBmV; In each of 3N contiguous 6MHz channels coinciding with 3rd harmonic components (up to 1000MHz);
CTB		-67		dBc	V _O = 46dBmV, flat, 79 analog channels plus 75 digital channels (-6dB offset)[2]
XMOD		-60		dBc	
CSO		-70		dBc	
CIN		64		dB	

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Attenuator					$V_+ = 12V$; $T_{MB} = 30^\circ C$; $Z_S = Z_L = 75\Omega$
Attenuator Range	0 to 20			dB	Attenuator Voltage 0V to 12V
Power Enable/Disable					
		Amp enabled			Logic high (3.3V) applied to power enable pin[3]
		Amp disabled			Logic low (0V) applied to power enable pin[4]

[1] The slope is defined as the difference between the gain at the start frequency and the gain at the stop frequency.

[2] 79 analog channels, NTSC frequency raster: 55.25MHz to 547.25MHz, +46dBmV flat output level, plus 75 digital channels, -6dB offset relative to the equivalent analog carrier.

Composite second order (CSO) - The CSO parameter (both sum and difference products) is defined by the NCTA.

Composite triple beat (CTB) - The CTB parameter is defined by the NCTA.

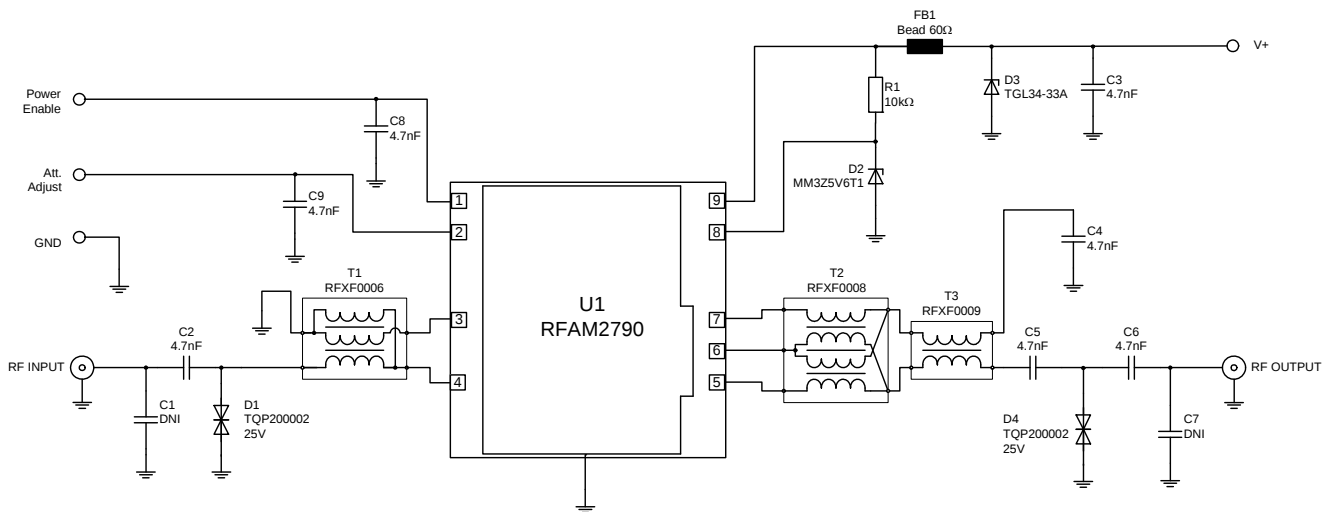
Cross modulation (XMOD) - Cross modulation (XMOD) is measured at baseband (selective voltmeter method), referenced to 100% modulation of the carrier being tested.

carrier to intermodulation noise (CIN) - The CIN parameter is defined by ANSI/SCTE 17 (Test procedure for carrier to noise).

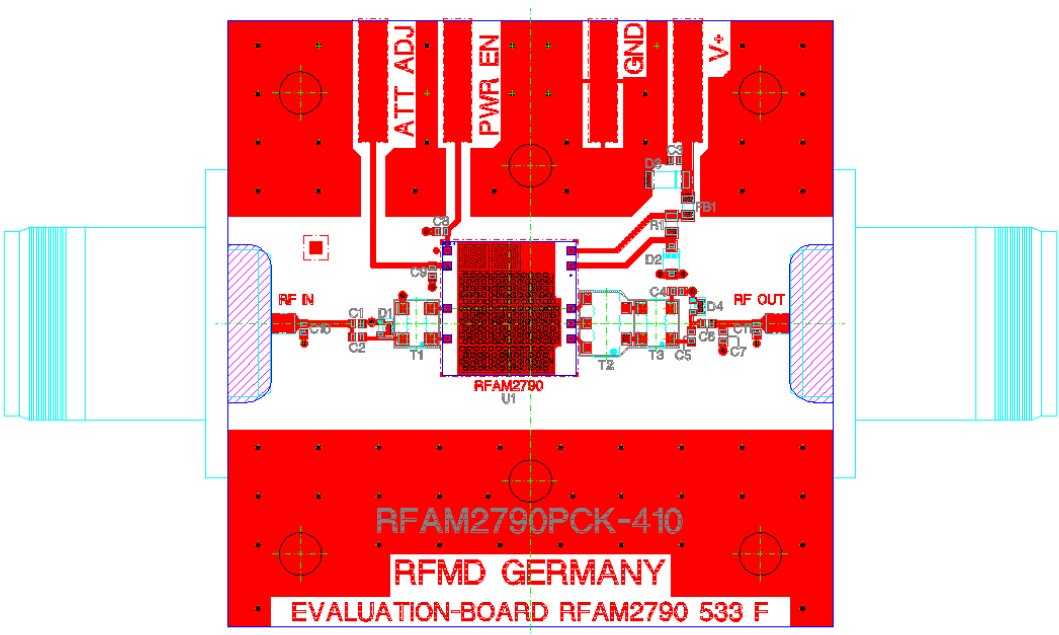
[3] Logic high is defined as power enable voltage $> 2V$

[4] Logic low is defined as power enable voltage $< 0.4V$

Application Circuit



Evaluation Board Layout



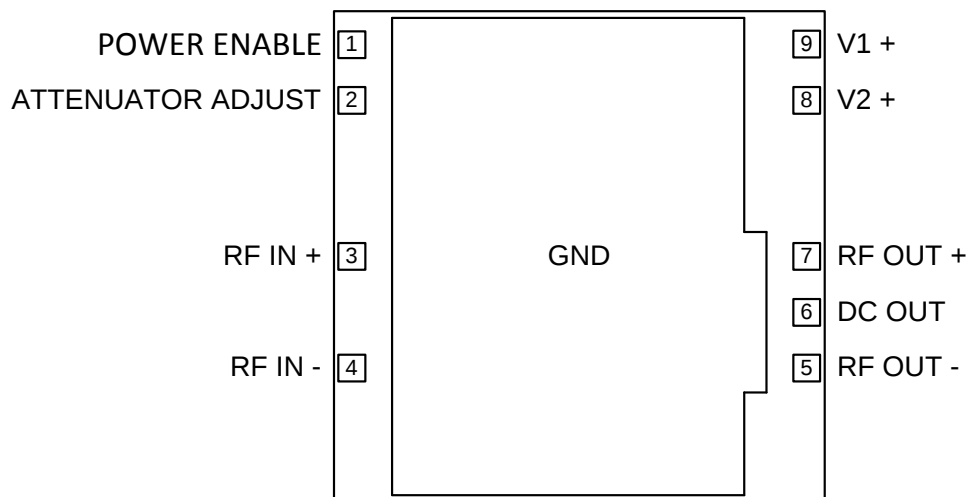
Component Chart

Component Type	Value	Qty	Designator	Comments
Evaluation Board	RFAM2790PCBA-410	1		
Capacitor	DNI	1	C1	optional, to improve matching in application
Capacitor	4.7nF	7	C2, C3, C4, C5, C6, C8, C9	
Capacitor	DNI	1	C7	optional, to improve matching in application
Resistor	10kΩ	1	R1	
Chip Bead	60Ω at 100MHz	1	FB1	
ESD Protection Diode	TQP200002	2	D1, D4	
Zener Voltage Diode	MM3Z5V6T1G	1	D2	
Transient Voltage Suppressor Diode	TGL34-33A	1	D3	
Transformer	RFXF0006	1	T1	
Transformer	RFXF0008	1	T2	
Transformer	RFXF0009	1	T3	
DUT	RFAM2790	1	U1	

Pin Names and Descriptions

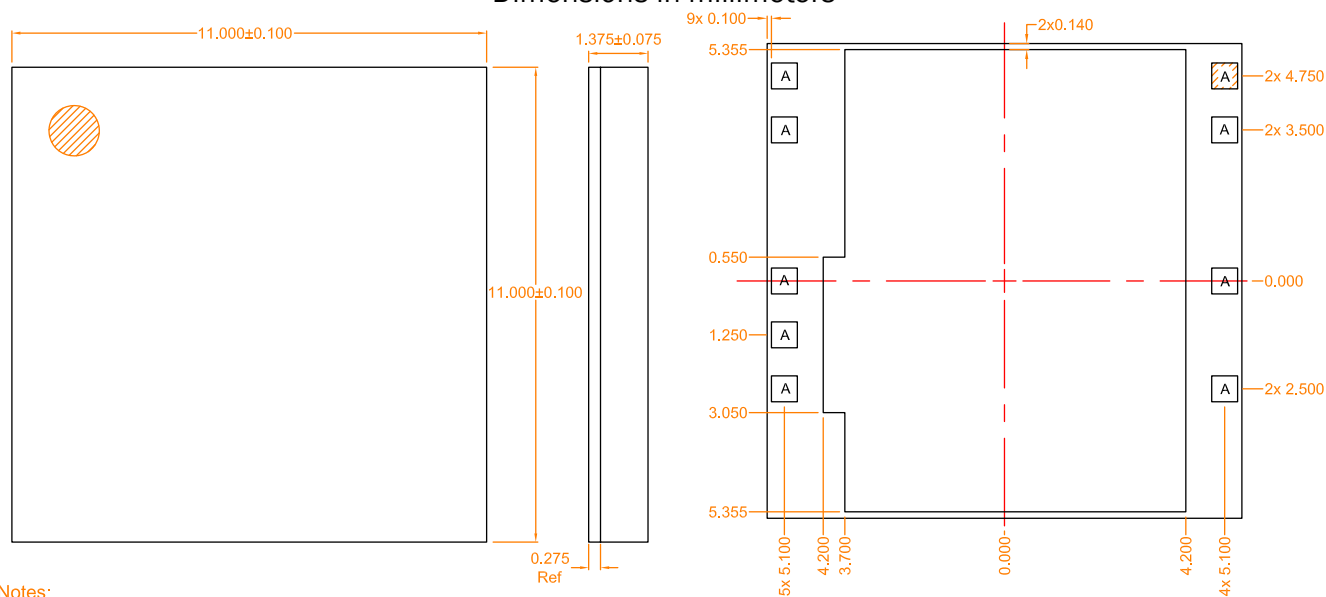
Pin	Name	Description
1	POWER ENABLE	Logic Level (3.3V) Power Enable Control
2	ATTENUATOR ADJUST	Voltage Adjustable Attenuator
3	RF IN +	RF AMP Positive Input
4	RF IN -	RF AMP Negative Input
5	RF OUT -	RF AMP Negative Output
6	DC OUT	12V Output
7	RF OUT +	RF AMP Positive Output
8	V2+	Supply Voltage 5.6V
9	V1+	Supply Voltage 12V

Pin Configuration



Package Drawing

Dimensions in millimeters

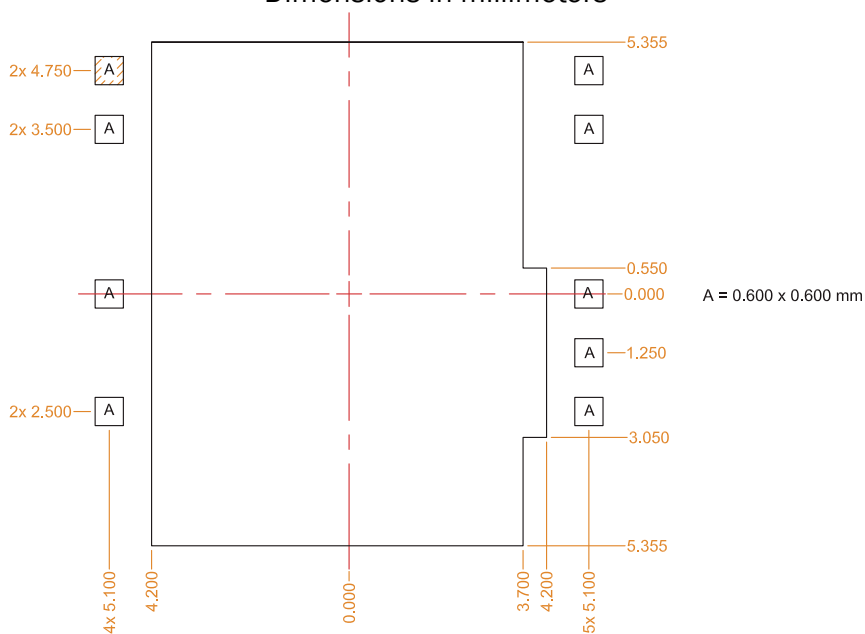


Notes:

1. Shaded area represents Pin 1 location

PCB Metal Land Pattern

Dimensions in millimeters



PCB METAL LAND PATTERN

Shaded area represents PIN 1.