

DATA SHEET

SKYA21001: 20 MHz to 3.0 GHz SPDT Switch

Automotive Applications

- Infotainment
- Automated toll systems
- Garage door opener
- 802.11 b/g/n WLAN, Bluetooth systems
- Wireless control systems
- Outdoor lighting control
- Remote keyless entry
- Telematics
- GPS/Navigation

Features

- IP1dB = +30 dBm typical @ 3 V
- IP3 = +43 dBm typical @ 3 V
- Low insertion loss: 0.3 dB @ 0.9 GHz
- Low DC power consumption
- Ultraminiature, SC-70 (6-pin, 2.00 x 1.25 mm) package
- Designed and manufactured in an ISO/TS16949-certified facility
- AEC-Q100 qualification in progress
- Compliant to AEC-Q100 at room temperature without over-temperature read points
- JEDEC (JESD22) qualified at 25 °C
- Lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

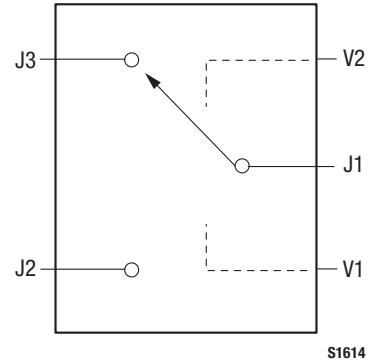


Figure 1. SKYA21001 Block Diagram

Description

The SKYA21001 is a Single-Pole, Double-Throw (SPDT) switch. The device features low insertion loss and positive voltage operation with very low DC power consumption. The SKYA21001 is manufactured in a compact 2.00 x 1.25 mm, 6-pin SC-70 package.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

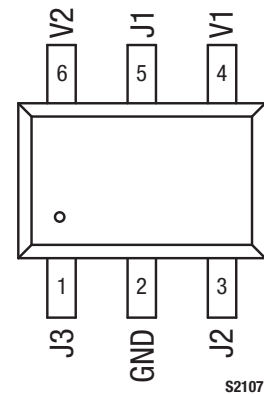


Figure 2. SKYA21001 Pinout –6-Pin SC-70 (Top View)

Table 1. SKYA21001 Signal Descriptions

Pin #	Name	Description	Pin #	Name	Description
1	J3	RF output (Note 1)	4	V1	DC control voltage
2	GND	Ground	5	J1	RF output (Note 1)
3	J2	RF output (Note 1)	6	V2	DC control voltage

Note 1: A 100 pF blocking capacitor is required for >500 MHz operation. Use larger value capacitors for lower frequency operation.

Table 2. SKYA21001 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Control voltage	V _{CTL}	−0.2	+8.0	V
RF input power (V _{CTL} = 0–7 V): >500 MHz <500 MHz			6 500	W mW
Operating temperature	T _{OP}	−40	+85	°C
Storage temperature	T _{STG}	−65	+150	°C

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SKYA21001 Electrical Specifications (1 of 2) (Note 1)

(V_{CTL} = 0–3 V, T_{OP} = +25 °C, Characteristic Impedance = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Insertion loss (Note 2, Note 3)		0.02 to 1.0 GHz 1.0 to 2.0 GHz 2.0 to 3.0 GHz		0.3 0.4 0.4	0.4 0.5 0.6	dB dB dB
Isolation (Note 3)		0.02 to 1.0 GHz 1.0 to 2.0 GHz 2.0 to 3.0 GHz	22 22 20	25 25 23		dB dB dB
Voltage standing wave ratio (Note 3, Note 4)	VSWR	0.02 to 1.0 GHz 1.0 to 2.0 GHz 2.0 to 3.0 GHz		1.2:1 1.2:1 1.3:1	1.4:1 1.4:1 1.45:1	— — —
Switching characteristics: Rise/fall On/off Video feedthrough		10/90% or 90/10% RF 50% control to 90/10% RF T _{RISE} = 1 ns, bandwidth = 500 MHz		10 100 25		ns ns mV
1 dB input compression point	IP1dB	@ 0.5 to 3.0 GHz V _{CTL} = 0 to 2 V V _{CTL} = 0 to 3 V V _{CTL} = 0 to 5 V @ 48 MHz V _{CTL} = 0 to 3 V V _{CTL} = 0 to 5 V		+26 +30 +34 +28.9 +29.5		dBm dBm dBm dBm dBm

Table 3. SKYA21001 Electrical Specifications (2 of 2) (Note 1)**($V_{CTL} = 0-3\text{ V}$, $T_{OP} = +25\text{ }^{\circ}\text{C}$, Characteristic Impedance = $50\text{ }\Omega$, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
3 rd order intercept point	IP3	+5 dBm two-tone input power @ 0.5 to 3.0 GHz $V_{CTL} = 0\text{ to }2\text{ V}$ $V_{CTL} = 0\text{ to }3\text{ V}$ $V_{CTL} = 0\text{ to }5\text{ V}$		+43 +43 +50		dBm dBm dBm
Control voltage: Low (@ 20 μA max)	V_{CTL_L}		0		0.2	V
High (@100 μA max)	V_{CTL_H}				2.0	V
High (@ 200 μA max)	V_{CTL_H}				5.0	V

Note 1: Performance is guaranteed only under the conditions listed in this Table.**Note 2:** Insertion loss changes by 0.003 dB/ $^{\circ}\text{C}$.**Note 3:** Insertion loss state.

Typical Performance Characteristics

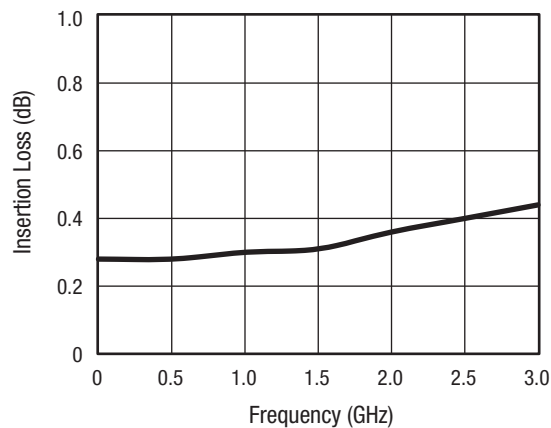
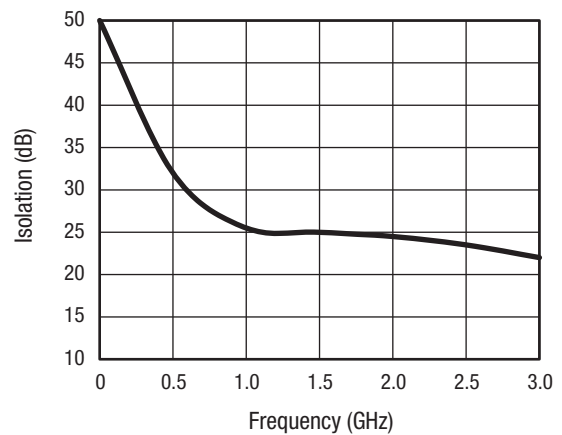
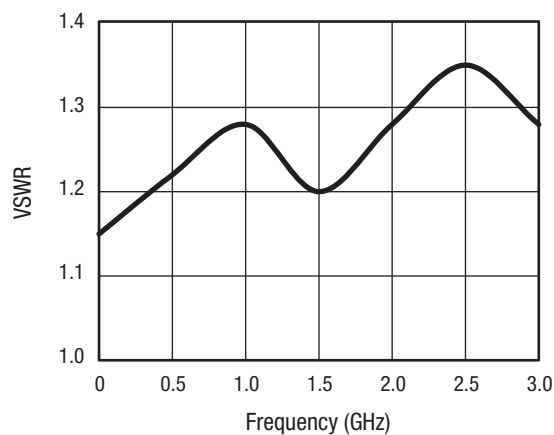
($V_{CTL} = 0-3\text{ V}$, $T_{OP} = +25\text{ }^{\circ}\text{C}$, $P_{IN} = 0\text{ dBm}$, Characteristic Impedance [Z_0] = $50\text{ }\Omega$, $C_{BL} = 100\text{ pF}$, Unless Otherwise Noted)**Figure 2. Insertion Loss vs Frequency****Figure 3. Isolation vs Frequency****Figure 4. VSWR vs Frequency**

Table 4. Truth Table ($V_{HIGH} = 2$ to 5 V)

V1	V2	J1-J2	J1-J3
V_{HIGH}	0	Isolation	Insertion loss
0	V_{HIGH}	Insertion loss	Isolation

Note: Any state other than described in this Table places the device in an undefined state. An undefined state does not damage the device.

Evaluation Board Description

The SKYA21001 Evaluation Board is used to test the performance of the SKYA21001 SPDT switch. An Evaluation Board schematic diagram is provided in Figure 5. An assembly drawing for the Evaluation Board is shown in Figure 6.

Package Dimensions

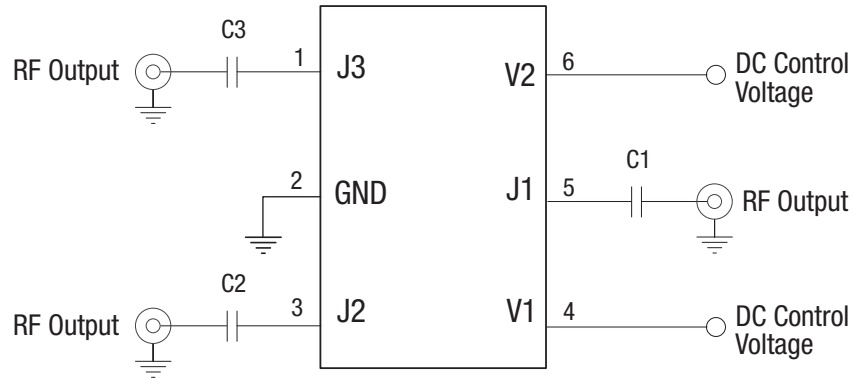
Package dimensions for the 6-pin SC-70 are shown in Figure 7, and tape and reel dimensions are provided in Figure 8.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKYA21001 is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

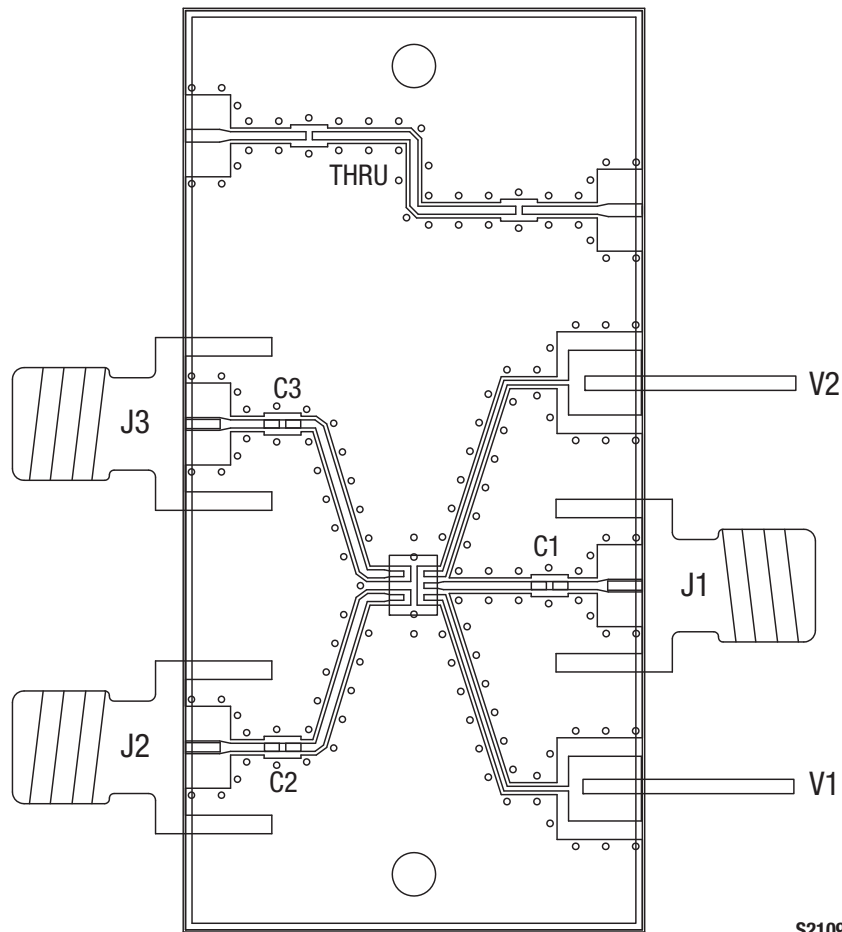


Note: Use 100 pF blocking capacitors (C1, C2, C3) for >500 MHz operation. Higher values recommended for lower frequency operation. Exposed paddle must be grounded.

Use 10 nF blocking capacitors (C1, C2, C3) for <50 MHz operation.

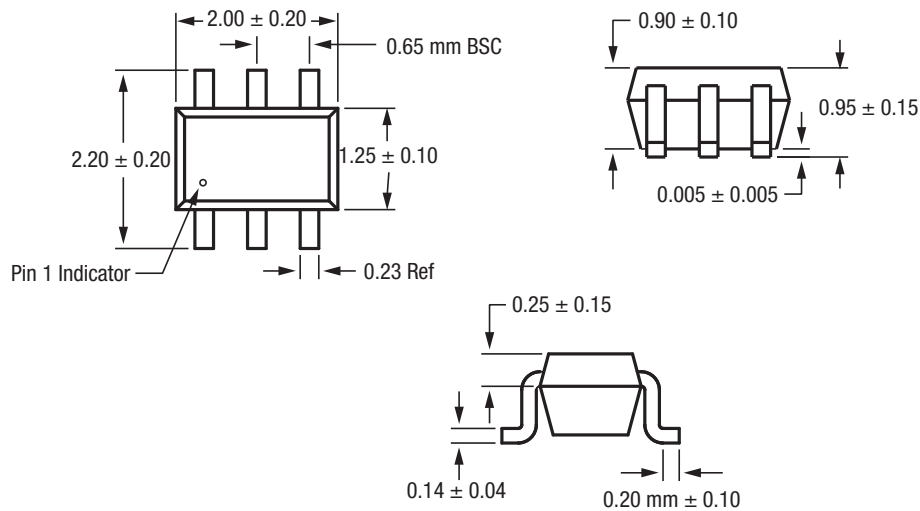
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Figure 5. SKYA21001 Evaluation Board Schematic



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Figure 6. SKYA21001 Evaluation Board Assembly Diagram

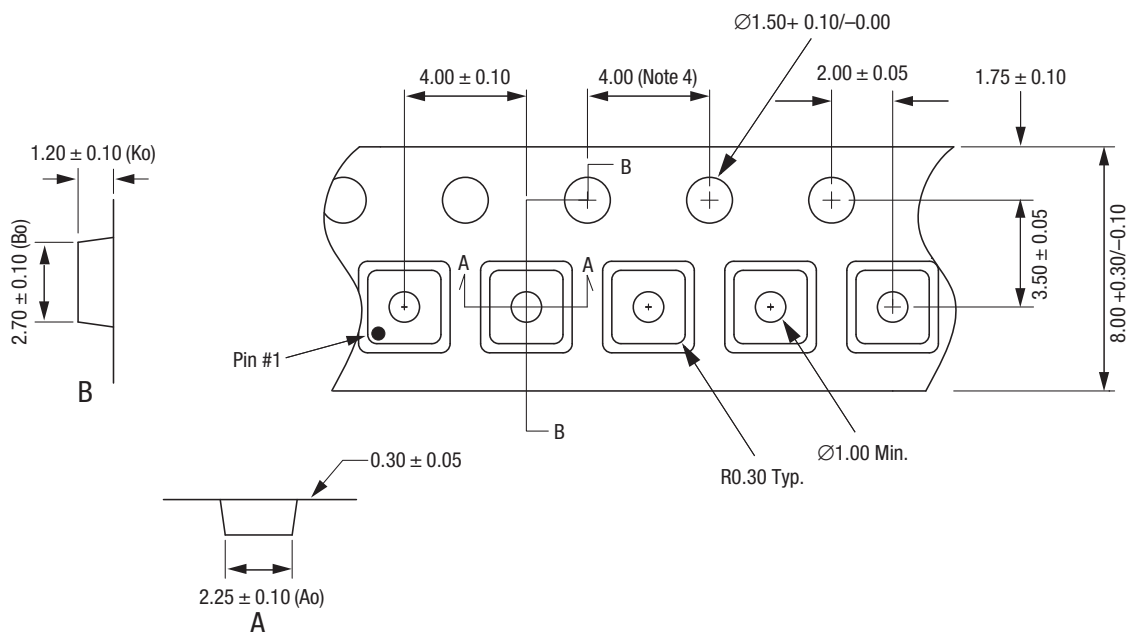


All measurements are in millimeters

Dimensioning and tolerancing according to ASME Y14.5M-1994

S1479

Figure 7. SKYA21001 6-Pin SC-70 Package Dimensions



Notes:

1. Carrier tape: black conductive polystyrene.
2. Cover tape material: transparent conductive HSA.
3. Cover tape size: 5.40 mm width.
4. Ten sprocket hole pitch cumulative tolerance ± 0.20 mm.
5. All measurements are in millimeters.

S1581

Figure 8. SKYA21001 Tape and Reel Dimensions

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKYA21001 SPDT Switch	SKYA21001	SKYA21001-EVB

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