



Description:	8-Way Power Divider
Operating Frequency:	4 – 8 GHz
Insertion Loss:	0.8 dB Max
Isolation:	22 dB Min
VSWR:	Σ Port 1.4:1 Max
	Outputs 1.3:1 Max
Amplitude Balance:	± 0.2 dB Max
Phase Balance:	$\pm 3^\circ$ Max
Power Handling (@ SUM Port):	Load VSWR= ∞ 1W AVG/CW Max
	Load VSWR <2:1 10W AVG/CW Max
Connectors:	SMA (f)
Impedance:	50 Ohms Nominal
Quality:	Best-Commercial-Grade

Temperature:	{Operating: -55°C to +95°C} & {Storage: -60°C to +110°C}
Humidity:	MIL-STD-202F, Method 103B, Cond. B (96 hours at 95% R.H.)
Shock:	MIL-STD-202F, Method 213B, Cond. B (75G, 6mSec)
Vibration:	MIL-STD-202F, Method 204D, Cond. B (.06" double amplitude, or 15G)
Altitude:	MIL-STD-202F, Method 105C, Cond. B (50,000 Feet)
Temp. Shock:	MIL-STD-202F, Method 107D, Cond. A (5 cycles)

Technical drawing of the UMCC MARKING SURFACE. The drawing shows a rectangular board with dimensions 3.000" [76.2mm] by 4.000" [101.6mm]. The board is labeled "UMCC MARKING SURFACE".

Dimensions and locations:

- Overall width: 3.000" [76.2mm]
- Overall height: 4.000" [101.6mm]
- Top edge dimensions: 2.450" [62.2mm] and 0.550" [14.0mm]
- Left edge dimensions: 2.000" [50.8mm] and 0.125" [3.2mm]
- Right edge dimensions: 0.380" [9.7mm] and 0.190" [4.8mm]
- Bottom edge dimensions: 0.125" [3.2mm] DIA THRU, 4 PLACES
- Component locations: J1, J2, J3, J4, J5, J6, J7, J8 (vertical row on the right), SMA(F) 9 PLACES (bottom left), and Σ (bottom center).
- Vertical spacing between components: 0.250" [6.35mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm], 0.500" [12.7mm]
- Horizontal spacing between components: 3.750" [95.3mm]

6.65 oz [188.5 g]