

Helping Customers Innovate, Improve & Grow



PX-700

Features

- 5X7 Surface Mount Package
- Reflow Process Compatible
- AC MOS, TTL, LVPECL and LVDS
- MIL-PRF-55310 Class B Screening (optional)
- Frequency Range: 1 MHz to 800 MHz
- Previous Model: C1250
- EAR99

Applications

- Military Systems
- Avionics and Instrumentation
- Test Equipment
- Medical Equipment

Performance Specifications

Parameter	Min	Typ	Max	Units	Condition
Frequency Stabilities¹					
vs. operating temperature range (Referenced to +25°C)	-15		+15	ppm	0... +70°C
	-25		+25	ppm	0... +70°C
	-50		+50	ppm	0... +70°C
	-100		+100	ppm	0... +70°C
	-50		+50	ppm	-40...+85°C
	-65		+65	ppm	-40...+85°C
	-100		+100	ppm	-40...+85°C
	-65		+65	ppm	-55...+125°C
	-100		+100	ppm	-55...+125°C
Initial tolerance	-15		+15	ppm	@+25°C
	-25		+25	ppm	@+25°C
	-50		+50	ppm	@+25°C
	-100		+100	ppm	@+25°C
Overall tolerance (Referenced to +25°C)	-25		+25	ppm	0... +70°C
	-50		+50	ppm	0... +70°C
	-100		+100	ppm	0... +70°C
	-50		+50	ppm	-40...+85°C
	-100		+100	ppm	-40...+85°C
	-80		+80	ppm	-55...+125°C
	-100		+100	ppm	-55...+125°C
vs. supply voltage change	-2		+2	ppm	VS ± 5%
vs. load change	-1		+1	ppm	Load ± 5%
vs. aging / 1 year	-3		+3	ppm	after 30 days of operation
vs. aging / year (following years)	-1		+1	ppm	

Performance Specifications

Supply Voltage (Vs)					
Supply voltage	4.75	5.0	5.25	VDC	
Supply voltage	3.135	3.3	3.465	VDC	
Supply voltage	2.375	2.5	2.625	VDC	
Supply voltage	1.710	1.8	1.890	VDC	
Current consumption (+5 VDC)			15 20 40	mA mA mA	ACMOS or TTL 1.0 to 23.9 MHz ACMOS or TTL 24 to 49.9 MHz ACMOS or TTL 50 to 125.00 MHz
Current consumption (+3.3 VDC or +2.5 VDC)			6 8 16 20 40 75 100	mA mA mA mA mA mA mA	ACMOS 1.0 to 14.9 MHz ACMOS 15.0 to 39.9 MHz ACMOS 40.0 to 59.9 MHz ACMOS 60.0 to 84.9 MHz ACMOS 85.0 to 125.0 MHz LVPECL or LVDS No load <200 MHz LVPECL or LVDS No load >200 MHz
Current consumption (+1.8 VDC)			1.5 2.0 8.0	mA mA mA	ACMOS 1.0 to 14.9 MHz ACMOS 15.0 to 49.9 MHz ACMOS 50.0 to 100 MHz

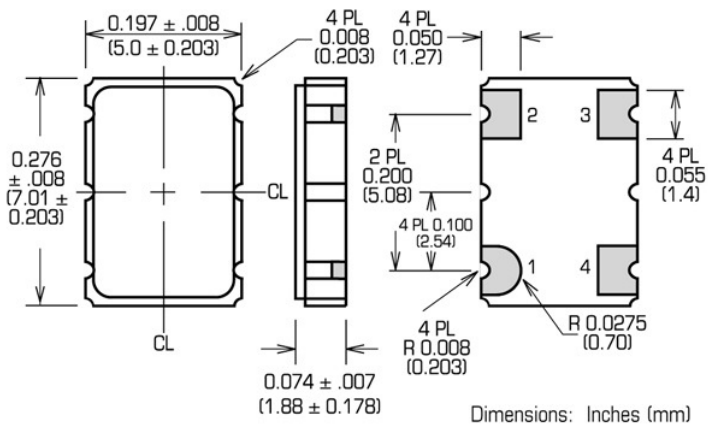
RF Output					
Signal	ACMOS				
Load		15	50	pF	
Signal Level (Vol)			0.50 0.30 0.25 0.20	VDC VDC VDC VDC	with Vs=5.0V and 15pF load with Vs=3.3V and 15pF load with Vs= 2.5V and 15pF load with Vs= 1.8V and 15pF load
Signal Level (Voh)	4.50 3.00 2.25 1.62			VDC VDC VDC VDC	with Vs=5.0V and 15pF load with Vs=3.3V and 15pF load with Vs=2.5V and 15pF load with Vs= 1.8V and 15pF load
Rise and fall times for ACMOS (measured 10% to 90%)			10 6 3	ns ns ns	1.0 to 23.9 MHz 24.0 to 79.9 MHz 80.0 to 125.0 MHz
Duty cycle	45 40		55 60	% %	@ 50% Vs< 15 MHz @ 50% Vs=> 15 MHz
Signal	TTL				
Load			10	pF	
Signal Level (Vol)			0.5	V	
Signal Level (Voh)	+2.4			V	
Rise and fall times (From .8V to 2V)			15 5	ns ns	<= 20MHz > 20 MHz
Duty cycle	45 40		55 60	% %	@ +1.4V, < 4 MHz @ +1.4V, >= 4 MHz
Signal	LVPECL				
Load			50	ohm	into Vcc-2V or Thevenin Equivalent
Signal Level (Vol)			Vs -1.62	VDC	-40.....+85°C operating temp
Signal Level (Voh)	Vs- 1.025			VDC	-40.....+85°C operating temp
Rise and fall times			1000 600	ps ps	< 100 MHz >= 100 MHz
Duty Cycle	45		55	%	@ 50% Vdd
Jitter (rms)			10 0.5	ps ps	BW = 10Hz to 20 MHz BW = 12KHz to 20 MHz
Period Jitter (pk-pk)			40	ps	10,000 Samples - Rising edge

Parameter	Min	Typ	Max	Units	Condition
Signal	LVDS				
Load	60	100	140	ohm	Between outputs
Signal Level (Vol)		1.2		VDC	
Signal Level (Voh)		1.4		VDC	
Differential Voltage (Vod)	240	330	460	mVpeak	
Common Mode (Offset) Voltage (Vos)	1.125	1.2	1.375	V	
Start-up Time			10	ms	
Rise \ Fall Time		600	1000	ps	measured @ 20% to 80% of Vod
Duty cycle	45		55	%	@ 50% of Vod
Jitter (rms)			5 1	ps ps	BW = 10Hz to 20 MHz BW = 12KHz to 20 MHz
Period Jitter (pk-pk)			40	ps	10,000 Samples - Rising edge
Absolute Maximum Ratings					
Supply voltage (Vs)			7.0	V	with Vs=5.0VDC and 3.3 VDC
Operable temperature range	-55		+125	°C	
Storage temperature range	-62		+125	°C	

Additional Parameters		
Screening	Vectron Verification	
	Class B, MIL-PRF-55310, Rev.E	
Output Enable	Logic "0" input = Outputs disabled (Tri-state) Logic "1" or floating input = Outputs enabled)	Standard (ACMOS, TTL and LVDS)
	Logic "0" or floating input = Outputs enabled Logic "1" input = Outputs disabled (Tri-state)	Standard (LVPECL)
Weight	< 2 grams	
Processing & Packing	Handling & processing note	

Standard Environmentals	
Vibration	MIL-STD-202, Method 204, Condition G (30 G, 10Hz-2000Hz)
Shock	MIL-STD-202, Method 213, Condition I (100 G, 6ms, Sawtooth)
Acceleration	MIL-STD-883, Method 2001, Condition A (5000 G, Y1 Plane)
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Thermal Shock	MIL-STD-202, Method 107, Condition B
Solderability	MIL-STD-202, Method 208
Leak Test (Fine and Gross)	MIL-STD-883, Method 1014, Condition A1 and C1

Outline Drawing / Enclosure

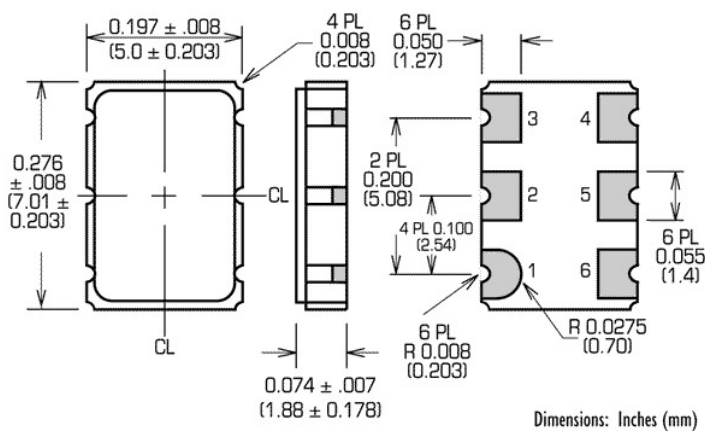


Type LCC (ACMOS or TTL)

Package Code	Height "H"
0	1.88mm

Pin Connections

1	Enable/Disable or N/C
2	Ground (Case)
3	Output
4	Supply



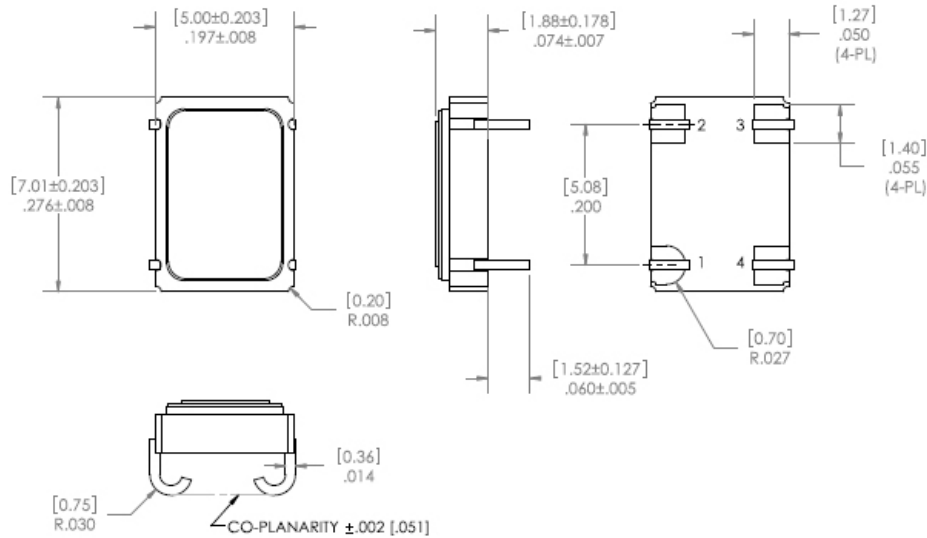
Type LCC (LVPECL or LVDS)

Package Code	Height "H"
1	1.88mm

Pin Connections

1	Enable/Disable or N/C
2	Enable/Disable or N/C (custom number required)
3	Ground (Case)
4	Output
5	Complementary Output
6	Supply

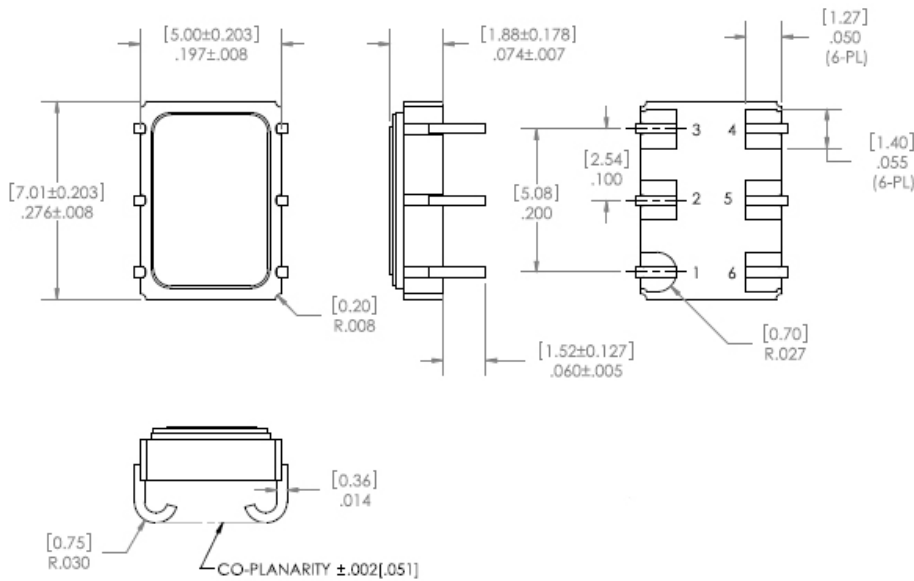
Outline Drawing / Enclosure



J leads are tinned with Alpha Pb/Sn 63/37 solder.

Type J LEAD (ACMOS or TTL)	
Package Code	Height "H"
2	3.40 mm

Pin Connections	
1	Enable/Disable or N/C
2	Ground (Case)
3	RF Output
4	Supply Voltage



J leads are tinned with Alpha Pb/Sn 63/37 solder.

Type J LEAD (LVPECL or LVDS)	
Package Code	Height "H"
3	3.40 mm

Pin Connections	
1	Enable/Disable or N/C
2	N/C
3	Ground (Case)
4	RF Output
5	Complementary Output
6	Supply Voltage

Ordering Information

PX - 700 0 - D A I - F K X B - 10M0000000

Product Family
PX: PXO

Package
5 x 7 mm

Package Code
0: 4 pad LCC
1: 6 pad LCC
2: 4 J Lead
3: 6 J Lead
4: 4 pad LCC solder coated
5: 6 pad LCC solder coated

RF Output Code
A: AC MOS
B: TTL
C: LVPECL
D: LVDS

Supply Voltage
D: 5 Vdc
E: 3.3 Vdc
H: 2.5 Vdc
J: 1.8 Vdc

Temperature Range
C: -55°C to +125°C
E: -40°C to +85°C
T: 0°C to +70°C

Frequency

Screening Option
B: "B" Level screening
X: No Screening

Enable Code
A: Enable Hi, Tristate
C: Enable Lo, Tristate
X: No Enable

Temperature Stability Code
(Reference to **Table: I**)

D: ±15ppm
F: ±25ppm
K: ±50ppm
P: ±80ppm
S: ±100ppm
Z: ±65PPM
X: Use with Overall Tolerance Code

Accuracy Code @ 25°C or Overall Temp Stability Code
(Reference to **Table: II**)

D: ±15ppm
F: ±25ppm
K: ±50ppm
P: ±80ppm
S: ±100PPM
Z: ±65PPM

Available Temperature Stability Code	
Temp Range	Temp Stability
C: -55°C to +125°C	K: ± 50ppm
	Z ± 65ppm
	P ± 80ppm
	S ± 100ppm
E: -40°C to +85°C	F: ± 25ppm
	K: ± 50ppm
	Z ± 65ppm
	P ± 80ppm
T: 0°C to +70°C	S ± 100ppm
	D: ± 15ppm
	F: ± 25ppm
	K: ± 50ppm
	Z ± 65ppm
	P ± 80ppm
	S ± 100ppm

Table: I

Available Overall Tolerance Code		
Temp Range	Overall Tolerance	Temp Stability
C: -55°C to +125°C	Z: ± 65ppm	X
	P: ± 80ppm	X
	S: ± 100ppm	X
E: -40°C to +85°C	K: ± 50ppm	X
	Z: ± 65ppm	X
	P: ± 80ppm	X
	S: ± 100ppm	X
	F: ± 25ppm	X
	K: ± 50ppm	X
T: 0°C to +70°C	Z: ± 65ppm	X
	P: ± 80ppm	X
	S: ± 100ppm	X

Table: II

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Notes:

1. Contact factory for improved stabilities or additional product options. **Not all options and codes are available at all frequencies or operating temperature ranges: reference table on page 1.**
2. Unless other stated all values are valid after warm-up time and refer to typical conditions for supply voltage, frequency control voltage, load, temperature (25°C).
3. Subject to technical modification.
4. Contact factory for availability.
5. **Enable Codes: Option A is standard for AC MOS, TTL and LVDS; Option C is standard for LVPECL. Contact factory for custom options.**

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