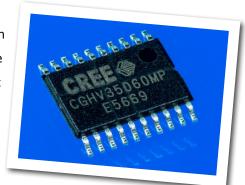


CGHV35060MP

60W, 2700-3500 MHz, 50V, GaN HEMT for S Band Radar and LTE base stations

Cree's CGHV35060MP is a 60W input matched, gallium nitride (GaN) high electron mobility transistor (HEMT) optimized for S Band performance. The CGHV35060MP is suitable for typical bands of 2.7-3.1GHz and 3.1-3.5GHz while the input matched transistor provides optimal gain, power and efficiency in a small 6.5mm \times 4.4mm plastic surface mount (SMT) package. The typical performance plots in the datasheet are derived with CGHV35060MP matched into a 3.1-3.5GHz high power amplifier.



PN: CGHV35060MP

Typical Performance Over 3.1 - 3.5 GHz ($T_c = 25$ °c) of Demonstration Amplifier

Parameter	3.1 GHz	3.3 GHz	3.5 GHz	Units
Gain	14.5	14.3	13.8	dB
Output Power	88	88	75	W
Drain Efficiency	61	67	64	%

Note:

Measured in the CGHV35060MP-TB amplifier circuit, under 100 μ s pulse width, 10% duty cycle, $P_{IN} = 35$ dBm.

Features

ROHS

- Reference design amplifier 3.1 3.5 GHz
- 75W Typical output power
- 14.5 dB power gain
- 67% Drain efficiency
- Internally pre-matched on input, unmatched output



Absolute Maximum Ratings (not simultaneous) at 25°C Case Temperature

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	$V_{\scriptscriptstyle DSS}$	150	Volts	25°C
Gate-to-Source Voltage	V_{GS}	-10, +2	Volts	25°C
Storage Temperature	T_{STG}	-65, +150	°C	
Operating Junction Temperature	T,	225	°C	
Maximum Forward Gate Current	I_{GMAX}	10.4	mA	25°C
Maximum Drain Current ¹	I_{DMAX}	6.3	Α	25°C
Soldering Temperature ²	T_s	245	°C	
CW Thermal Resistance, Junction to Case ³	$R_{_{\theta JC}}$	2.6	°C/W	85°C, P _{DISS} = 52 W
Pulsed Thermal Resistance, Junction to Case	$R_{_{ heta JC}}$	1.95	°C/W	85 °C, P_{DISS} = 62 W, 100 µsec 10%
Case Operating Temperature ⁴	T _c	-40, +107	°C	

Note:

- ¹ Current limit for long term, reliable operation.
- ² Refer to the Application Note on soldering at http://www.cree.com/rf/document-library
- ³ Measured for the CGHV35060MP
- $^{\mbox{\tiny 4}}$ See also, the Power Dissipation De-rating Curve on Page 4.

Electrical Characteristics ($T_c = 25$ °C)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics¹						
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V _{DC}	$V_{DS} = 10 \text{ V}, I_{D} = 10.4 \text{ mA}$
Gate Quiescent Voltage	$V_{GS(Q)}$	-	-2.7	-	V_{DC}	$V_{DS} = 50 \text{ V, } I_{D} = 125 \text{ mA}$
Saturated Drain Current ²	\mathbf{I}_{DS}	8.4	10.4	-	Α	$V_{DS} = 6.0 \text{ V}, V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	$V_{_{\mathrm{BR}}}$	150	-	-	V_{DC}	$V_{GS} = -8 \text{ V, I}_{D} = 10.4 \text{ mA}$
RF Characteristics ⁴ (T _c = 25 °C, F ₀ = 3.3 GHz unless otherwise noted)						
Saturated Output Power ³	P_{SAT}	-	75	-	W	$V_{_{\mathrm{DD}}}$ = 50 V, $I_{_{\mathrm{DQ}}}$ = 125 mA, $P_{_{\mathrm{IN}}}$ = 35 dBm
Pulsed Drain Efficiency ³	η	-	67	-	%	$V_{_{\mathrm{DD}}}$ = 50 V, $I_{_{\mathrm{DQ}}}$ = 125 mA, $P_{_{\mathrm{IN}}}$ = 35 dBm
Gain ³	G	-	14.5	-	dB	V_{DD} = 50 V, I_{DQ} = 125 mA, P_{IN} = 35 dBm
Gain ⁵	G	-	17	-	dB	V_{DD} = 50 V, I_{DQ} = 125 mA, P_{OUT} = 41.5 dBm
WCDMA Linearity⁵	ACLR	-	-35	-	dBc	$V_{DD} = 50 \text{ V, } I_{DQ} = 125 \text{ mA, } P_{OUT} = 41.5 \text{ dBm}$
Drain Efficiency⁵	η	-	35	-	%	V_{DD} = 50 V, I_{DQ} = 125 mA, P_{OUT} = 41.5 dBm
Output Mismatch Stress ³	VSWR	-	-	TBD	Ψ	No damage at all phase angles, $\rm V_{DD} = 50~V,~I_{DQ} = 125~mA,~P_{OUT} = 60~W~Pulsed$
Dynamic Characteristics						
Input Capacitance ⁶	C_{GS}	-	32.16	-	pF	$V_{DS} = 50 \text{ V}, V_{gs} = -8 \text{ V}, f = 1 \text{ MHz}$
Output Capacitance ⁶	C _{DS}	-	4.4	-	pF	$V_{DS} = 50 \text{ V}, V_{gs} = -8 \text{ V}, f = 1 \text{ MHz}$
Feedback Capacitance	C_{GD}	-	0.5	-	pF	$V_{DS} = 50 \text{ V}, V_{gs} = -8 \text{ V}, f = 1 \text{ MHz}$

Notes:

- ¹ Measured on wafer prior to packaging.
- ² Scaled from PCM data.
- $^{\scriptscriptstyle 3}$ Pulse Width = 100 μ s, Duty Cycle = 10%
- ⁴ Measured in CGHV35060MP-TB.
- 5 Single Carrier WCDMA, 3GPP Test Model 1, 64 DPCH, 45% Clipping, PAR = 7.5 dB @ 0.01% Probability on CCDF, $V_{DD} = 50 \text{ V}$.
- ⁶ Includes package.



Typical Performance

Figure 1. - Small Signal Gain and Return Losses of the CGHV35060MP Measured in Demonstration Amplifier Circuit CGHV35060MP-TB

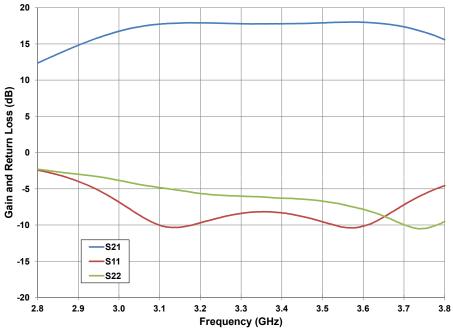
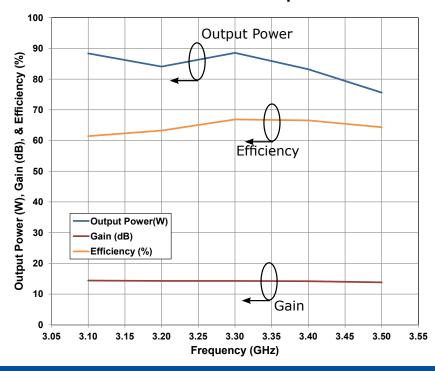
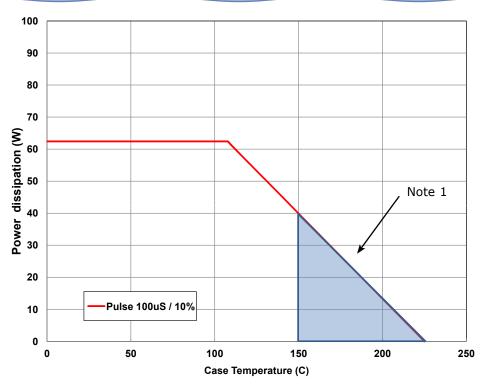


Figure 2. - Gain, Efficiency & Output Power for the CGHV35060MP at $P_{\rm IN}$ = 35 dBm with 100 µs/10% as Measured in Demonstration Amplifier Circuit CGHV35060MP





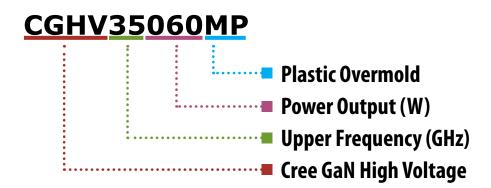
CGHV35060MP Power Dissipation De-rating Curve



Note 1. Area exceeds Maximum Case Temperature (See Page 2).



Part Number System



Parameter	Value	Units
Upper Frequency ¹	3.5	GHz
Power Output	60	W
Package	MP	-

Table 1.

Note¹: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value		
А	0		
В	1		
С	2		
D	3		
E	4		
F	5		
G	6		
Н	7		
J	8		
K	9		
Examples:	1A = 10.0 GHz 2H = 27.0 GHz		

Table 2.



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