

SBB-2082S

InGaP HBT Amplifier
50MHz to 850MHz

RFMD's SBB-2082S is a high performance InGaP HBT utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SBB-2082S does not require a dropping resistor as compared to typical Darlington amplifiers. The SBB-2082S product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to 50Ω. RFMD can provide various levels of device screening for military or Hi-Reliability applications.



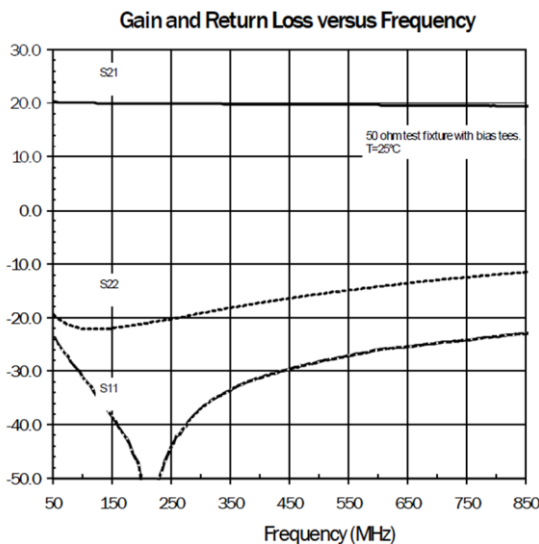
Package: Hermetic, 2-pin,
5.8mm x 2.8mm

Features

- Hermetic Package for Hi-Reliability Applications
- Single Fixed 5V Supply
- Patented Thermal Design and Bias Circuit
- OIP3 = 40.5dBm at 70MHz
- P1dB = 19.5dBm at 70MHz

Applications

- Military Communications
- Aerospace and Defense
- PA Driver Amp



Ordering Information

SBB-2082S Two lead hermetic package

Absolute Maximum Ratings

Parameter	Rating	Unit
Total Current (I_D)	110	mA
Device Voltage (V_D)	5.5	V
RF Input Power	+24	dBm
Operating Lead Temperature (T_L)	-40 to +85	°C
Storage Temperature Range	-55 to +150	°C
Operating Junction Temp (T_J)	+150	°C
Moisture Sensitivity Level	Hermetic	
ESD Rating - Human Body Model (HBM)	Class 2	



Caution! ESD sensitive device.



RFMD Green: RoHS compliant per EU Directive 2011/65/EU, halogen free per IEC 61249-2-21, <1000ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony solder.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

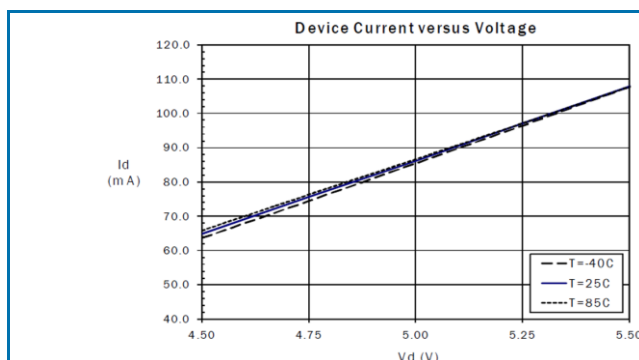
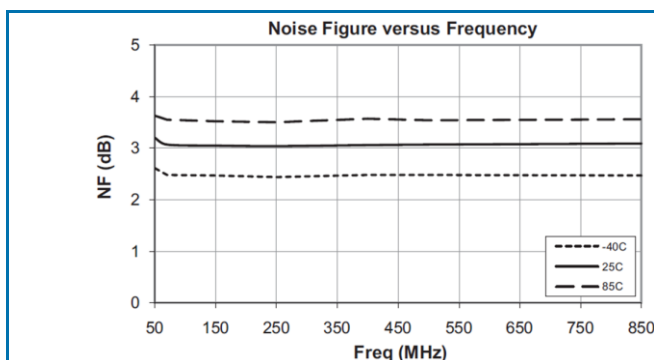
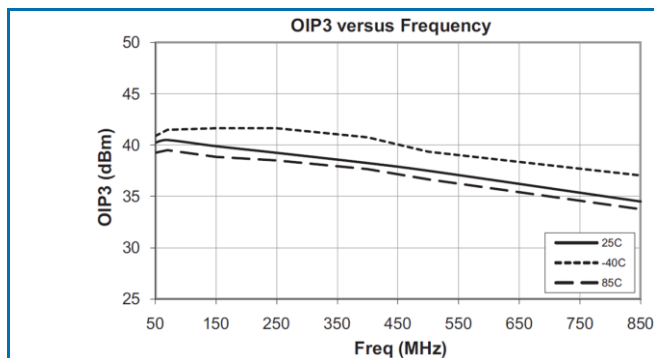
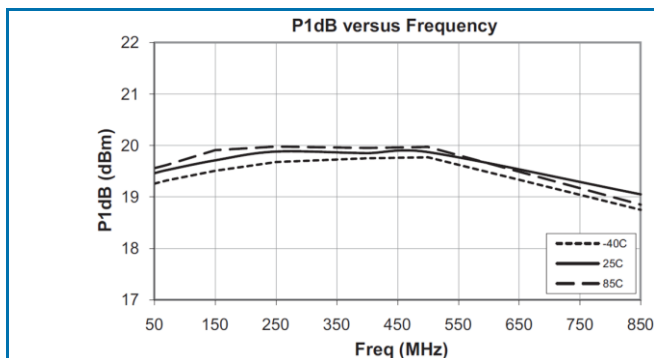
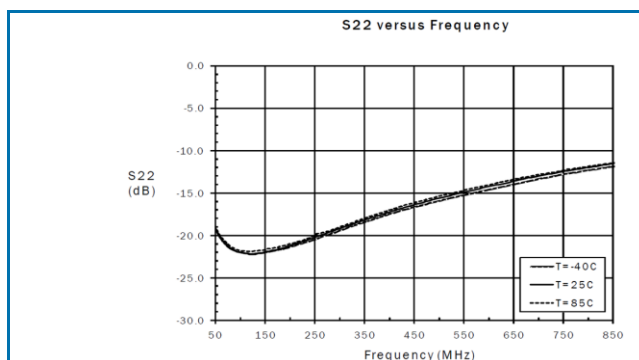
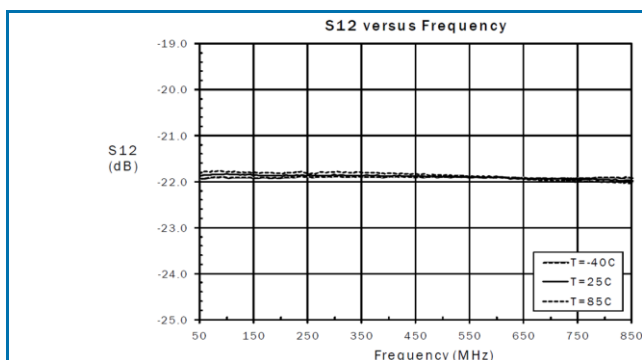
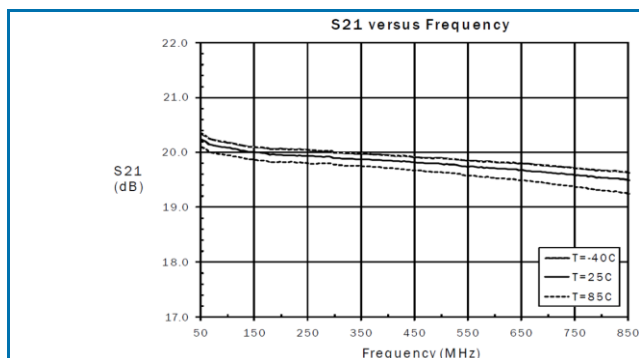
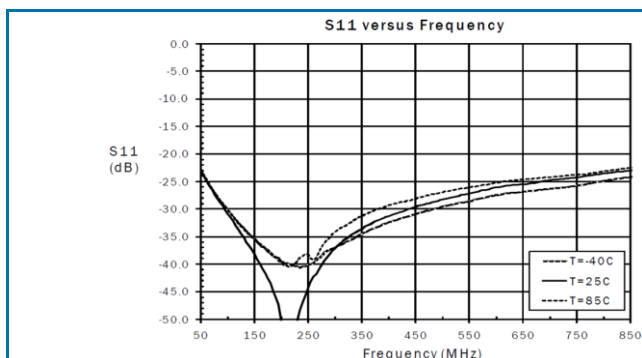
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table below.

Bias conditions should also satisfy the following expression: $I_D V_D < (T_J - T_L) / R_{TH}$, $j-l$ and $T_L = T_{LEAD}$

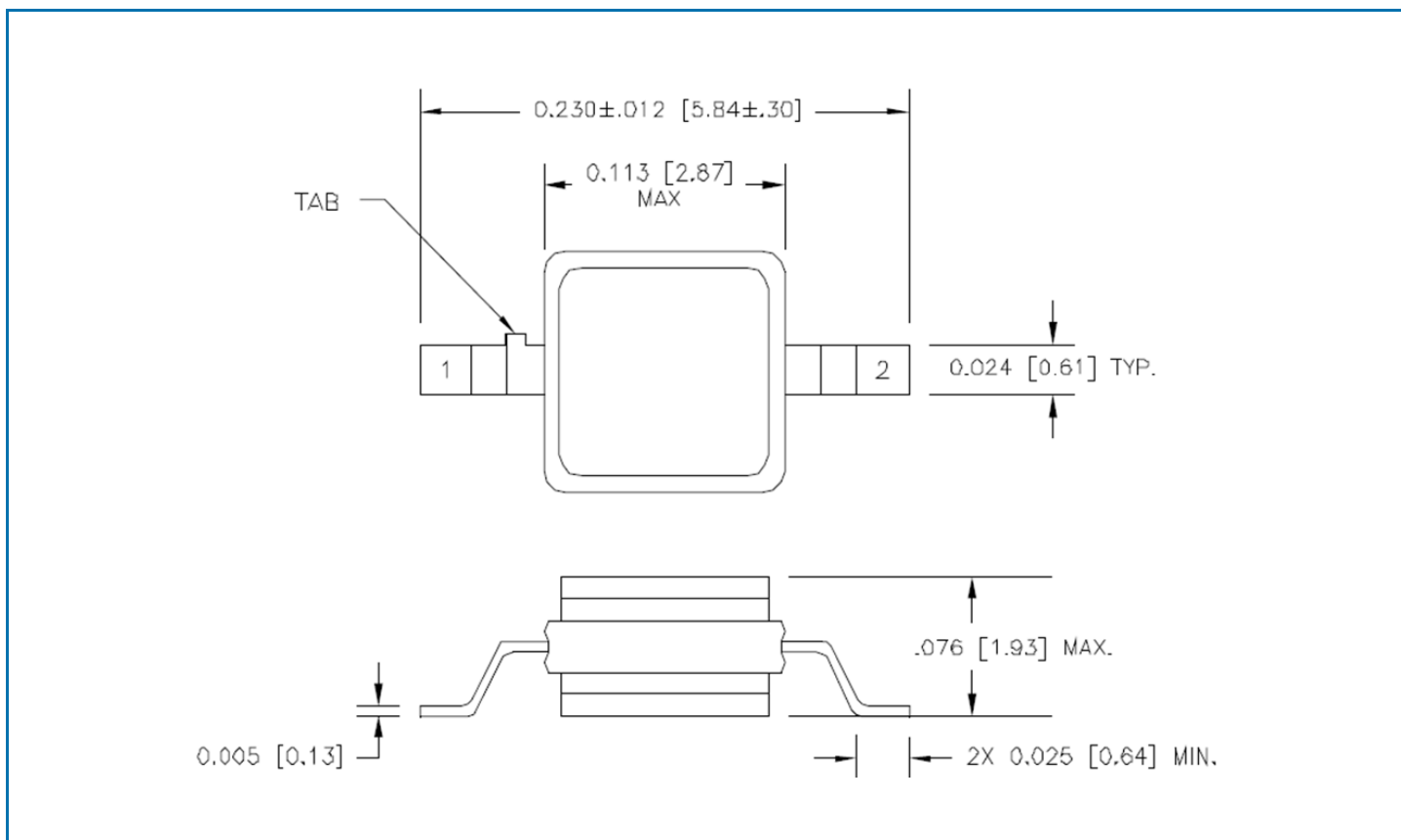
Nominal Operating Parameters

Parameter	Specification			Unit	Condition
	Min	Typ	Max		
General Performance					Test Conditions: $Z_0 = 50\Omega$, $V_D = 5V$, $I_D = 86mA$, $T = 25^\circ C$, OIP3 $P_{OUT}/tone = 0dBm$, 50Ω test fixture with bias tees
Frequency of Operation	50		850	MHz	
Supply Voltage		5.0		V	
Supply Current	75	86	98	mA	
Small Signal Gain	18.5	20.2		dB	Freq = 100MHz
		19.8		dB	Freq = 500MHz
Output Power at 1dB Compression	18.0	19.5		dBm	Freq = 70MHz
Output Third Order Intercept Point	38.5	40.5		dBm	F1 = 70MHz, F2 = 71MHz
Input Return Loss	9.5	25		dB	Freq = 100MHz
		25.0		dB	Freq = 500MHz
Output Return Loss	9.5	20		dB	Freq = 100MHz
		15.0		dB	Freq = 500MHz
Reverse Isolation	19	22		dB	Freq = 100MHz
		22.0		dB	Freq = 500MHz
Noise Figure		3.1		dB	Freq = 500MHz
Thermal Resistance		45		°C/W	Junction to case

Typical Performance: (50Ω Test Fixture with Bias Tees) $V_S = 5.0V$



Package Drawing (Dimensions in inches [millimeters])



Notes:

1. Package material: Ceramic
2. Lead finish: Gold

Pin Names and Descriptions

Pin	Name	Description
1	RFIN	This pin is DC coupled and matched to 50Ω. An external DC block is required.
2	RFOUT	This pin is DC coupled and matched to 50Ω. DC bias is applied through this pin.
Package Paddle	GND	Package backside must be connected to RF/DC ground.

Typical Application Schematic

