

MINIATURE SMD HCMOS/TTL OSCILLATOR (5.0 x 3.2 x 1.3mm)

FEATURES:

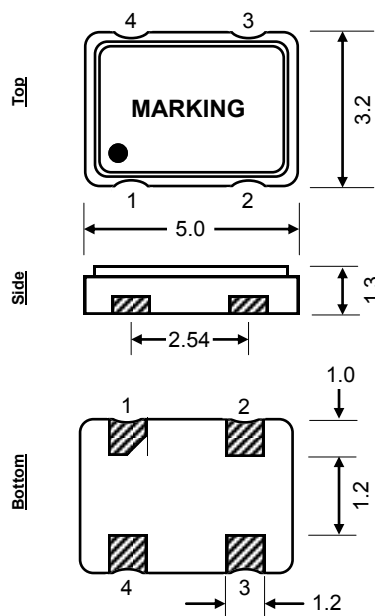
- WIDE FREQUENCY RANGE
- LOW VOLTAGE / LOW CURRENT
- LOW COST



■ SPECIFICATION

FREQUENCY RANGE		1.000MHz ~ 165.000MHz	1.000MHz ~ 200.000MHz	1.000MHz ~ 165.000MHz
FREQUENCY STABILITY		±10ppm ~ ±100ppm		
OPERATING TEMPERATURE RANGE		STD: 0°C ~ 70°C, Option: -40°C ~ 85°C (See P/N guide for other options)		
STORAGE TEMPERATURE RANGE		-55°C ~ 125°C		
SUPPLY VOLTAGE ±10%		1.0V _{DC} ~ 2.5V _{DC}	3.3V _{DC}	5.0V _{DC}
SUPPLY CURRENT	1MHz ≤ f < 25MHz	12mA max.	15mA max.	20mA max.
	25MHz ≤ f < 50MHz	15mA max.	20mA max.	30mA max.
	50MHz ≤ f < 70MHz	20mA max.	30mA max.	45mA max.
	70MHz ≤ f < 125MHz	30mA max.	60mA max.	60mA max.
	125MHz ≤ f	40mA max.	60mA max.	80mA max.
OUTPUT	LOGIC FAMILY	HCMOS, TTL, CMOS/TTL		
	LOAD	15pF	30pF max. (15pF typ.)	50pF max. (15pF typ.)
	LEVEL LOGIC '1' (V _{OH})	HCMOS: 0.9 x V _{DD} min. TTL: 2.4V _{DC} min.		
	LEVEL LOGIC '0' (V _{OL})	HCMOS: 0.1 x V _{DD} max. TTL: 0.4V _{DC} max.		
	SYMMETRY (DUTY CYCLE)	40% / 60% @ 1/2V _{DD} level, Option: 45% / 55%		
	RISE AND FALL TIME (Tr/Tf)	3.0nS typ. , 10.0nS max.		
STAND-BY VOLTAGE	START-UP TIME	10mS max.		
	ENABLE (ViH)	0.7 x V _{DD} ≤ ViH ≤ V _{DD} or N/C		
	DISABLE (ViL)	ViL ≤ 0.3 x V _{DD}		
ENABLE / DISABLE DELAY TIME		10mS max.		
AGING @ 25°C ± 3°C		±3.0ppm / 1 st year max.		
PHASE JITTER RMS (12kHz ~ 20MHz)		1.0pS max.		

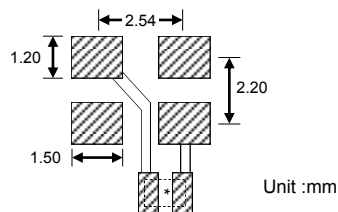
■ PACKAGE DIMENSIONS



Unit : mm

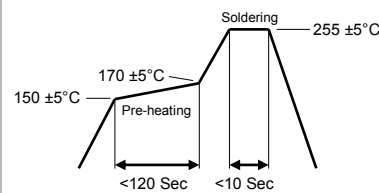
Pin Configuration	
1	En/Dis
2	GND
3	Output
4	V _{DD}

■ SOLDER PATTERN



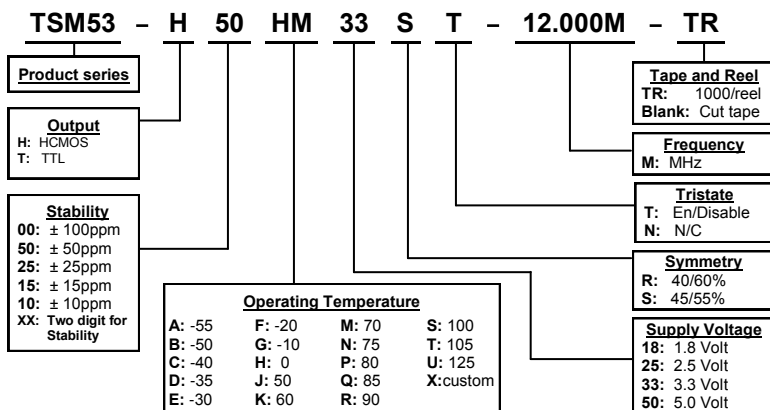
*0.01μF to 0.1μF bypass capacitor recommended

■ REFLOW PROFILE



Note: Consult factory for detail

■ PART NUMBERING GUIDE



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