

MINIATURE CERAMIC SMD VCXO (7.0 x 5.0 x 1.8mm)
FEATURES

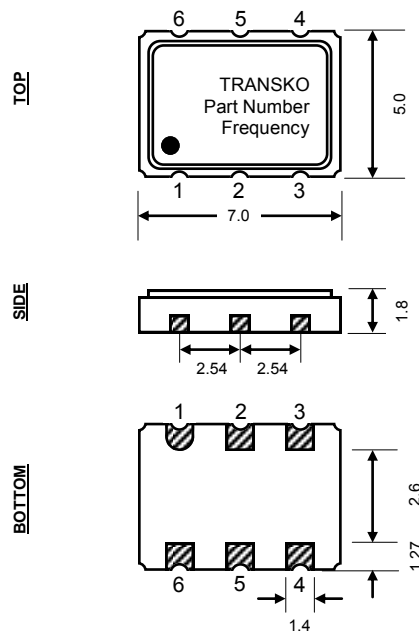
- HIGH FREQUENCY VCXO
- LVDS / LVPECL OUTPUT VCXO
- LOW NOISE / LOW CURRENT


■ SPECIFICATION

PARAMETER			MIN.	TYP.	MAX.	UNIT	NOTE
FREQUENCY RANGE		2.5V	1		400	MHz	
		2.8V	1		400	MHz	
		3.3V	0.75		800	MHz	
STANDARD FREQUENCY			19.44, 44.736, 74.250, 74.1758, 76.800, 77.76, 106.25, 122.88, 155.52, 156.25, 153.60, 161.1328, 200.00, 312.50, 800.00				
FREQUENCY STABILITY			±10		±100	ppm	See P/N guide for other options
OPERATING TEMPERATURE RANGE			-40		85	°C	See P/N guide for other options
STORAGE TEMPERATURE RANGE			-55		125	°C	
SUPPLY VOLTAGE ±10%			2.5		3.3	V	
SUPPLY CURRENT		LVDS			80	mA	
		LVPECL			90	mA	
OUTPUT	LOGIC FAMILY		LVDS, LVPECL				
	LOAD	LVDS	100Ω				Output - Complementary Output
		LVPECL	50Ω				to V _{DD} - 2.0V
	LEVEL	LVDS (V _{OH})		1.4	1.6	V	
		LVDS (V _{OL})	0.9	1.1		V	
		LVPECL (V _{OH})	V _{DD} - 1.02V			V	
		LVPECL (V _{OL})			V _{DD} - 1.63V	V	
	SYMMETRY (DUTY CYCLE)		45	50	55	%	
	RISE AND FALL TIME (Tr/Tf)			0.3	1.0	nS	@ 20% ~ 80% Output level
	START-UP TIME				5	mS	
STAND-BY VOLTAGE		ENABLE (V _{IH})	0.7 x V _{DD}			V	
		DISABLE (V _{IL})			0.3 x V _{DD}	V	
FREQUENCY DEVIATION			±50			ppm	See P/N guide for other options
CONTROL VOLTAGE		2.5V	0		2.5	V	
		2.8V	0		2.8	V	
		3.3V	0.3		3.0	V	
SLOPE			Positive				
LINEARITY					10	%	
MODULATION BANDWIDTH			25.0			kHz	
INPUT IMPEDANCE				50		kΩ	
AGING @ 25°C + 3°C					±3	ppm	@ for 1 st year
PHASE JITTER RMS				0.5	1.0	pS	@ 12kHz ~ 20MHz offset

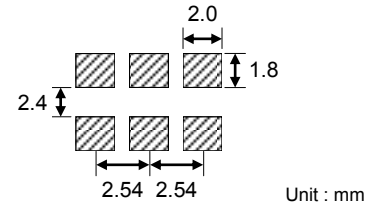
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■ PACKAGE DIMENSIONS

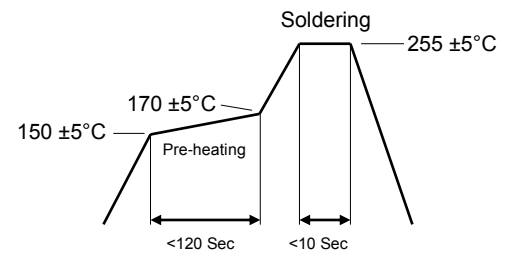


Pin Configuration	
1	V _C
2	En/Disable
3	GND
4	Output
5	C-Output
6	V _{DD}

■ SOLDER PATTERN

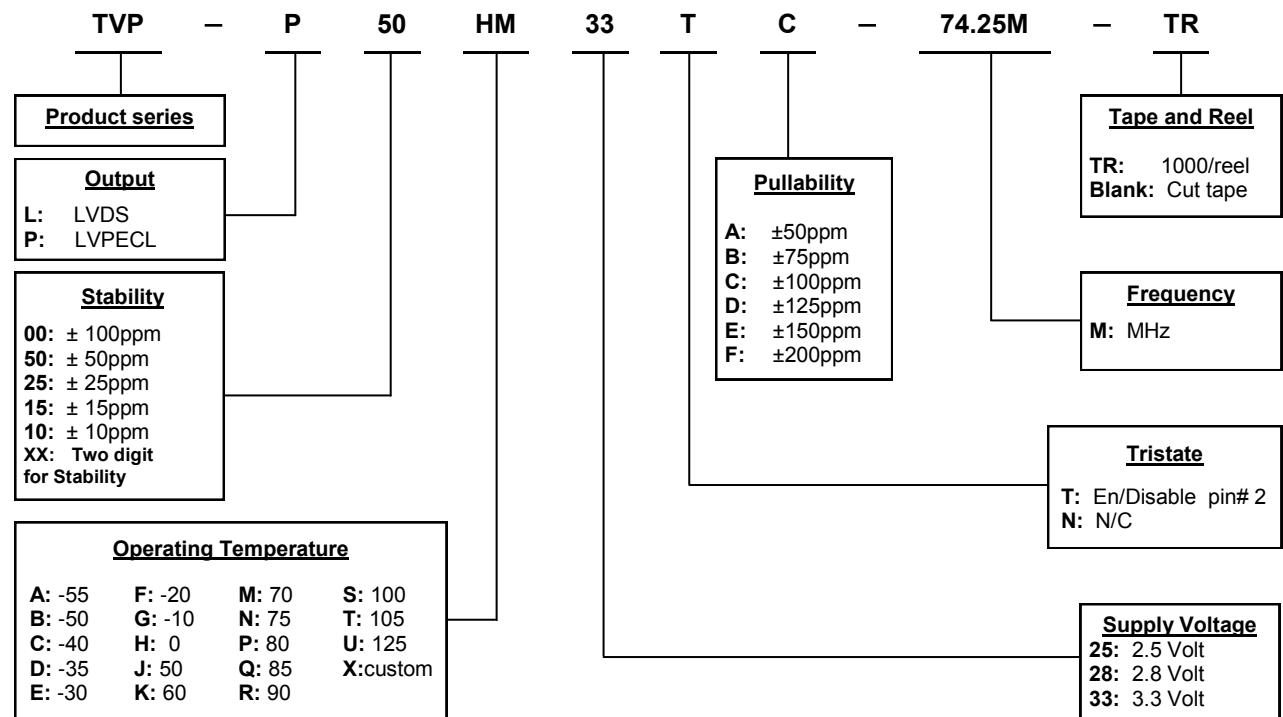


■ REFLOW PROFILE



Note: Consult factory for detail

■ PART NUMBERING GUIDE



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