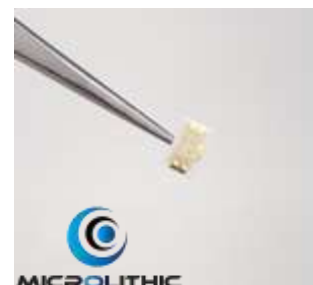


MICROLITHIC™ DOUBLE-BALANCED MIXER

ML1-1050

The ML1-1050 is a Microlithic™ double balanced mixer. As with all Microlithic™ mixers (patent pending), it features excellent conversion loss, isolations, and spurious performance across a broad bandwidth and in a miniaturized form factor. Accurate, nonlinear software models are available for Microwave Office through the Marki Microwave PDK. The ML1-1050 is available as a wire bondable chip or a 2.4 mm connectorized package. The ML1-1050 is an excellent alternative to Marki Microwave M9 mixers packaged in drop-in carriers such as the ES carrier.



Features

- Compact Chip Style Package (0.152" x 0.090" x 0.010")
- CAD Optimized for Superior Isolation and Spurious Response
- Broadband Performance
- Excellent Unit-to-Unit Repeatability
- Fully nonlinear software models available with Marki PDK for Microwave Office
- RoHS Compliant

Electrical Specifications - Specifications guaranteed from -55 to +100°C, measured in a 50Ω system.

Parameter	LO (GHz)	RF (GHz)	IF (GHz)	Min	Typ	Max	Diode Option LO drive level (dBm)
Conversion Loss (dB)	10-50		DC-14		9	13	
			14-16		11	14	
Isolation (dB)							
LO-RF					43		
LO-IF					20		
RF-IF					20		
Input 1 dB Compression (dBm)					+3		L (+10 to +13)
					+9		I (+15 to +19)
Input Two-Tone Third Order Intercept Point (dBm)					+15		L (+10 to +13)
					+21		I (+15 to +19)

Part Number Options

Please specify diode level and package style by adding to model number.				
Package Styles		Examples		
Connectorized ¹	<u>S</u>	ML1-1050LCH-2, ML1-1050LS		
Chip ^{2,3} (RoHS)	<u>CH-2</u>	<u>ML1-1050</u> (Model)	<u>L</u> (Diode Option)	<u>CH-2</u> (Package)

¹Connectorized package consists of chip package wire bonded to a substrate, equivalent to an evaluation board.

²Chip package connects to external circuit through wire bondable gold pads.

³Note: For port locations and I/O designations, refer to the drawing on page 4 of this document.



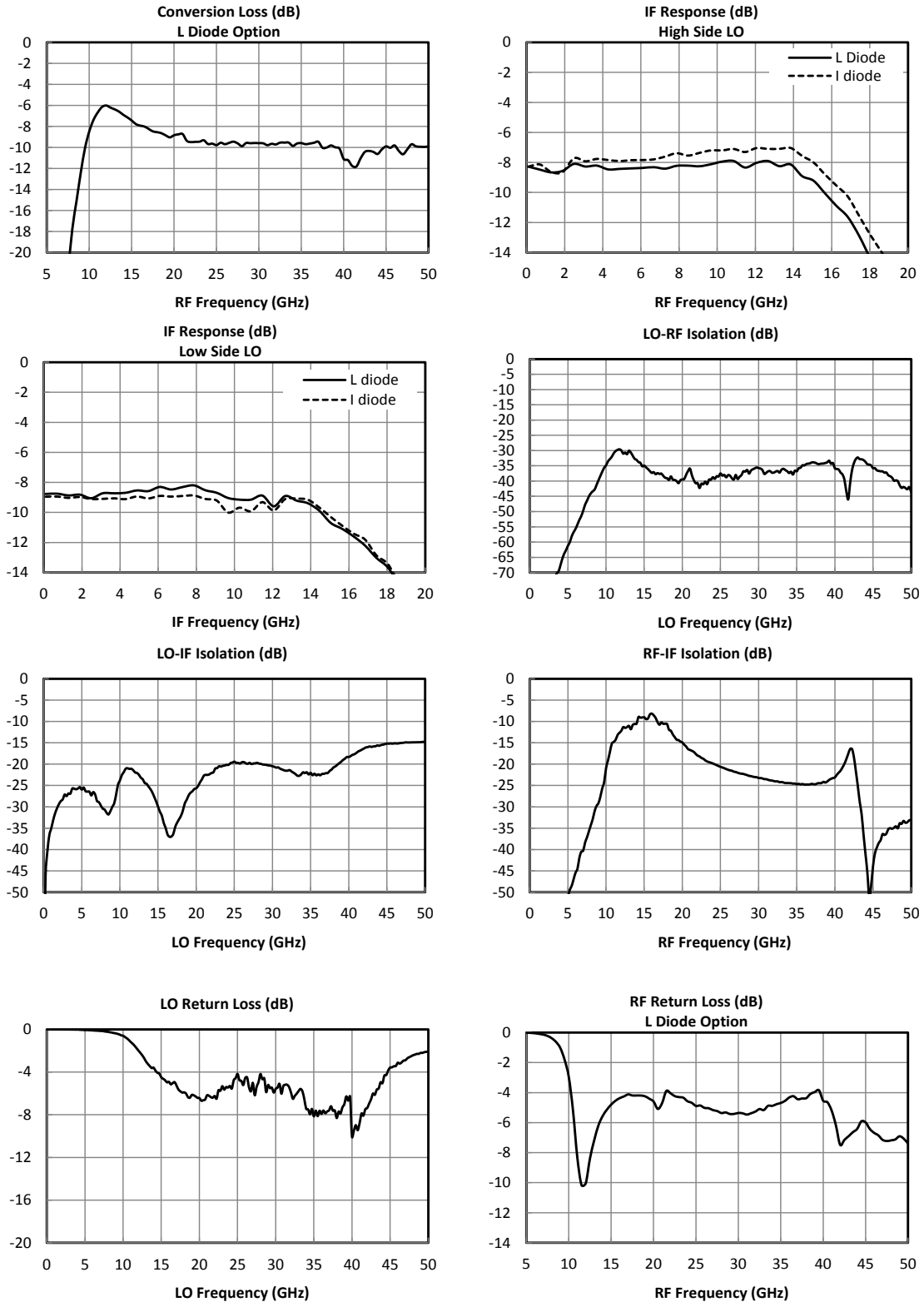
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LO/RF 10 to 50 GHz
IF DC to 16 GHz

Typical Performance





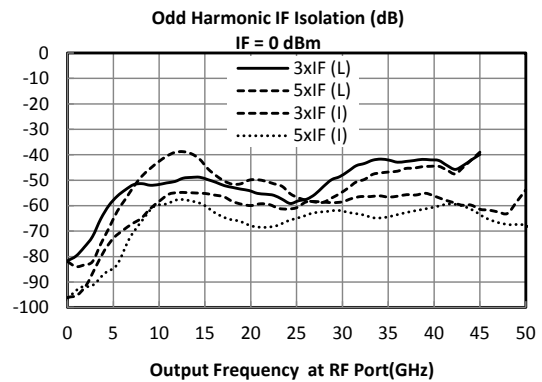
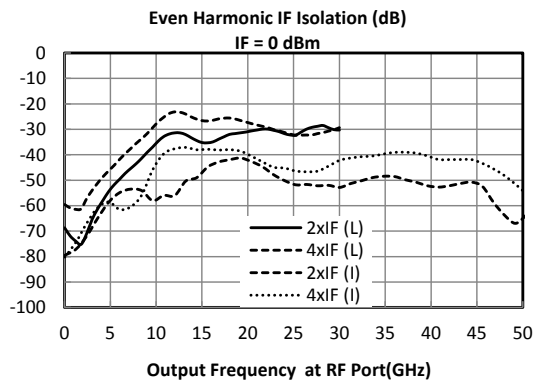
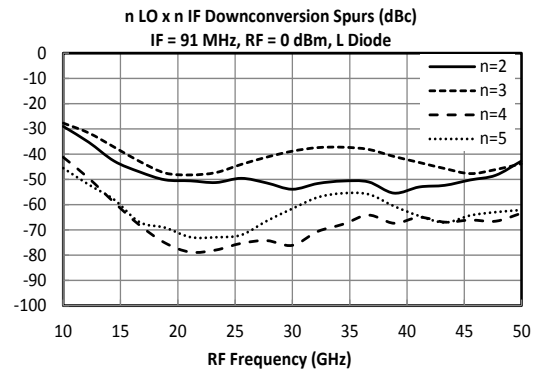
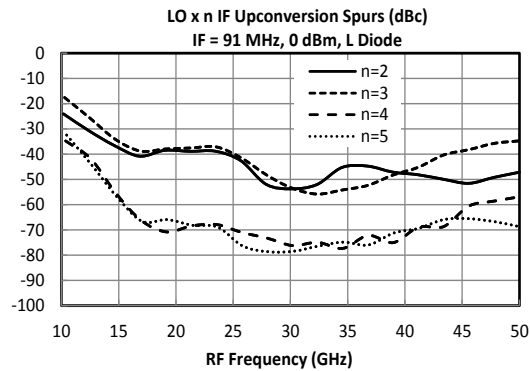
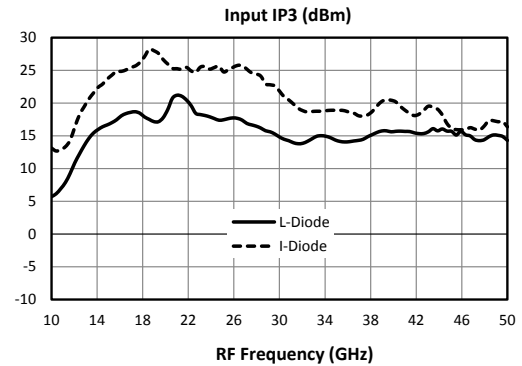
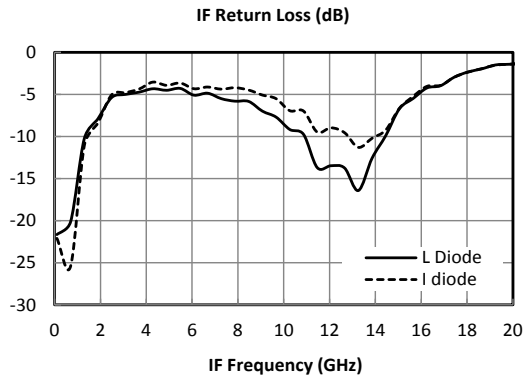
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LO/RF 10 to 50 GHz
IF DC to 16 GHz

Typical Performance





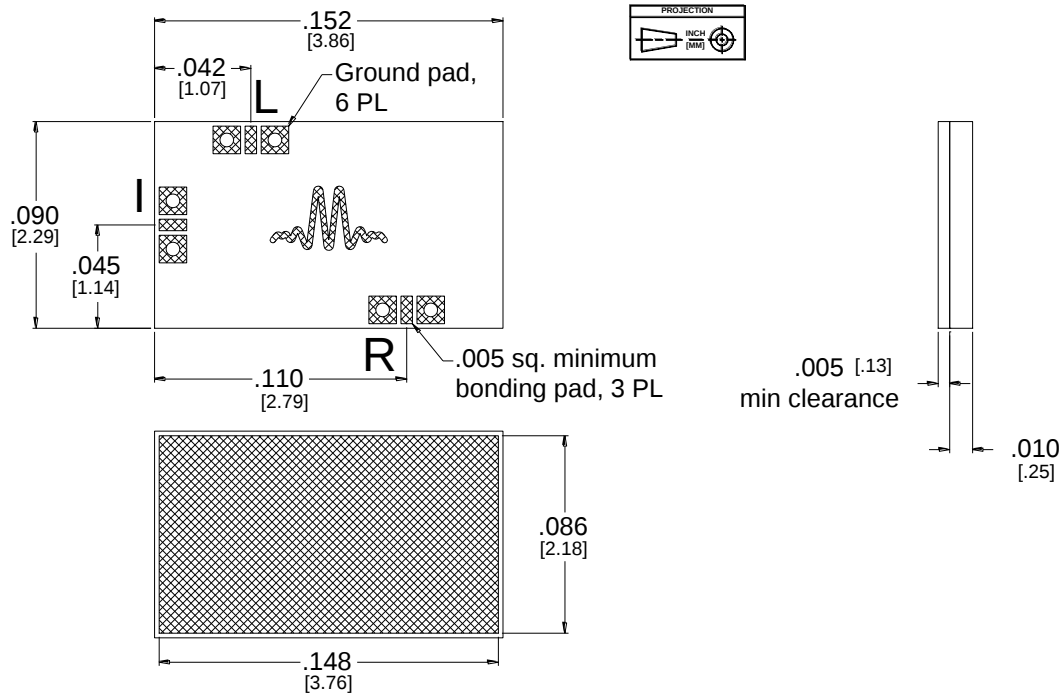
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LO/RF 10 to 50 GHz

IF DC to 16 GHz

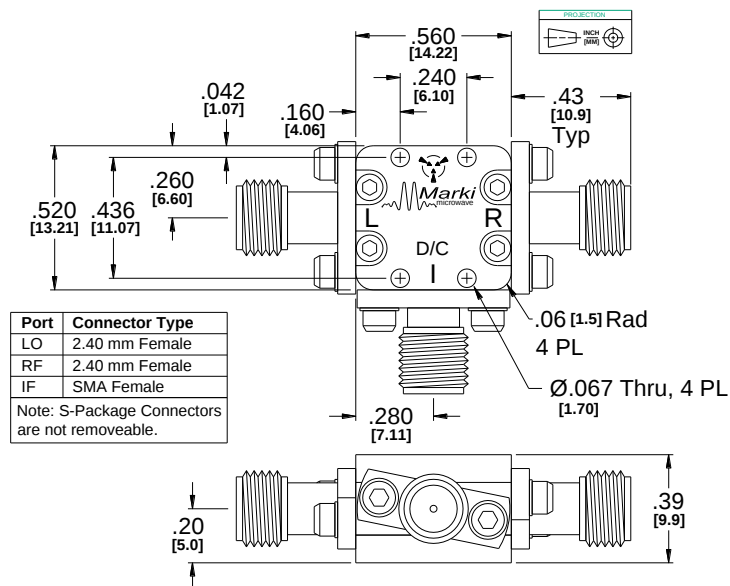


Outline Drawing – CH-2 package

*CH Substrate material is .010 thick Ceramic.

I/O traces and ground plane finish is 2.5 microns Au over .05 microns WTi.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. All bonds should be as short as possible <0.31 mm (12 mils).



Outline Drawing – SV package



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**LO/RF 10 to 50 GHz
IF DC to 16 GHz**

Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies ($\pm mLO \pm nRF$) within the 10 to 50 GHz RF/LO bands, which create a 100 MHz IF spurious output. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where “n” is the RF spur order. For example, the 2RFx2LO spur is 58 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) dB lower, or 68 dBc.

Typical Downconversion Spurious Suppression (dBc): I diode (L diode) ⁵

-10 dBm RF Input	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	Reference	29(23)	16(16)	27(23)	30(30)
2xRF	44(61)	58(61)	47(59)	55(56)	48(57)
3xRF	76(61)	84(78)	72(72)	82(82)	74(74)
4xRF	114(110)	98(105)	94(106)	102(106)	94(106)
5xRF	123(122)	120(112)	120(119)	118(118)	115(118)

Upconversion Spurious Suppression

Spurious data is taken by mixing a 100 MHz IF with LO frequencies ($\pm mLO \pm nIF$), which creates an RF within the 10 to 50 GHz RF band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where “n” is the IF spur order. For example, the 2IFx1LO spur is typically 53 dBc for a -10 dBm input, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) dB lower, or 63 dBc.

Typical Upconversion Spurious Suppression (dBc): I diode (L diode) ⁵

-10 dBm IF Input	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	Reference	20(23)	15(14)	34(35)	37(37)
2xIF	53(53)	47(48)	50(49)	51(45)	56(58)
3xIF	57(61)	65(61)	61(55)	63(59)	69(72)
4xIF	94(95)	90(88)	92(78)	78(75)	81(80)
5xIF	105(107)	107(98)	99(84)	95(91)	92(93)

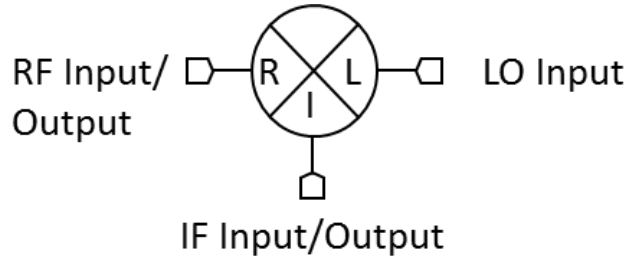


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LO/RF 10 to 50 GHz
IF DC to 16 GHz



Port	Description	DC Interface Schematic
LO	The LO port is DC short to ground and AC matched to 50 Ohms from 10 GHz to 50 GHz. Blocking capacitor is optional.	
RF	The RF port is DC short to ground and AC matched to 50 Ohms from 10 GHz to 50 GHz. Blocking capacitor is optional.	
IF	The IF port is DC coupled to the diodes. Blocking capacitor is optional.	

Absolute Maximum Ratings	
Parameter	Maximum Rating
RF DC Current	1 Amp
LO DC Current	1 Amp
IF DC Current	50 mA
RF Power Handling (RF+LO)	+25 dBm at +25°C, derated linearly to +20 dBm at +100°C
Operating Temperature	-55°C to +100°C
Storage Temperature	-65°C to +125°C

DATA SHEET NOTES:

1. Mixer Conversion Loss Plot IF frequency is 100 MHz.
2. Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
3. Conversion Loss typically degrades less than 0.5 dB for LO drives 2 dB below the lowest and 3 dB above highest nominal LO drive levels.
4. Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
5. Unless otherwise specified L diode data taken with 13 dBm LO drive, I diode data taken with 17 dBm LO drive.
6. Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
7. Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.

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