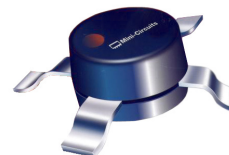


# Surface Mount Monolithic Amplifier

## DC-2 GHz

### Product Features

- Wideband, DC to 2 GHz
- Exact footprint substitute for Avago's MSA-0386
- Internally Matched to 50 Ohms
- Unconditionally stable
- Protected by US Patent, 6,943,629



## MAR-3SM+

CASE STYLE: WW107  
PRICE: \$1.24 ea. QTY. (20)

### Typical Applications

- Cellular
- PCN instrumentation
- VHF/UHF transmitters/receivers

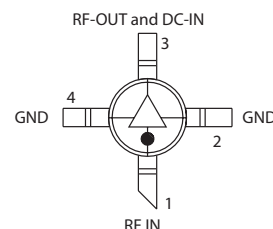
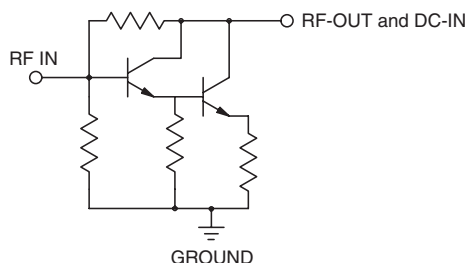
#### +RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

### General Description

MAR-3SM+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a Micro-X package. MAR-3SM+ uses Darlington configuration and is fabricated using InGaP HBT technology. Expected MTBF is 2700 years at 85°C case temperature.

### simplified schematic and pin description



| Function         | Pin Number | Description                                                                                                                                                                                                                                                                |
|------------------|------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| RF IN            | 1          | RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.                                                                                                                                                        |
| RF-OUT and DC-IN | 3          | RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit". |
| GND              | 2,4        | Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.                                                                                                                                  |

#### Notes

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REV. C  
M120653  
MAR-3SM+  
150320  
Page 1 of 4

## Electrical Specifications at 25°C and 35mA, unless noted

| Parameter                                         |               | Min.             | Typ.  | Max. | Units |
|---------------------------------------------------|---------------|------------------|-------|------|-------|
| Frequency Range*                                  |               | DC               |       | 2    | GHz   |
| Gain                                              | f=0.1 GHz     | —                | 12.5  | —    | dB    |
|                                                   | f=1 GHz       | —                | 12.0  | —    |       |
|                                                   | f=2 GHz       | 8.0 <sup>2</sup> | 10.5  | —    |       |
| Input Return Loss                                 | f=DC to 2 GHz |                  | 17.5  |      | dB    |
| Output Return Loss                                | f=DC to 2 GHz |                  | 17.5  |      | dB    |
| Output Power @ 1 dB compression                   | f=1 GHz       |                  | +10.0 |      | dBm   |
| Output IP3                                        | f=1 GHz       |                  | +28   |      | dBm   |
| Noise Figure                                      | f=1 GHz       |                  | 3.7   |      | dB    |
| Recommended Device Operating Current              |               |                  | 35    |      | mA    |
| Device Operating Voltage                          |               |                  | 5.0   |      | V     |
| Device Voltage Variation vs. Temperature at 35 mA |               |                  | -2.6  |      | mV/°C |
| Device Voltage Variation vs. Current at 25°C      |               |                  | 15.5  |      | mV/mA |
| Thermal Resistance, junction-to-case <sup>1</sup> |               |                  | 179   |      | °C/W  |

\*Guaranteed specification DC-2 GHz. Low frequency cut off determined by external coupling capacitors.

## Absolute Maximum Ratings

| Parameter             | Ratings        |
|-----------------------|----------------|
| Operating Temperature | -40°C to 85°C  |
| Storage Temperature   | -55°C to 100°C |
| Operating Current     | 70mA           |
| Power Dissipation     | 400mW          |
| Input Power           | 13dBm          |

Note: Permanent damage may occur if any of these limits are exceeded.

These ratings are not intended for continuous normal operation.

<sup>1</sup>Case is defined as ground leads.

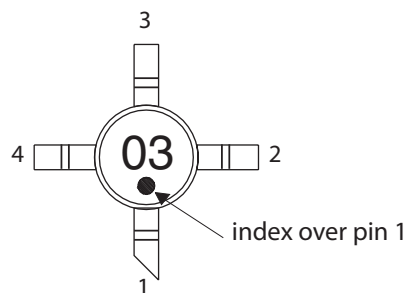
<sup>2</sup>Full temperature range.

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## Product Marking



Markings in addition to model number designation may appear for internal quality control purposes.

## Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

## Performance data, graphs, s-parameter data set (.zip file)

### Case Style: WW107

Plastic micro-x, .085 body diameter, lead finish: tin/silver/nickel

### Tape & Reel: F4

7" Reels with 20, 50, 100, 200, 500, 1K devices

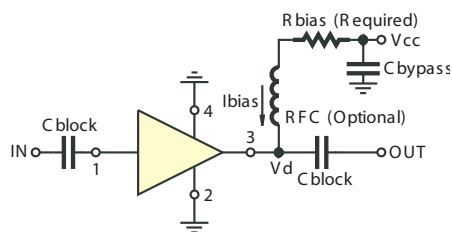
13" Reels with 2K, 4K devices

## Suggested Layout for PCB Design: PL-253

## Evaluation Board: TB-411-3+

## Environmental Ratings: ENV08T3

## Recommended Application Circuit



Test Board includes case, connectors, and components (in bold) soldered to PCB

| R BIAS |                                                |
|--------|------------------------------------------------|
| Vcc    | "1%" Res. Values (ohms)<br>for Optimum Biasing |
| 7      | 57.6                                           |
| 8      | 86.6                                           |
| 9      | 115                                            |
| 10     | 143                                            |
| 11     | 169                                            |
| 12     | 200                                            |
| 13     | 226                                            |
| 14     | 255                                            |
| 15     | 287                                            |

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**ESD Rating**

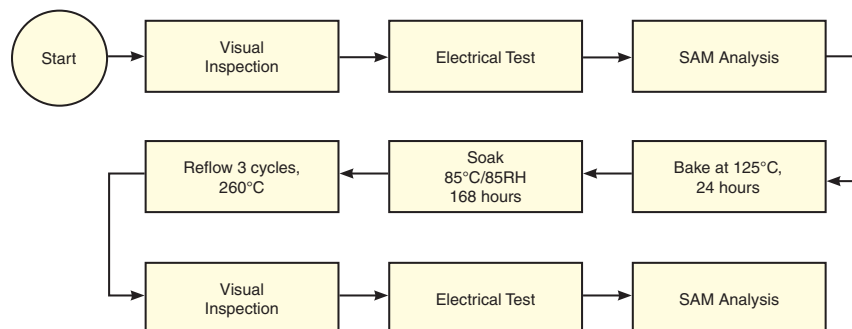
Human Body Model (HBM): Class 1B (500 v to < 1000 v) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (<100 v) in accordance with ANSI/ESD STM 5.2 - 1999

**MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDECJ-STD-020C

| No. | Test Required                   | Condition                                                                                       | Standard                       | Quantity |
|-----|---------------------------------|-------------------------------------------------------------------------------------------------|--------------------------------|----------|
| 1   | Visual Inspection               | Low Power Microscope<br>Magnification 40x                                                       | MIP-IN-0003<br>(MCT spec)      | 45 units |
| 2   | Electrical Test                 | Room Temperature                                                                                | SCD<br>(MCL spec)              | 45 units |
| 3   | SAM Analysis                    | Less than 10% growth in term of<br>delamination                                                 | J-Std-020C<br>(Jedec Standard) | 45 units |
| 4   | Moisture Sensitivity<br>Level 1 | Bake at 125°C for 24 hours<br>Soak at 85°C/85%RH for 168 hours<br>Reflow 3 cycles at 260°C peak | J-Std-020C<br>(Jedec Standard) | 45 units |

**MSL Test Flow Chart****Notes**

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