

#### Model: FXU-PC52 SERIES

## LVPECL 5 x 3.2mm 2.5V Oscillator Freq: 0.016 MHz to 670 MHz

#### **Features**



Low Cost

XPRESS Delivery

Frequency Resolution to six decimal places

Stabilities to ± 20 PPM

-20 to +70°C or -40 to +85°C operating temperatures

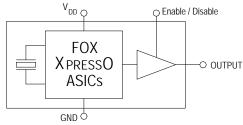
Tri-State Enable / Disable Feature

Industry Standard Package, Footprint & Pin-Out

Fully RoHS compliant

Gold over Nickel Termination Finish

Serial ID with Comprehensive Traceability



For more information -- Click on the drawing

## **Description**

The Fox XpressO-ULTRA Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. XpressO-ULTRA utilizes a family of proprietary ASICs, designed and developed by Fox, with a key focus on noise reduction technologies.

The 4<sup>th</sup> order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has the ability to select the output type and supply voltage.

With the XPRESSO-ULTRA lead-time, low cost, low noise, wide frequency range, excellent ambient performance, XPRESSO-ULTRA is an excellent choice over the conventional technologies.

Finished XPRESSO-ULTRA parts are 100% final tested.









## **Applications**

- ANY application requiring a high performance LVPECL oscillator
- SONET
- Ethernet
- Storage Area Network
- Broadband Access
- Microprocessors / DSP / FPGA
- Industrial Controllers
- Test and Measurement Equipment

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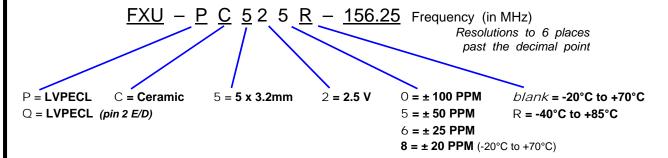
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#### Model Selection Guide & Fox Part Number

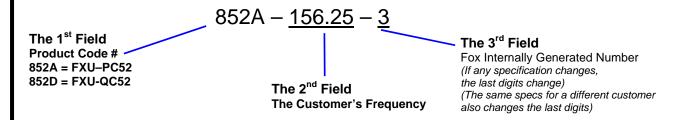
STEP #1: Customer selects the Model Description and provides to Fox Customer Service

#### **Model Description**



**STEP #2:** The Fox Customer Service team provides a customer specific Part Number for use on their Bill Of Materials (BOM).

Fox Part Number (The assigned Fox Part Number must be on the BOM – not the above Model Description) (This will ensure receipt of the proper part)



This example, FXU-PC525R-156.25 = LVPECL Output, Ceramic, 5 x 3.2mm Package, 2.5V, ±50 PPM Stability, -40 to +85°C Temperature Range, at 156.25 MHz







Electrical Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Frequency Range	Fo		0.016 MHz to 670 MHz
Frequency Stability 1		0.016 MHz to 670 MHz	100, 50, 25, 20 <sup>2</sup> PPM
	To	Standard operating 0.016 MHz to 670 MHz	-20°C to +70°C
Temperature Range		Optional operating 0.016 MHz to 670 MHz	-40°C to +85°C
	T <sub>STG</sub>	Storage	-55°C to +125°C
Supply Voltage	$V_{DD}$	Standard	2.5 V ± 5%
Input Current (@ Standard Load)	I <sub>DD</sub>	0.016 MHz to 156.25 MHz 156.25+ MHz to 400 MHz 400+ MHz to 670 MHz	102 mA 112 mA 118 mA
Output Load		Standard	50 Ohms into V <sub>DD</sub> -2V <sub>DC</sub> . TYP.
Start-Up Time	Ts		10 mS
Output Enable / Disable Time			100 nS
Moisture Sensitivity Level	MSL	JEDEC J-STD-020	1
Termination Finish			Au

Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, 1st year aging, shock and vibration.

Note 2 –. ±20 PPM stability -20°C to +70°C only.

Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Input Voltage	$V_{DD}$		-0.5V to +5.0V
Operating Temperature	T <sub>AMAX</sub>		−55°C to +85°C
Storage Temperature	T <sub>STG</sub>		−55°C to +125°C
Junction Temperature			125°C
ESD Sensitivity	HBM	Human Body Model	1 kV



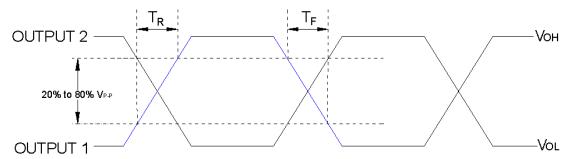




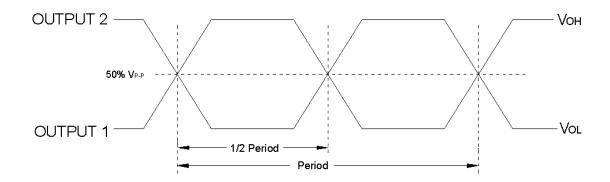
Output Wave Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Low Output Voltage	V <sub>OL</sub>	0.016 MHz to 670 MHz	0.35 V ~ 0.85 V
High Output Voltage	V <sub>OH</sub>	0.016 MHz to 670 MHz	1.1 V ~ 1.45 V
Output Symmetry  @ 50% V <sub>P-P</sub> Level (See Drawing Below)		0.016 to 156.25 MHz 156.25+ to 670 MHz	45% ~ 55% 40% ~ 60%
Output Enable <sup>1</sup> (PIN # 1) Voltage	V <sub>IH</sub>		≥ 70% V <sub>DD</sub>
Output Disable <sup>1</sup> (PIN # 1) Voltage	V <sub>IL</sub>		≤ 30% V <sub>DD</sub>
Cycle Rise Time	T <sub>R</sub>	(20%~80%)V <sub>P-P</sub>	630 pS
Cycle Fall Time	T <sub>F</sub>	(80%~20%)V <sub>P-P</sub>	630 pS

Note 1 - An optional PIN # 2 as Enable / Disable is available – see Model Selection Guide (page 2)

#### Rise Time / Fall Time Measurements

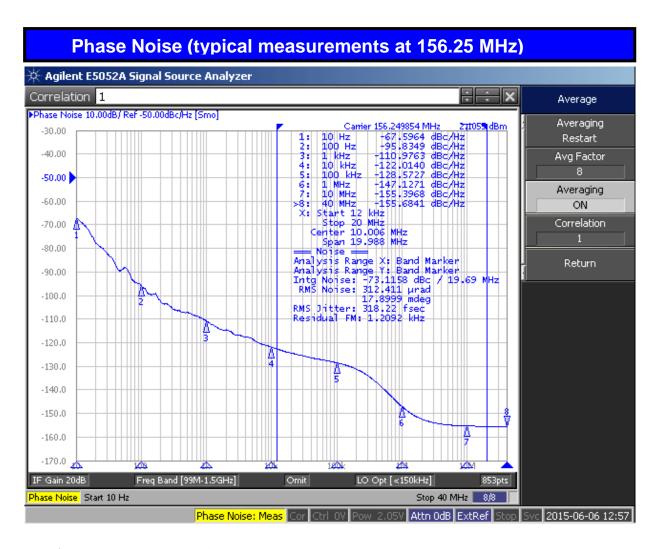


## Oscillator Symmetry









Jitter is frequency dependent. Below are typical values at 156.25 MHz.

LVPECL Phase Jitter & Time Interval Error (TIE)			
Frequency	Phase Jitter (pS) (12kHz to 20MHz)	TIE (pS) (sigma of jitter distribution	
156.25 MHz	0.32	1.6	

Phase Jitter is integrated from Agilent 5052A Signal Noise Analyzer; measured directly into 50 ohm input; V<sub>DD</sub> = 2.5V.

TIE was measured on LeCroy SDA-6000A Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software; V<sub>DD</sub> = 2.5V.

Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications)

LVPECL Random & Deterministic Jitter Composition				
Frequency	Random (Rj)	Deterministic (Dj)	Total Jitter (Tj) (14 x Rj) + Dj	
156.25 MHz	0.67	4.4	14	

Rj and Dj, measured on LeCroy SDA-6000A Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software.

Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications

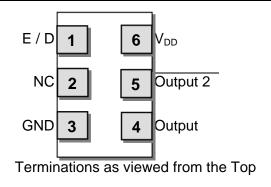




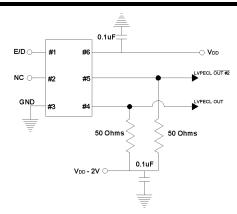
Pin Description and Recommended Circuit			
Pin #	Name	Туре	Function
1	E/D <sup>1</sup>	Input	Enable / Disable Control of Output (0 = Disabled)
2	NC <sup>2</sup>		No Connection – Leave OPEN
3	GND	Ground	Electrical Ground for V <sub>DD</sub>
4	Output	Output	LVPECL Oscillator Output
5	Output 2	Output	Differential LVPECL Output
6	V <sub>DD</sub> 3	Power	Power Supply Source Voltage

#### NOTES:

- Includes pull-up resistor to  $V_{DD}$  to provide output when the pin (1) is No Connect. (Also see note 2) An optional pin # 2 Enable / Disable is available.
- Installation should include a  $0.1\mu F$  bypass capacitor placed between  $V_{DD}$  (Pin 6) and GND (Pin 3) to minimize power supply line noise.

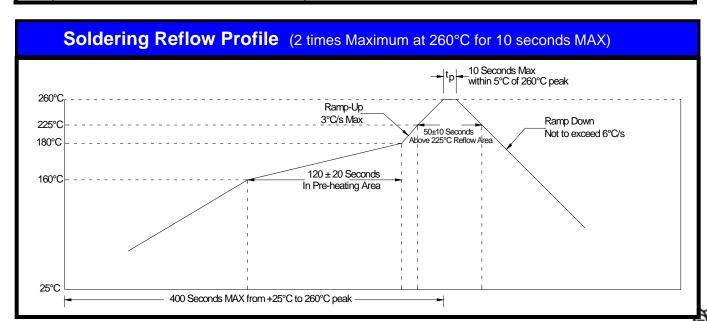


"0" Level  $V_{IL} \le 30\% V_{DD}$ 



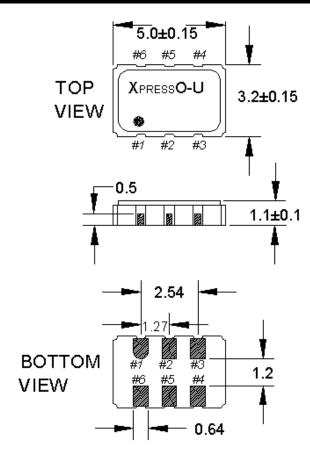
High Impedance

Enable / Disable Control	
Pin # 1 (state)	Output (Pin # 4, Pin # 5)
OPEN (No Connection)	ACTIVE Output
"1" Level V <sub>IH</sub> ≥ 70% V <sub>DD</sub>	ACTIVE Output





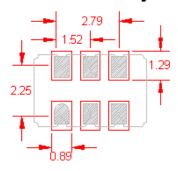
## **Mechanical Dimensional Drawing & Pad Layout**



# Actual part marking is depicted.

See **Traceability** (pg. 9) for more information

### Recommended Solder Pad Layout



#### Pin Connections

- #1) E/D\*
- #4) Output
- #2) NC\*
- #5) Output 2
- #3) GND #6) V<sub>DD</sub>

Drawing is for reference to critical specifications defined by size measurements.

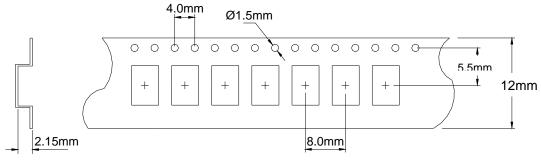
Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary



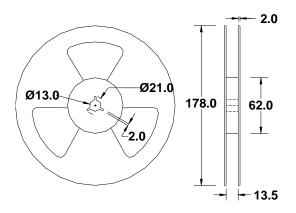
An optional PIN # 2 as Enable / Disable is available – see Model Selection Guide (page 2)



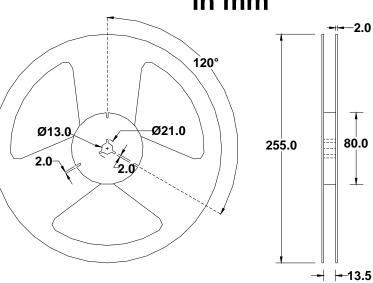
## **Tape and Reel Dimensions**



# 1k Reel Dimensions in mm



# 2k Reel Dimensions in mm



## Labeling (Reels and smaller packaging are labeled with the below)

SKU 852A-156.25-3

TEX

Xpress0 

Covered by one or nore of listed

U.S. Patents: 6,664,860, 5,966,403
5,960,405 5,952,809 6,188,290

BATE COD 1 3 3 5

Pb-Free
RoHS Compliant
Category (e4)

Philippines Patent: 1-1998-00024

US and Foreign Patents Pending

Xpress0 0 is a Registered Tradenark of Fox Electronics

Visual Foreign Patents Pending

Xpress0 0 is a Registered Tradenark of Fox Electronics





### Traceability - LOT Number & Serial Identification

#### **LOT Number**

The LOT Number has direct ties to the customer purchase order. The LOT Number is stored internally in non-volatile memory. XPRESSO-ULTRA parts are packaged in Cut Tape or Tape and Reel and placed in an ESD bag. The package label containing the Lot number is affixed to the ESD bag, and in the case of Tape and Reel it is attached to the reel as well

It is recommended that the XPRESSO-ULTRA parts remain in this ESD bag during storage for protection and identification.

If the parts become separated from the label showing the LOT Number, it can be retrieved from inside one of the parts, and the information that can be obtained is listed below:

- Customer Purchase Order Number
- Internal Fox Sales Order Number
- Dates that the XPRESSO-ULTRA part was shipped from the factory
- The assigned customer part number
- The specification that the part was designed for

#### Serial Identification

The Serial ID is the individualized information about the configuration of that particular XPRESSO-ULTRA part. The Serial ID is unique for each and every XPRESSO-ULTRA part, and can be read by special Fox equipment.

With the Serial ID, the below information can be obtained about that individual, XPRESSO-ULTRA part:

- Equipment that the XPRESSO-ULTRA part was configured on
- Raw material used to configure the XPRESSO-ULTRA part
- Traceability of the raw material back to the foundries manufacturing lot
- Date and Time that the part was configured
- Any optimized electrical parameters based on customer specifications
- Electrical testing of the actual completed part
- Human resource that was monitoring the configuration of the part

Fox has equipment placed at key Fox locations World Wide to read the Lot Identification and Serial Number of any XPRESSO-ULTRA part produced and can then obtain the information from above within 24 hours







## **Mechanical Testing**

Parameter	Test Method
Mechanical Shock	MIL-STD-202 Method 213 Condition C
Mechanical Vibration	MIL-STD-202 Method 204 5g's for 20 minutes 12 cycles of each 3 orientations: X, Y, Z
High Temperature Operating Life (HTOL)	Under Power @ 125°C for 1000 Hours
Hermetic Seal	MIL-STD-202F: Method 112D Test condition C He pressure: 4 ±1 kgf / cm <sup>2</sup> 2 Hour soak





#### **FXU-PC52 Series**

#### XpressO-ULTRA Home

XpressO-ULTRA XOs

**XpressO Brochure** 

#### Patent Numbers:

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The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

