

PECL OUTPUT THROUGH HOLE OSCILLATOR (20.8 x 13.2 x 7.24mm)

FEATURES:

- WIDE FREQUENCY RANGES
- COMPLEMENTARY OUTPUT
- STANDARD PACKAGE

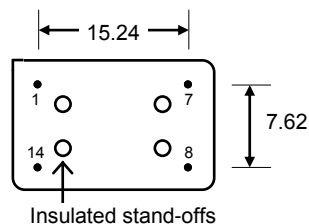
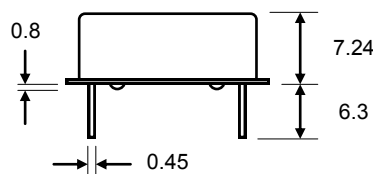
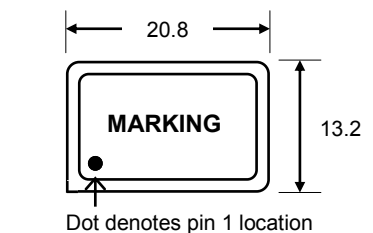


SPECIFICATION

FREQUENCY RANGE		0.75MHz ~ 800.0MHz
FREQUENCY STABILITY		±10ppm ~ ±100ppm
OPERATING TEMPERATURE RANGE		STD: 0°C ~ 70°C (See P/N guide for other options)
STORAGE TEMPERATURE RANGE		-55°C ~ 105°C
SUPPLY VOLTAGE ±5%		3.3V _{DC} , 5.0V _{DC}
SUPPLY CURRENT		24 MHz ~ 800 MHz 25mA max. ~ 100mA max.
OUTPUT	LOGIC FAMILY	PECL
	LOAD	50Ω (into V _{DD} - 2.0V _{DC})
	LEVEL LOGIC '1' (V _{OH})	V _{DD} - 1.025V _{DC} min.
	LEVEL LOGIC '0' (V _{OL})	V _{DD} - 1.620V _{DC} max.
	SYMMETRY (DUTY CYCLE)	45% / 55%
STAND-BY VOLTAGE *	RISE AND FALL TIME (Tr/Tf)	1.0nS max.
	ENABLE (V _{IL})	0.8V _{DC} max.
	DISABLE (V _{IH})	2.0V _{DC} min. or N/C
AGING @ 25°C ± 3°C		±3.0ppm / 1 st year max.
PHASE JITTER RMS (12kHz ~ 20MHz)		1.0pS max.

* Disable (V_{IL}) / Enable (V_{IH}) is also available.

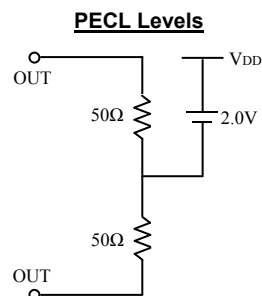
PACKAGE DIMENSIONS



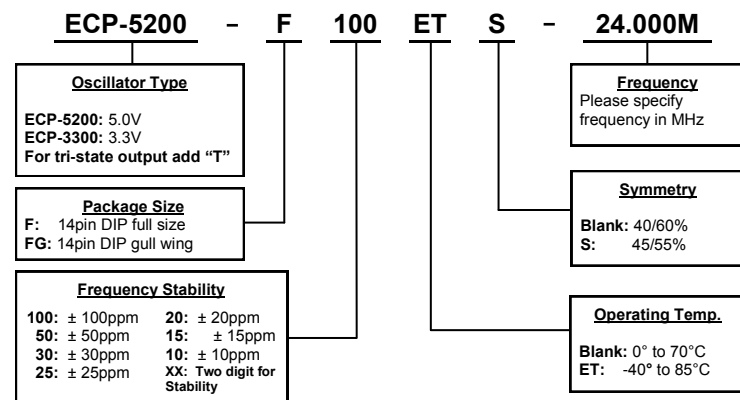
Unit : mm

Pin Configuration	
1	C.Output / En/Dis / N.C.
7	GND
8	OUTPUT
14	V _{DD}

TEST CIRCUIT



PART NUMBERING GUIDE



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