

SMA Transfer Switch 转换开关

自保持型 DC-18GHZ

RF SPEC.

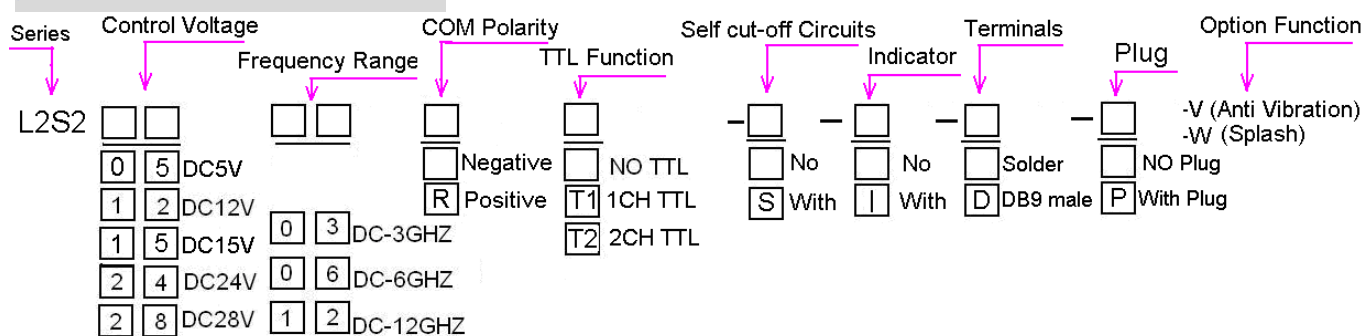
Frequency Range	DC-3GHZ	3-6GHZ	6-12GHZ	12-18GHZ
VSWR	<1.20	<1.30	<1.40	<1.50
Insertion Loss	<0.2dB	<0.3dB	<0.4dB	<0.5dB
Isolation	>80dB	>70dB	>65dB	>60dB
Power Handling(CW)	85W	60W	50W	30W

*承载功率条件: VSWR=1.1, 25℃, sea level, cold switching

- . RF Impedance: 50 ohm
- . Operating Voltage: DC12/460mA(DC24V/185mA)
- . Switching Time: <20ms
- . Life:>1000000 cycles
- . Switching Sequence:Break Before Make
- . Operating Temperature: -30 ~ +65℃
- . RF Interface: SMA female



产品编号定义:



产品编号举例

L2S21206-I (自保持型转化开关/DC12V(公共端负极)/DC-6GHZ/SMA female/带指示/焊线式接线)

L2S22403R-D-P (自保持转换开关/DC24V(公共端正极)/DC-3GHZ/SMA female/DB9 公头接线/带支架)

L2S21512T2-S-I-D-P-V (自保持转换开关/DC15V/DC-12GHZ/SMA female/2 路 TTL 控制/带自关断电路
带指示/DB9 公头接线/带支架/抗振动)

L2S22806T1-S-D-P (自保持型转换开关/DC28V/DC-6GHZ/SMA female/1 路 TTL 控制/带自关断电路
DB9 公头接线/带支架)

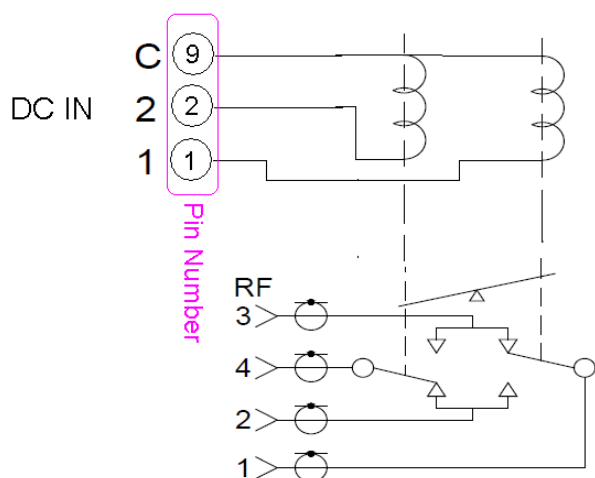
说明 1) 带 TTL 控制功能的产品对公共端电压极性无定义

2) 1 路 TTL 控制 (带自关断电路) 产品功能与 Failsafe Type (失效保护型) 带 TTL 控制的产品一致



控制原理图

电压控制型



C –电压控制公共端 (Pin9)

(电压控制端在订货时需定义极性)

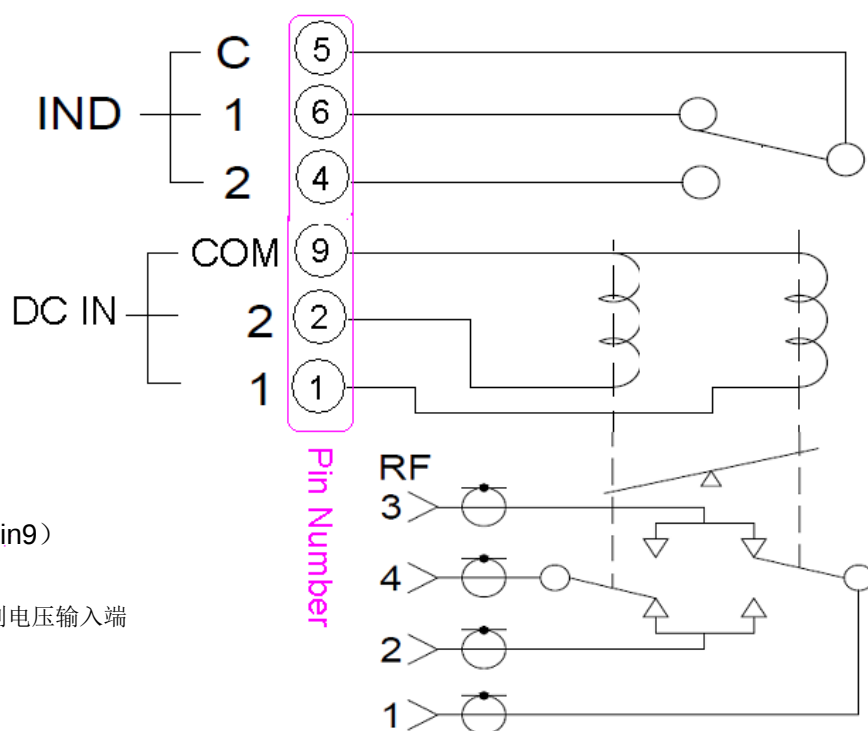
1 (Pin1) ,2 (Pin2) 控制电压输入端

DC IN	RF ON	RF OFF
1-C=V,2-C=0V	J1-J3 J2-J4	J1-J2 J3-J4
1-C=0V,2-C=V	J1-J2 J3-J4	J1-J3 J2-J4

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电压控制型带指示功能



电压控制部分:

DC IN COM 是电压控制公共端 (Pin9)

(电压控制端在订货时需定义极性)

DC IN 1 (Pin1) ,DC IN 2 (Pin2) 是控制电压输入端

IND (指示) 部分:

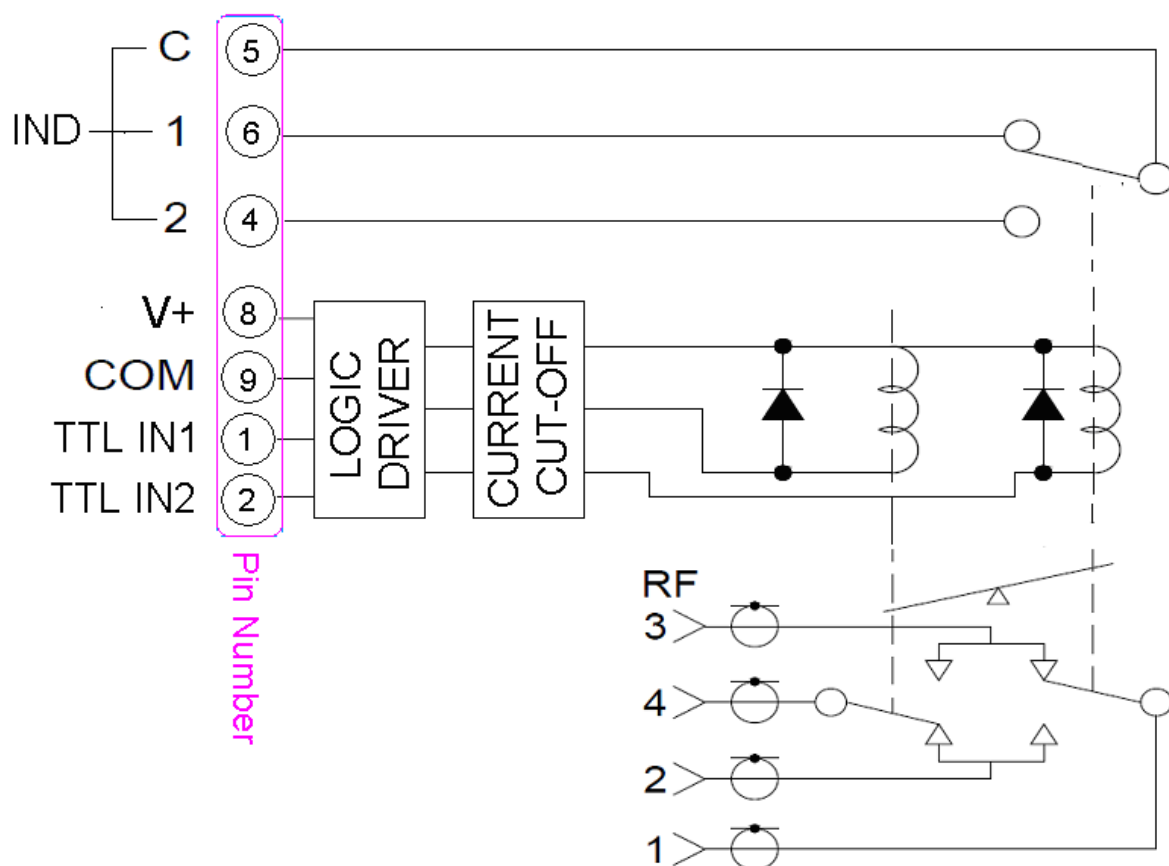
C (Pin5) 是指示功能的公共端

1 (Pin6) 是“1”端指示

2 (Pin4) 是“2”端指示

DC IN	RF ON	RF OFF	IND	
1-C=V,2-C=0V	J1-J3 ; J2-J4	J1-J2 ; J3-J4	C-1 ON	C-2 OFF
1-C=0V,2-C=V	J1-J2 ; J3-J4	J1-J3 ; J2-J4	C-1 OFF	C-2 ON

2 路 TTL 控制带指示:



TTL IN	RF ON	RF OFF	IND	
TTL IN1="1";TTL IN2="0"	J1-J3 ; J2-J4	J1-J2 ; J3-J4	C-1 ON	C-2 OFF
TTL IN1="0";TTL IN2="1"	J1-J2 ; J3-J4	J1-J3 ; J2-J4	C-1 OFF	C-2 ON

Logic "1" =2.5-5.0V

Logic "0" =0-0.8V

管脚定义. :

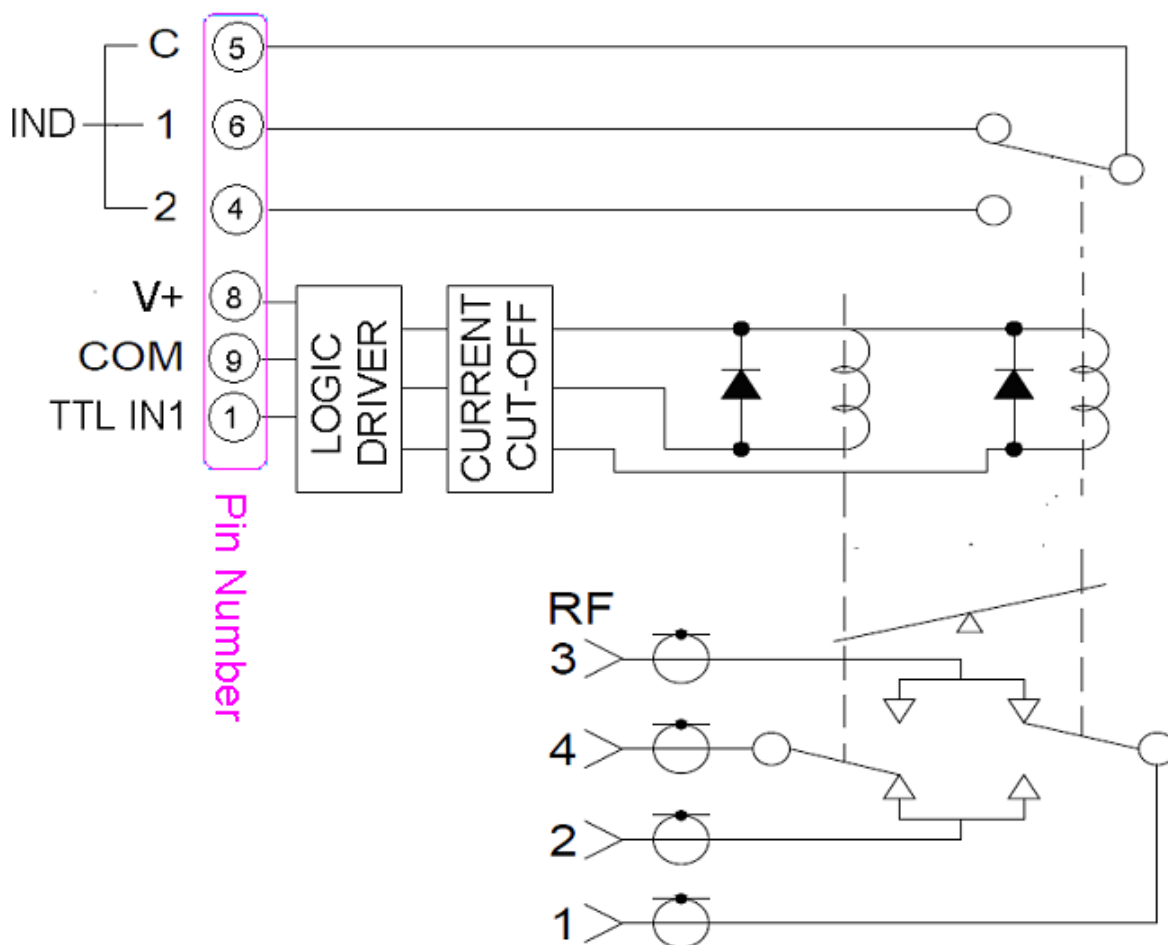
Pin1,Pin2:TTL 逻辑控制输入

Pin9: TTL 逻辑电平及控制电压的公共端（地）

Pin8:控制电压正极 输入端

Pin4,5,6:指示功能端口

1 路 TTL 控制带指示:



TTL IN	RF ON	RF OFF	IND	
TTL IN1="1";TTL IN2="0"	J1-J3 ; J2-J4	J1-J2 ; J3-J4	C-1 ON	C-2 OFF
TTL IN1="0";TTL IN2="1"	J1-J2 ; J3-J4	J1-J3 ; J2-J4	C-1 OFF	C-2 ON

Logic "1" =2.5-5.0V

Logic "0" =0-0.8V

管脚定义. :

Pin1:TTL 逻辑控制输入

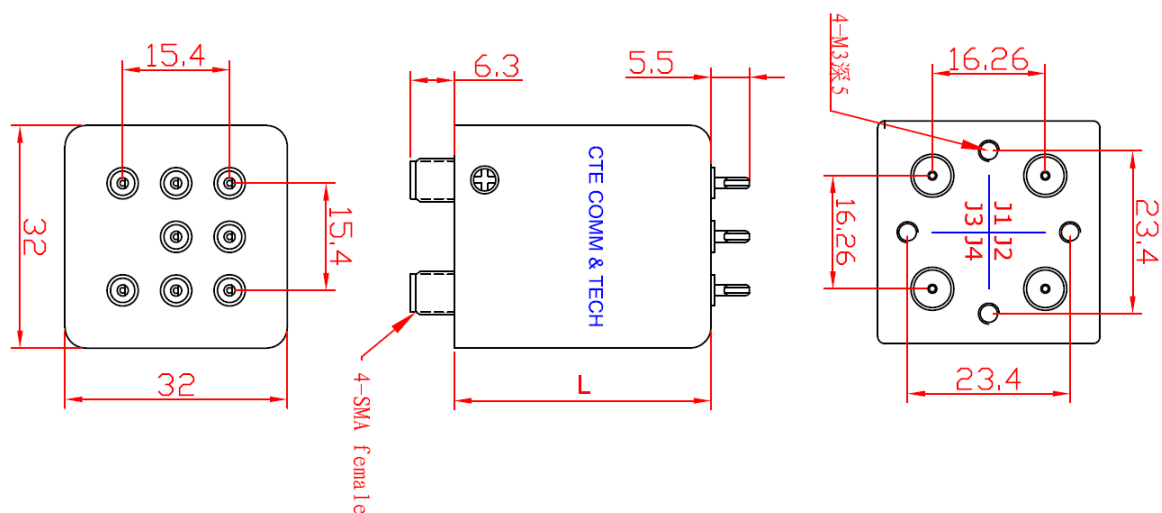
Pin9: TTL 逻辑电平及控制电压的公共端（地）

Pin8:控制电压正极 输入端

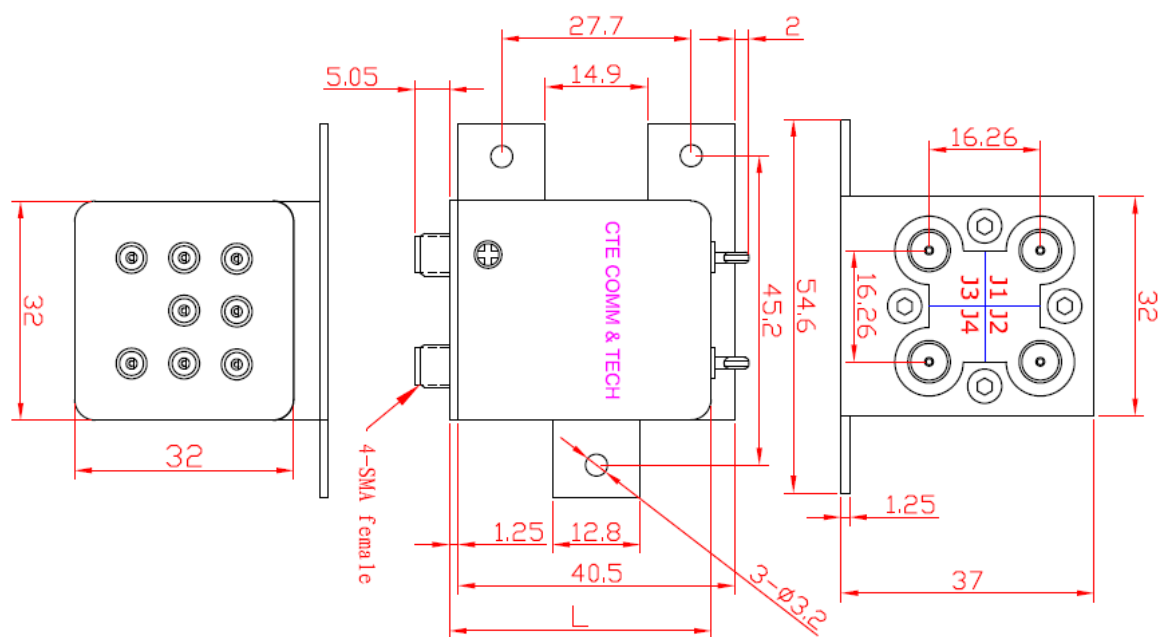
Pin4,5,6:指示功能端口

焊线式接线外型图(mm)

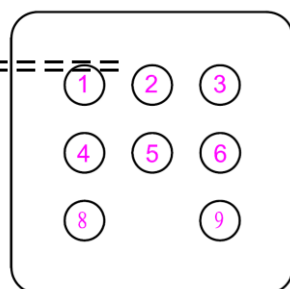
无支架 L=37mm(NO TTL Control) L=46mm(With TTL Control)



带支架 L=37mm(NO TTL Control) L=46mm(With TTL Control)

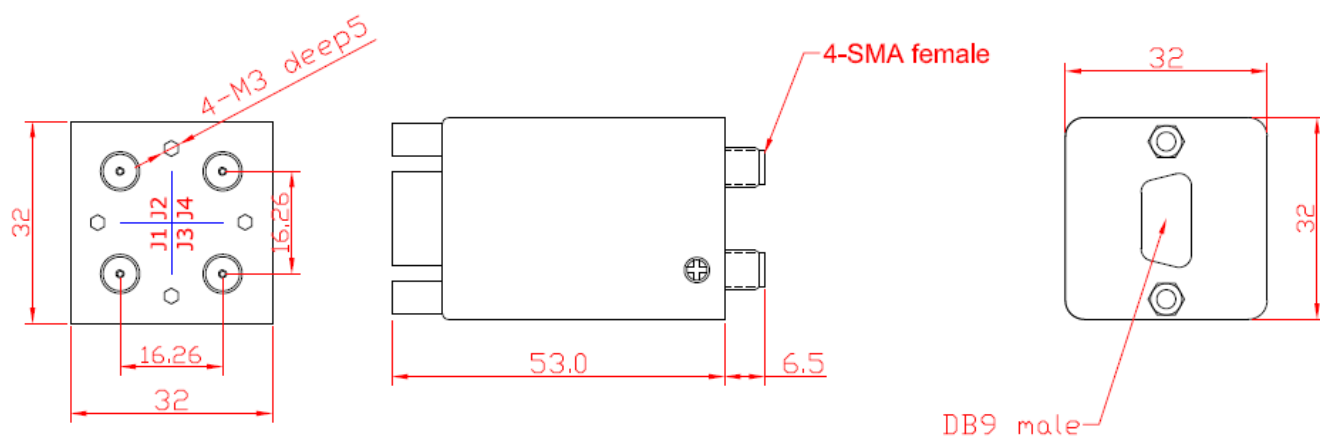


焊线式接线管脚顺序图



DB9 公头接线外型图 Outline(mm)

无支架



带支架

