

Chip Attenuators

PCAAW Series: DC-18 GHz

The PCAAW Series Chip Attenuators consist of

a laser trimmed distributed thin film element on

an alumina ceramic substrate with a protective

paint layer and solderable terminals.



FEATURES

- Solderable terminals
- SN62, SN96, Gold terminals available
- 0-20 dB values
- S-parameter data files available
- RoHS Compliant
- Design kits available

GENERAL SPECIFICATIONS

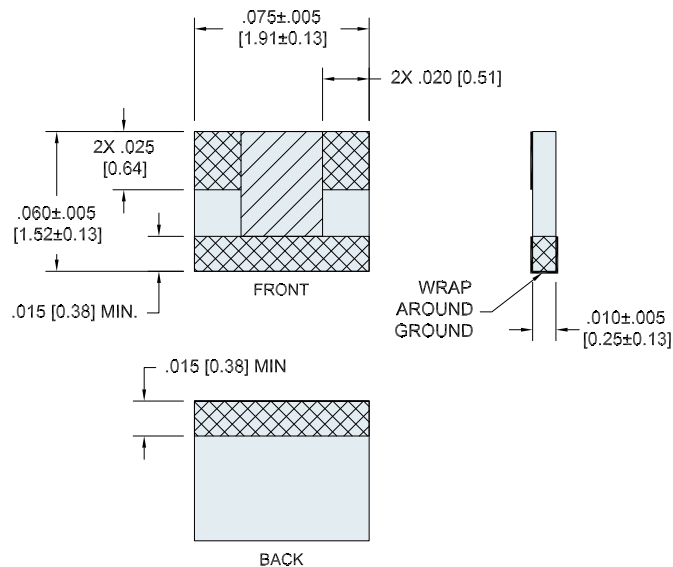
Resistive Element: Proprietary Thin Film

Substrate: Alumina

Terminal Finish: Electroplated Silver over Nickel

Attenuation Stability: ± 0.0001 dB/dB/°C

Operating Temperature Range: -55°C to +150°C



ELECTRICAL SPECIFICATIONS

Model Number	dB Value	Attenuation Accuracy ¹ (dB)				Max VSWR			Power Rating ² (W)
		DC-4GHz	4-8GHz	8-12.4GHz	12.4-18GHz	DC-4GHz	4-8GHz	8-18GHz	
PCAAW - 0	0	+0.5/-0	+0.5/-0	+0.5/-0	+0.5/-0	1.25	1.35	1.50	4.0
PCAAW - 1	1	± 0.5	± 0.5	± 0.5	± 0.5	1.25	1.35	1.50	4.0
PCAAW - 2	2	± 0.5	± 0.5	± 0.5	± 0.5	1.25	1.35	1.50	2.0
PCAAW - 3	3	± 0.5	± 0.5	± 0.5	± 0.5	1.25	1.35	1.50	2.0
PCAAW - 4	4	± 0.5	± 0.5	± 0.5	± 0.75	1.25	1.35	1.50	2.0
PCAAW - 5	5	± 0.5	± 0.5	± 0.5	± 0.75	1.25	1.35	1.50	2.0
PCAAW - 6	6	± 0.5	± 0.5	± 0.5	± 0.75	1.25	1.35	1.50	1.0
PCAAW - 7	7	± 0.5	± 0.5	± 0.75	± 1.00	1.25	1.35	1.50	1.0
PCAAW - 8	8	± 0.5	± 0.5	± 0.75	± 1.00	1.25	1.35	1.50	1.0
PCAAW - 9	9	± 0.5	± 0.5	± 0.75	± 1.00	1.25	1.35	1.50	1.0
PCAAW - 10	10	± 0.5	± 0.5	± 0.75	± 1.00	1.25	1.35	1.50	1.0
PCAAW - 12	12	± 0.75	+0.5/-3	+0.5/-4		1.25	1.35	1.50	0.75
PCAAW - 15	15	± 0.75	+0.5/-3	+0.5/-4		1.25	1.35	1.50	0.75
PCAAW - 20	20	± 1.00	+0.5/-4			1.25	1.35	1.50	0.75

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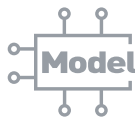
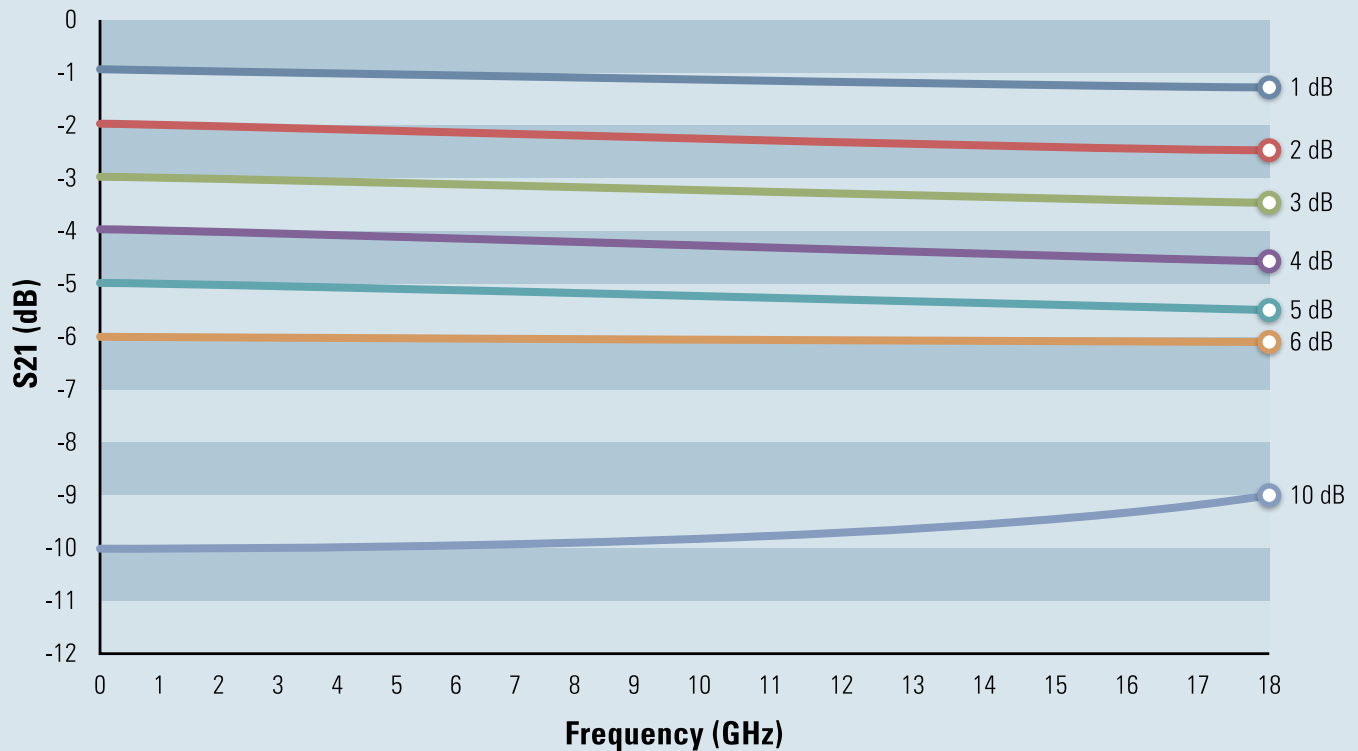
¹Performance is based on device mounted in matched 50Ω line. ²Assumes 100° C mounting temperature derated linearly to 0 Watts at 150° C, mounted circuit side up or down

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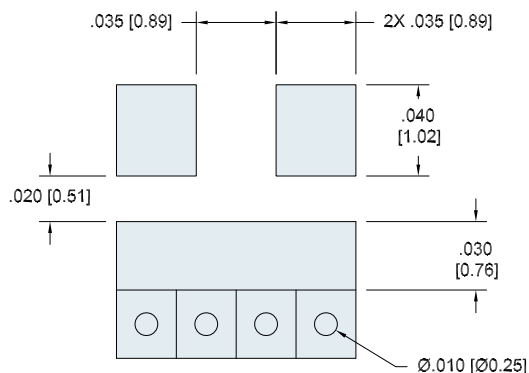
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ATTENUATION VS. FREQUENCY



PC BOARD LAYOUT



ORDERING KEY

Model Number

PCAAW-T3 — dB Value

Terminal Options

- = Silver over Nickel [standard]
- T = Tin Lead Solder
- H = Lead Free Solder
- G = Gold Plated



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