

High Frequency SMT 5.0 x 7.0mm LVPECL VCXO Models: V752 / V762

CONNOR WINFIELD



2111 Comprehensive Drive

Aurora, Illinois 60505

Phone: 630-851-4722

Fax: 630-851-5040

www.conwin.com

US Headquarters:

630-851-4722

European Headquar-

ters: +353-61-472221

Description

The Connor-Winfield Models V752 and V762 are 3.3V. Surface Mount 5.0x7.0mm, Voltage Controlled Crystal Oscillator (VCXO) with LVPECL Differential outputs and enable/disable function. The V752 and V762 are designed for use with applications utilizing a PLL system requiring very high frequency and low jitter. The surface mount package is designed for high-density mounting and is optimum for mass production.



Features:

- 3.3V Operation
- Absolute Pull Range (APR): ± 50 ppm
- Temperature Range: -40° to 85°C
- Differential LVPECL Outputs
- Low Jitter 0.3ps RMS Typical
- Enable / Disable Function:
Models: V752: Enable Low
Models: V762: Enable High
- 5.0x7.0mm Surface Mount Package
- Tape and Reel Packaging
- RoHS Compliant / Lead Free

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	$^{\circ}\text{C}$	
Supply Voltage (Vcc)	-0.5	-	4.6	Vdc	
Control Voltage (Vc)	-0.5	-	Vcc+0.5	Vdc	

Operating Specifications

Parameter	Minimum	Nominal	Maximum	Units	Notes
Center Frequency (Fo)	25	-	204.8	MHz	
Operating Temperature Range	-40	-	85	$^{\circ}\text{C}$	
Supply Voltage (Vcc)	3.135	3.3	3.465	Vdc	
Supply Current (Icc)	-	-	100	mA	
Period Jitter RMS	-	3	5	ps RMS	
Integrated Phase Jitter (BW=12kHz to 20MHz)	-	0.3	1.0	ps RMS	
Typical Phase Noise for 155.52MHz					
SSB Phase Noise at 10Hz offset	-	-60	-	dBc/Hz	
SSB Phase Noise at 100Hz offset	-	-90	-	dBc/Hz	
SSB Phase Noise at 1kHz offset	-	-120	-	dBc/Hz	
SSB Phase Noise at 10kHz offset	-	-140	-	dBc/Hz	
SSB Phase Noise at 100kHz offset	-	-145	-	dBc/Hz	

Input Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range (Vc)	0.3	1.65	3.0	Vdc	
Typical Slope (Fo = 155.52MHz @Vc=1.65V)	-	75	-	ppm/V	
Absolute Pull Range (APR)	± 50	-	-	ppm	1
Monotonic Linearity	-10	-	10	%	
DC Input Resistance (Pad 1)	-	60K	-	Ohm	
Modulation Bandwidth (3dB)	25	-	-	kHz	
Model V752 Enable / Disable Function					
Enable Input Voltage - Low (Vil)	-	-	1.68	Vdc	2
Disable Input Voltage - High (Vih)	2.275	-	-		
Model V762 Enable / Disable Function					
Enable Input Voltage - High (Vih)	2.275	-	-	Vdc	2
Disable Input Voltage - Low (Vil)	-	-	1.68		

LVPECL Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	-	50	Ohms	3
Voltage High (Voh)	2.275	-	-	Vdc	
Low (Vol)	-	-	1.68		
Duty Cycle at 50% Level	45	50	55	%	
Rise / Fall Time 20% to 80%	-	0.6	1.5	nS	

Package Characteristics

Package	Hermetically sealed, ceramic package with grounded metal cover.
Soldering Process	RoHS compliant / lead free, see solder profile on page 2.

Notes:

1. Absolute pull range (APR) is the minimum guaranteed pull range of the VCXO under all conditions over the lifetime operation. Including calibration @ 25°C , frequency vs. change in temperature, frequency vs. change in supply voltage, frequency vs. change in load, shock and vibration and aging for ten years. The APR is referenced to Fo. Positive Transfer Function.
2. Outputs are enabled with no connection on pad 2. When oscillator is disabled both outputs are in a high impedance state.
3. 50 ohm termination into Vcc-2V or Thevein equivalent.



Ordering Information

V 7	5	2	155.25M
Type	Temperature Range	APR and Supply Voltage	Output Frequency
LVPECL VCXO	-40 to 85°C	2 = ±50 ppm	Frequency Format
5.0x7.0mm	Enable / Disable Function	3.3 Vdc	-xxx.xM Minimum*
Package	5 = Enable = Low		-xxx.xxxxxM Maximum*
	6 = Enable = High		*Amount of numbers after the decimal point.
			M = MHz

Example: To order an V752 with an output frequency of:
25 MHz = V752-025.0M
44.736 MHz = 752-044.736M
155.52 MHz = V752-155.52M

Enable / Disable Function

Models: V752

Enable / Disable Function (Pad 2)

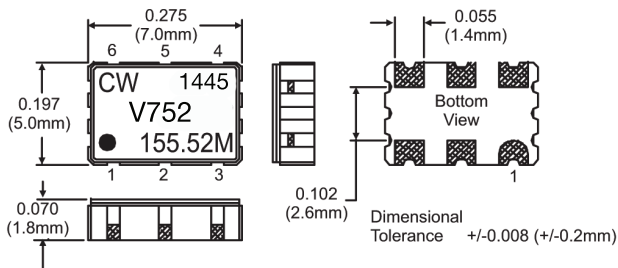
Enable / Disable Function (Pad 2)	Output
No Connection	Enable
Low	Enable
High	Disable (High Impedance)

Models: V762

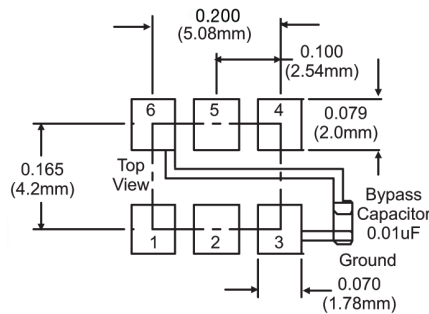
Enable / Disable Function (Pad 2)

Enable / Disable Function (Pad 2)	Output
No Connection	Enable
High	Enable
Low	Disable (High Impedance)

Package Layout



Suggested Pad Layout

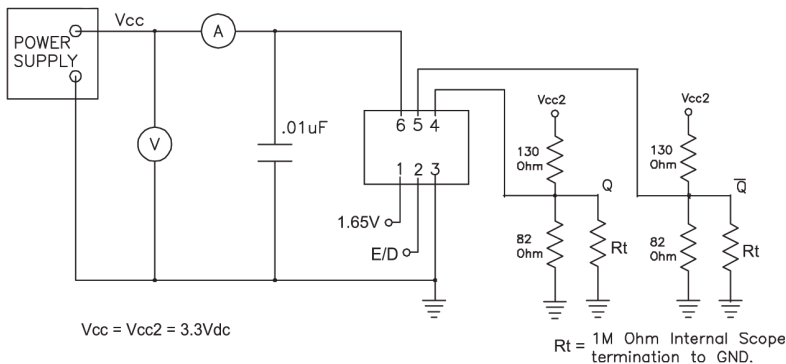


Pad Connections

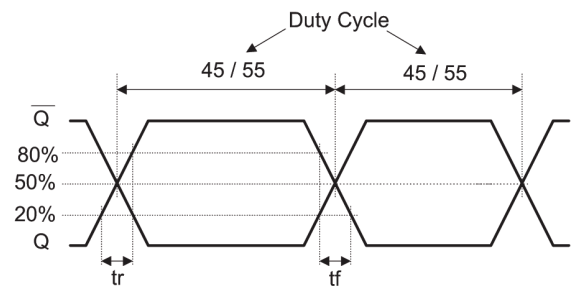
Pad Pad Connection

1:	Control Voltage
2:	Enable / Disable
3:	Ground (Case)
4:	Output Q
5:	Output Q̄
6:	Vcc

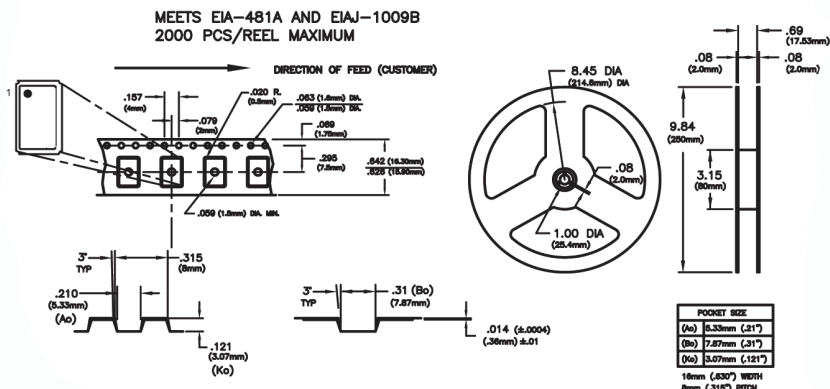
Test Circuit



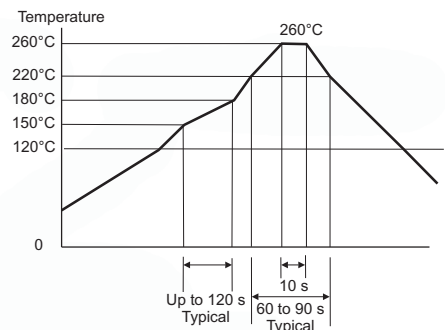
Output Waveform



Tape and Reel Dimensions



Solder Profile



Meets IPC/JEDEC J-STD-020C

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