



TAI-SAW TECHNOLOGY CO., LTD.

No. 3, Industrial 2nd Rd., Ping-Chen Industrial District,
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Product Specifications Approval Sheet

Product Description: 43.75MHz 6MHz BW SMD 13.1x7.15mm SAW Filter

TST Part No.: TB1021A

Customer Part No.: _____

Customer signature required
Company: _____
Division: _____
Approved by : _____
Date: _____

Checked by: _____ Michael Yang *Michael*

Approved by: _____ Francis Chen *[Signature]*

Date: _____ 2012/4/5

1. Customer signed back is required before TST can proceed with sample build and receive orders.
2. Orders received without customer signed back will be regarded as agreement on the specifications.
3. Any specifications changes must be approved upon by both parties and a new revision of specifications shall be released to reflect the changes.



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IF SAW Filter 43.75 MHz

MODEL NO.: TB1021A

REV. NO.:1

A. FEATURES:

1. IF Filter for Digital Cable TV

RoHS Compliant
Lead free
Lead-free soldering

B. MAXIMUM RATING:

1. DC voltage: 12 V.
2. AC voltage: 10 V.
3. Storage Temperature: -40°C to +85 °C.
4. Operabl Temperature: -20°C to +65 °C.

Electrostatic Sensitive Device (ESD)

C. ELECTRICAL CHARACTERISTICS: Reference Temperature: 25(45) °C

Characteristic	Units	Minimum	Typical	Maximum
Center frequency, F_o	MHz	43.68	43.75	43..82
Insertion Loss, IL	dB	13	14.8	16
Bandwidth -3dB	MHz		5.9	
Bandwidth -30dB	MHz		7.6	
Relative attenuation				
41.28(41.22)MHz	dB	-0.8	0.3	1.4
46.34(46.28)MHz	dB	-0.8	0.3	1.4
40.81(40.75)MHz	dB	1.5	2.7	4.0
46.81(46.75)MHz	dB	1.5	2.7	4.0
39.81(39.75)MHz	dB	37	50	
47.81(47.75)MHz	dB	35	48	
Group Delay Ripple (p-p) 40.81~46.81(40.75 ~ 46.75)MHz	nS		45	
Lower Sidelobe 35.06~38.81 (35.00 ~ 38.75)MHz	dB	40	47	
Lower Sidelobe 38.81~39.81(38.75 ~ 39.75)MHz	dB	37	44	
Upper Sidelobe 47.81~48.81(47.75 ~ 48.75)MHz	dB	35	42	
Upper Sidelobe 48.81~55.06(48.75 ~ 55.00)MHz	dB	40	42	
Temp.coeff.	ppm/K		-72	
Input/Output Impedance	kΩ pF	Zin:1.2 16.6 ; Zout = 1.2 4.6pF		

D.FREQUENCY CHARACTERISTICS:

TB1021A

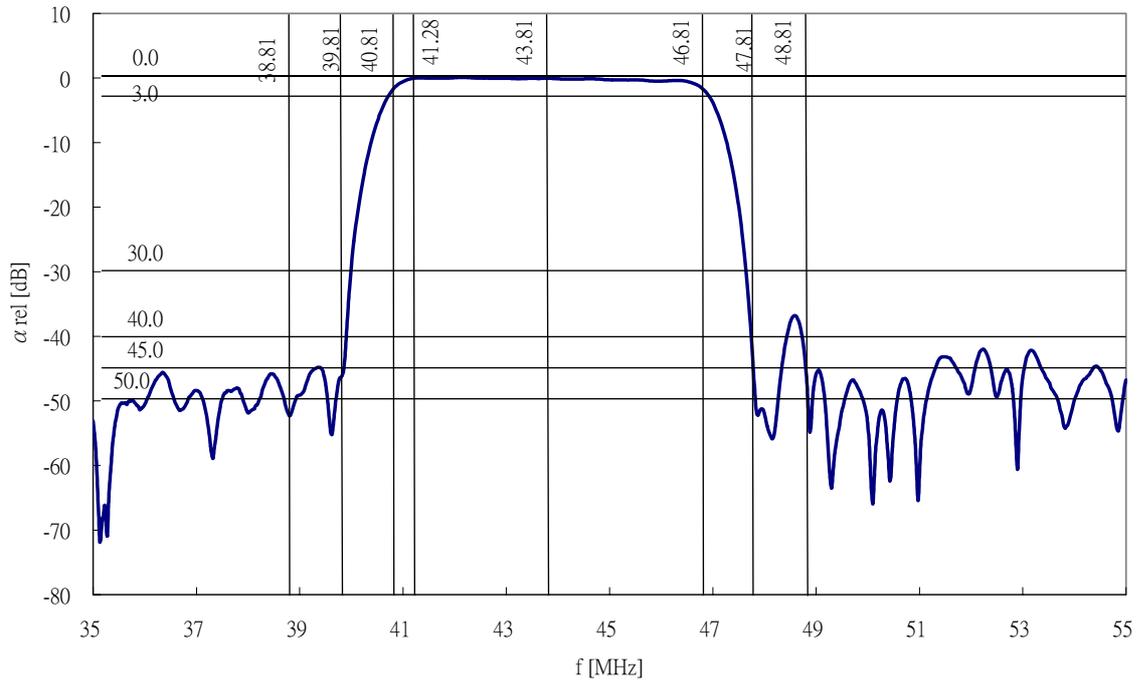


Fig.1 S21 Response (Fo=43.75MHz/Span=20MHz)

TB1021A

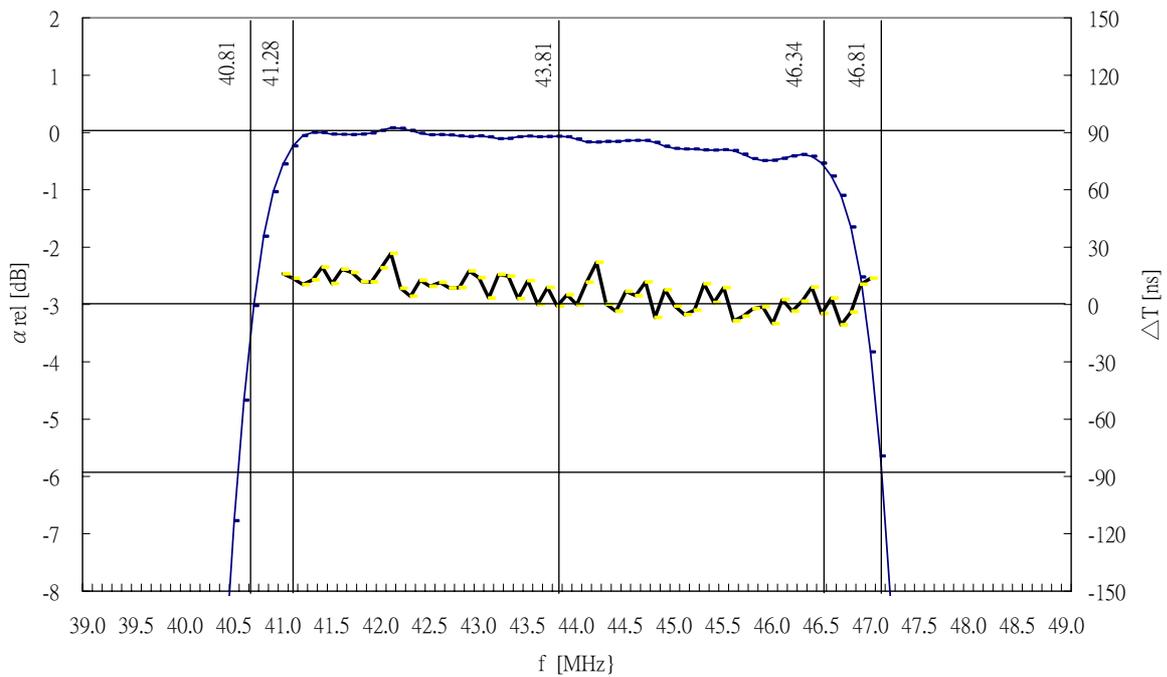


Fig.2 Group Delay

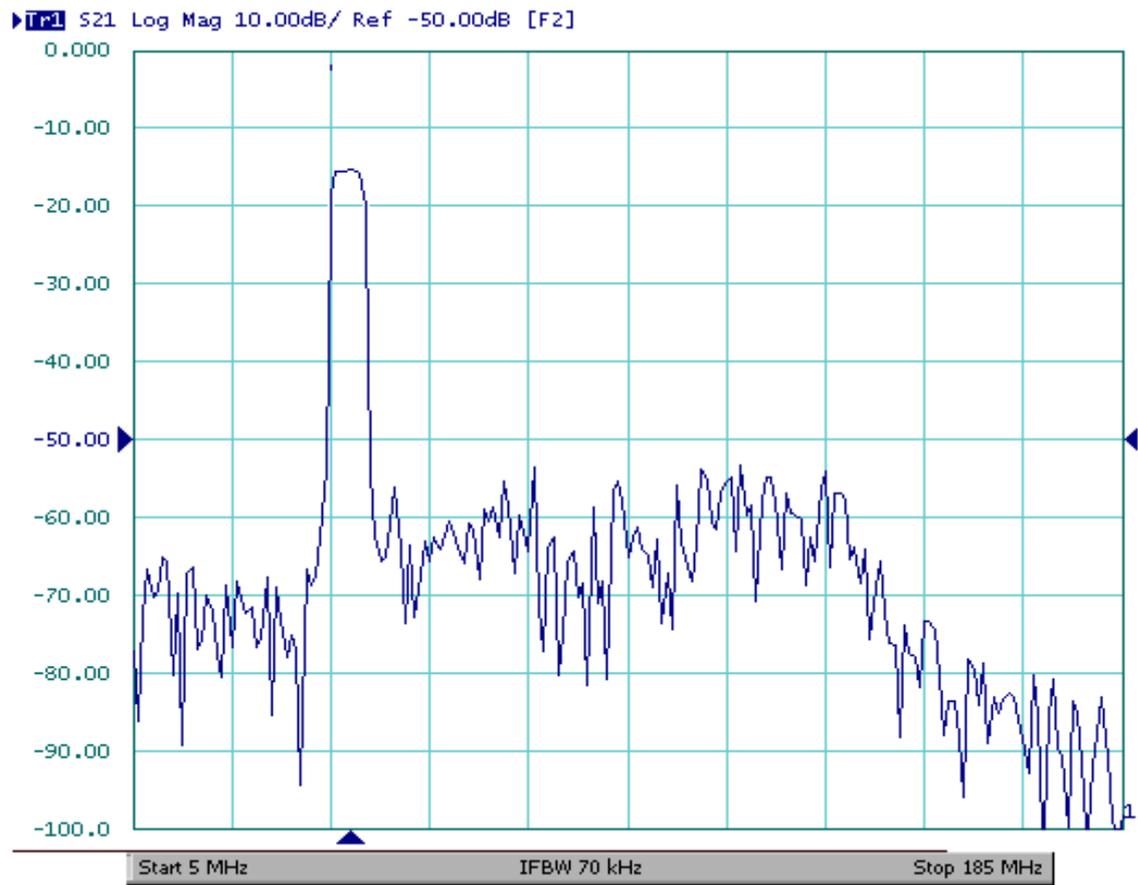


Fig.3 S21 Response (Fo=43.75MHz/Span=180MHz)

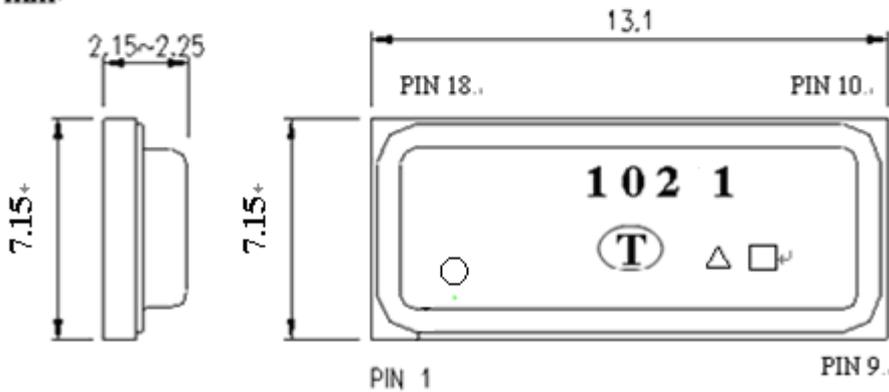
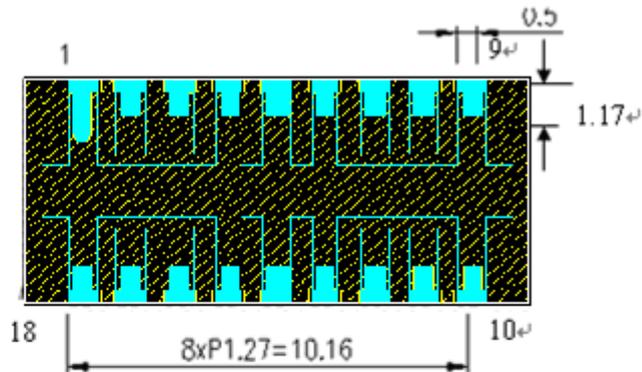
E. OUTLINE DRAWING:

- Pin 17: Input
- Pin 16: Input/GND
- Pin 1,4,5,6,9,10,13,14,15,18: GND
- Pin 7,8: Output
- Pin 2,3,11,12 : Not connect

△ : Date Code for Year

□ : Date Code for Week

Unit : mm



□ Data code : See the table

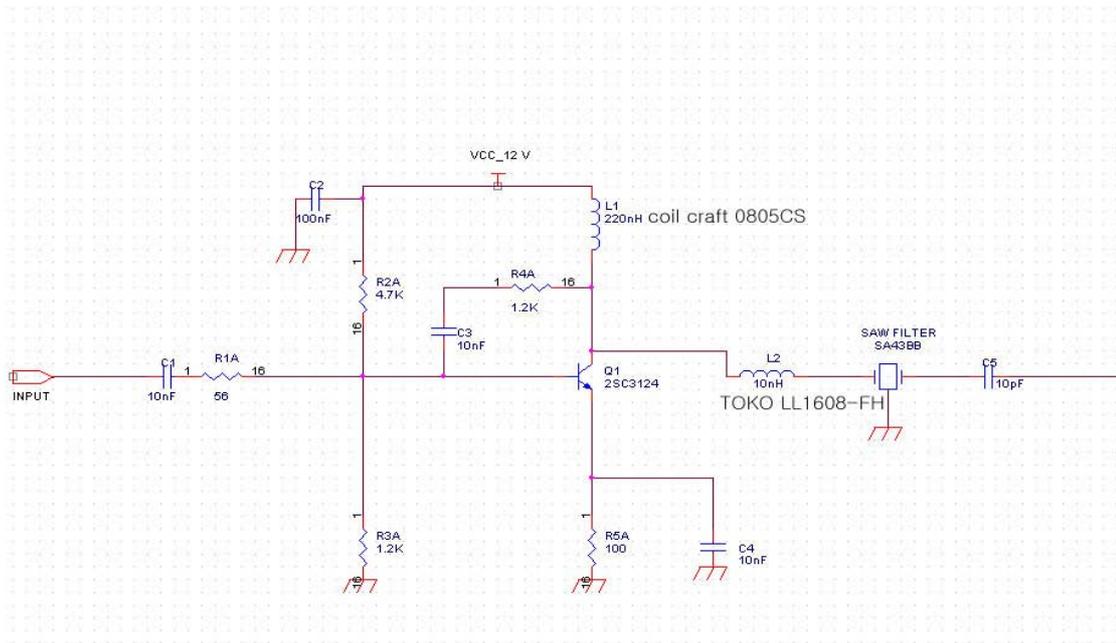
Date Code Table

WK01	WK02	WK03	WK04	WK05	WK06	WK07	WK08	WK09	WK10	WK11	WK12	WK13
A	B	C	D	E	F	G	H	I	J	K	L	M
WK14	WK15	WK16	WK17	WK18	WK19	WK20	WK21	WK22	WK23	WK24	WK25	WK26
N	O	P	Q	R	S	T	U	V	W	X	Y	Z
WK27	WK28	WK29	WK30	WK31	WK32	WK33	WK34	WK35	WK36	WK37	WK38	WK39
a	b	c	d	e	f	g	h	i	j	k	l	m
WK40	WK41	WK42	WK43	WK44	WK45	WK46	WK47	WK48	WK49	WK50	WK51	WK52
n	o	p	q	r	s	t	u	v	w	x	y	z

△ : Product / Year Code

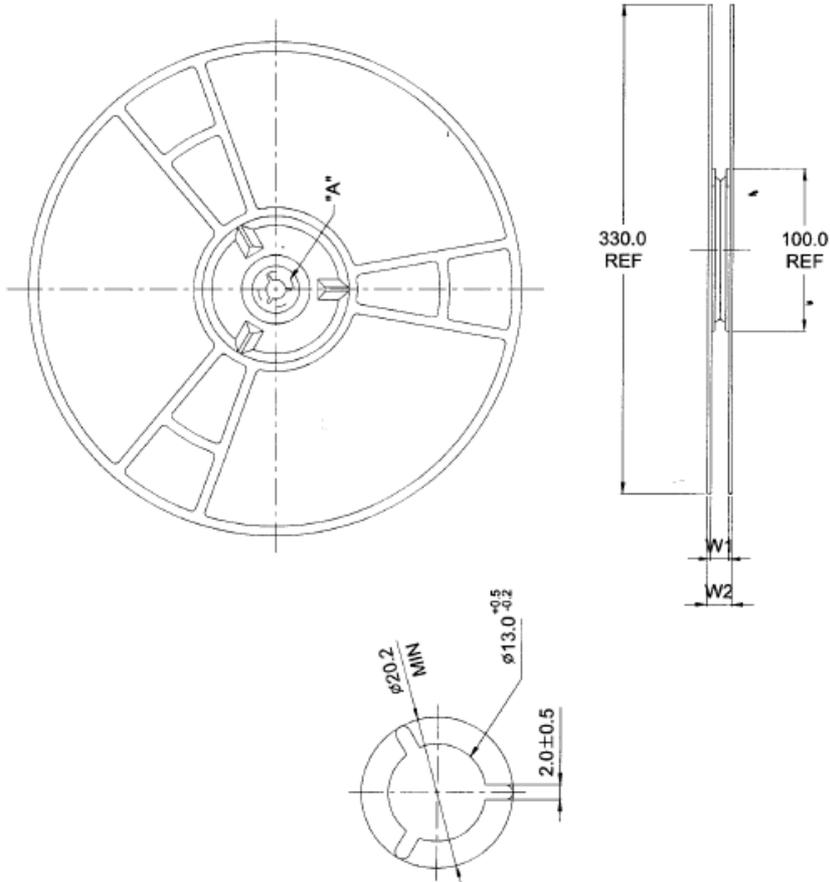
Year	2013 2009	2014 2010	2015 2011	2016 2012
Product Code	B	b	<u>B</u>	<u>b</u>

F. TEST CIRCUIT

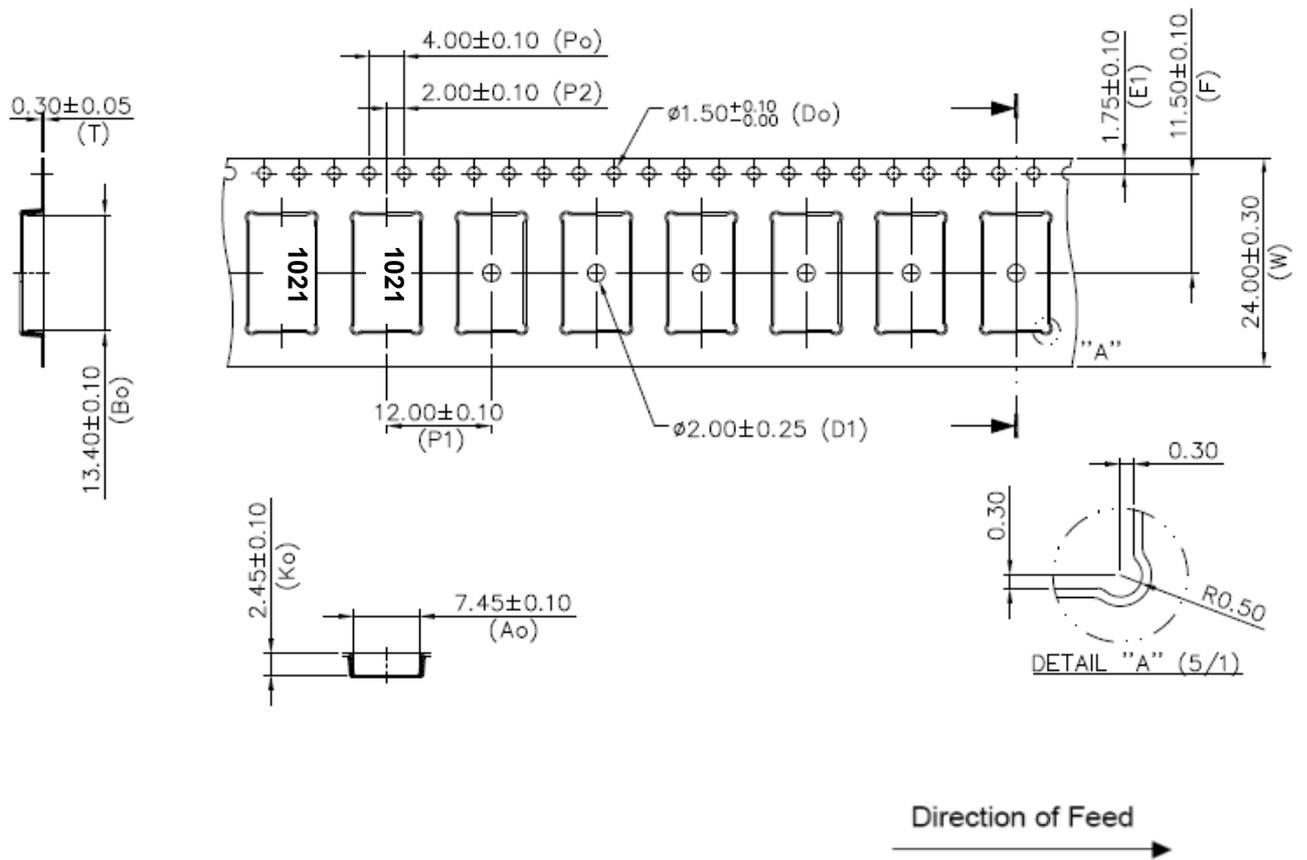


G. PACKING:

1. REEL DIMENSION



2. TAPE DIMENSION



H. RECOMMENDED REFLOW PROFILE :

