# Ultra-Small, Low-Power, Window Comparator in 4 UCSP and 5 SOT23

## **General Description**

The MAX9065 is an ultra-small, low-power, window comparator ideal for a wide variety of portable electronics applications such as cell phones, portable media players, and notebooks that have extremely tight board space and power constraints. It comes in both a 4-bump UCSP<sup>TM</sup> package with a 1mm x 1mm footprint (as small as two 0402 resistors) and a 5-pin SOT23 package.

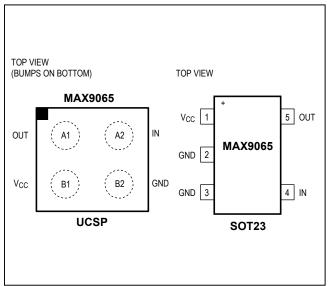
The MAX9065 features a common-mode input range of -0.3V to +5.5V independent of supply voltage. The input current goes to zero when the MAX9065 is powered down ( $V_{CC}=0$ ). Additionally, the MAX9065 features high RF immunity.

The MAX9065 has a push-pull output and consumes only  $1\mu A$  (max) supply current. The MAX9065 operates down to 1.0V over the extended -40°C to +85°C temperature range.

# **Applications**

- Cell Phones
- Portable Media Players
- Electronic Toys
- Notebook Computers
- Portable Medical Devices

# **Pin Configurations**



UCSP is a trademark of Maxim Integrated Products, Inc.

#### **Features**

- Tiny 1mm x 1mm 4-Bump UCSP
  - Footprint = Two 0402 Resistors
  - Also Available in 5-Pin SOT23 Package
- Ultra-Low Power Operating Current
  - 1μA (max)
- -0.3V to +5.5V Input Voltage Range
- 1.0V to 5.5V V<sub>CC</sub> Range
- 3.0V and 4.2V Trigger Points
- -40°C to +85°C Extended Temperature Range

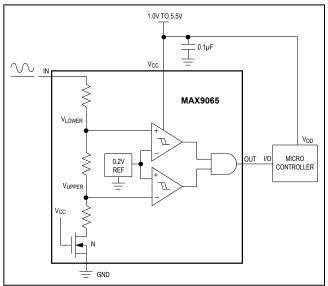
## **Ordering Information**

| PART             | PIN-PACKAGE | TOP MARK |
|------------------|-------------|----------|
| MAX9065EBS+G45   | 4 UCSP      | AGC      |
| MAX9065EUK+      | 5 SOT23     | AFFL     |
| MAX9065AEWS+TCNB | 4 WLP       | AGO      |

**Note:** All devices are specified over the extended -40°C to +85°C operating temperature range.

G45 = Protective die coating.

# **Typical Operating Circuit**





<sup>+</sup>Denotes a lead-free/RoHS-compliant package.

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# **Absolute Maximum Ratings**

| V <sub>CC</sub> , IN to GND0.3V to +          | Operating Temperature Range40°C to +85°C              |
|---|---|
| OUT to GND0.3V to (V <sub>CC</sub> + 0.3      | V) Junction Temperature+150°C                         |
| Output Short-Circuit Current Duration         | Os Storage Temperature Range65°C to +150°C            |
| Input Current into Any Terminal±20            | nA Lead Temperature (SOT23 only, soldering 10s)+300°C |
| Continuous Power Dissipation                  | Soldering Temperature (reflow)+260°C                  |
| 4-Bump UCSP (derate 3.0mW/°C above +70°C)238n | W   |
| 5-Pin SOT23 (derate 3.9mW/°C above +70°C)312n | W   |
| 4-Bump WLP (derate 3mW/°C above +70°C)915n    | W   |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## **Electrical Characteristics**

( $V_{CC}$  = 3.3V,  $T_A$  = -40°C to +85°C. Typical values are at  $T_A$  = +25°C, unless otherwise noted.) (Note 1)

| PARAMETER                        | SYMBOL               | CONDITIONS  |                                | MIN                   | TYP  | MAX                 | UNITS          |
|----------------------------------|----------------------|---|--------------------------------|-----------------------|------|---------------------|----------------|
| DC CHARACTERISTICS               |                      |   |                                |                       |      |                     |                |
|                                  |                      | MAX9065EBS+<br>4 UCSP   | T <sub>A</sub> = +25°C         | 4.158                 | 4.20 | 4.242               | 30<br>36<br>42 |
|                                  |                      |   | -40°C < T <sub>A</sub> < +85°C | 4.10                  |      | 4.30                |                |
| Upper Threshold Voltage          | UTV                  | MAX9065EUK+   | T <sub>A</sub> = +25°C         | 4.04                  | 4.20 | 4.36                |                |
| Opper Threshold Voltage          | 010                  | 5 SOT23   | -40°C < T <sub>A</sub> < +85°C | 3.98                  |      | 4.42                |                |
|                                  |                      | MAX9065AEBS+  | T <sub>A</sub> = +25°C         | 1.152                 | 1.20 | 1.248               |                |
|                                  |                      | 4 UCSP  | -40°C < T <sub>A</sub> < +85°C | 1.132                 |      | 1.268               |                |
|                                  |                      | MAX9065EBS+   | T <sub>A</sub> = +25°C         | 2.94                  | 3.00 | 3.06                |                |
|                                  |                      | 4 UCSP  | -40°C < T <sub>A</sub> < +85°C | 2.92                  |      | 3.08                |                |
| Lower Threshold Voltage          | LTV                  | MAX9065EUK+   | T <sub>A</sub> = +25°C         | 2.88                  | 3.00 | 3.12                | V              |
| Lower Threshold Voltage          | LIV                  | 5 SOT23   | -40°C < T <sub>A</sub> < +85°C | 2.83                  |      | 3.17                |                |
|                                  |                      | MAX9065AEBS+<br>4 UCSP  | T <sub>A</sub> = +25°C         | 0.576                 | 0.60 | 0.624               |                |
|                                  |                      |   | -40°C < T <sub>A</sub> < +85°C | 0.566                 |      | 0.634               |                |
| Input Voltage Range              | V <sub>IN</sub>      |   |                                | -0.3                  |      | +5.5                | V              |
| Hysteresis                       | V <sub>HYS</sub>     | (Note 2)  |                                |                       | ±1.0 |                     | %              |
| Resistor String Input Resistance | R <sub>IN</sub>      |   |                                | 5.8                   | 11   | 17.7                | МΩ             |
| Input Shutdown Current           | I <sub>IN_SHDN</sub> | $V_{CC} = 0V, V_{IN} = 5.$  | 5V                             |                       | 1    | 15                  | nA             |
|                                  | V <sub>OL</sub>      | $I_{SINK}$ = 100 $\mu$ A, $V_{CC}$ = 1V, $T_{A}$ = +25 $^{\circ}$ C |                                |                       |      | 0.2                 |                |
| Output Voltage Low               |                      | I <sub>SINK</sub> = 1.2mA, V <sub>CC</sub> = 3.3V                   |                                |                       |      | 0.3                 | V              |
|                                  |                      | I <sub>SINK</sub> = 1.2mA, V <sub>CC</sub> = 5.5V                   |                                |                       |      | 0.5                 |                |
|                                  | V <sub>OH</sub>      | $I_{SOURCE} = 25\mu A, V_{CC} = 1V, T_A = +25^{\circ}C$             |                                |                       | ٧    | <sub>CC</sub> - 0.2 | V              |
| Output Voltage High              |                      | I <sub>SOURCE</sub> = 0.3mA, V <sub>CC</sub> = 3.3V                 |                                |                       | ٧    | <sub>CC</sub> - 0.3 |                |
|                                  |                      | I <sub>SOURCE</sub> = 0.75mA, V <sub>CC</sub> = 5.5V                |                                | V <sub>CC</sub> - 0.5 |      |                     |                |
| AC CHARACTERISTICS               |                      |   |                                |                       |      |                     |                |
| Propagation Delay                | t <sub>PD</sub>      | Overdrive = ±100mV (Notes 3, 4)                                     |                                | 25                    |      | μs                  |                |
| Fall Time                        | t <sub>F</sub>       | C <sub>L</sub> = 10pF   |                                |                       | 14   |                     | ns             |
| Rise Time                        | t <sub>R</sub>       | C <sub>L</sub> = 10pF   |                                |                       | 30   |                     | ns             |

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## **Electrical Characteristics (continued)**

( $V_{CC}$  = 3.3V,  $T_A$  = -40°C to +85°C. Typical values are at  $T_A$  = +25°C, unless otherwise noted.) (Note 1)

| PARAMETER                    | SYMBOL          | CONDITIONS   | MIN | TYP | MAX  | UNITS |
|------------------------------|-----------------|--|-----|-----|------|-------|
| POWER SUPPLY                 |                 |  |     |     |      |       |
| Supply Voltage               | V <sub>CC</sub> | Guaranteed by V <sub>OS</sub> tests                    | 1   |     | 5.5  | V     |
| Consider Comment             | lcc             | V <sub>CC</sub> = 5.5V                                 |     | 0.7 | 1.35 | μА    |
| Supply Current               |                 | V <sub>CC</sub> = 1.0V, T <sub>A</sub> = +25°C         |     | 0.6 | 1.0  |       |
| Power-Supply Rejection Ratio | PSRR            | V <sub>CC</sub> = 0.9V to 5.5V, T <sub>A</sub> = +25°C | 40  | 53  |      | dB    |
| Power-Up Time                | ton             |  |     | 3   |      | ms    |

Note 1: All devices are 100% production tested at T<sub>A</sub> = +25°C. Temperature limits are guaranteed by design.

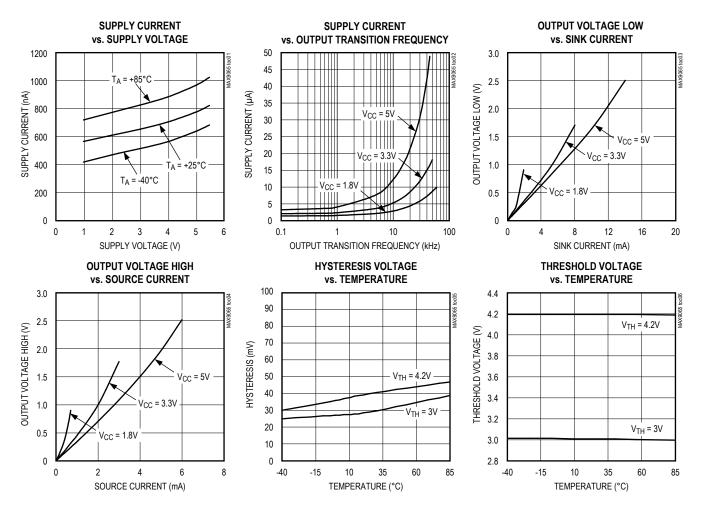
Note 2: Hysteresis is the input voltage difference between the two switching points.

Note 3: Overdrive is defined as the voltage above or below the average of the switching points.

Note 4: Guaranteed by ATE and/or bench characterization over temperature.

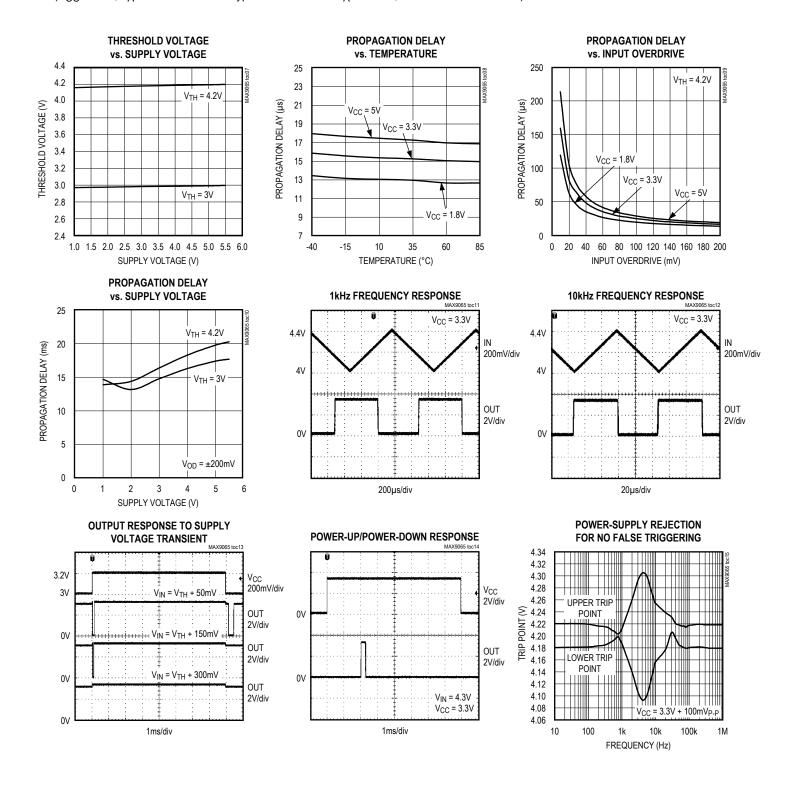
## **Typical Operating Characteristics**

(V<sub>CC</sub> = 3.3V,  $T_A$  = -40°C to +85°C. Typical values are at  $T_A$  = +25°C, unless otherwise noted.)



# **Typical Operating Characteristics (continued)**

( $V_{CC}$  = 3.3V,  $T_A$  = -40°C to +85°C. Typical values are at  $T_A$  = +25°C, unless otherwise noted.)



## **Pin Description**

| PI    | N    | NAME            | FUNCTION   |  |
|-------|------|-----------------|--|--|
| SOT23 | UCSP | NAIVIE          |  |  |
| 1     | B1   | V <sub>CC</sub> | External Supply Input. Bypass to ground with a 0.1µF bypass capacitor. |  |
| 2, 3  | B2   | GND             | Ground   |  |
| 4     | A2   | IN              | Window Comparator Input  |  |
| 5     | A1   | OUT             | Push-Pull Output   |  |

## **Detailed Description**

The MAX9065 is an extremely small window comparator designed for compact, low-current applications, featuring a supply current of less than  $1\mu$ A (max).

#### **MAX9065 Operation**

At the heart of the MAX9065 are two comparators, a resistor-divider with a disconnect switch, a 200mV reference, digital logic circuitry, and an output stage (see the *Typical Operating Circuit*).

The digital logic circuitry and the output stage together behave like an AND gate. The gate's inputs are the outputs of the two comparators. When either comparator's output is low, the output asserts low. When both comparator's outputs are high, the output asserts high.

When power is applied to  $V_{CC}$ , the n-channel FET at the bottom of the resistor-divider is turned on. The resistor-divider provides two voltages,  $V_{UPPER}$  and  $V_{LOWER}$ , for comparison with an internal 0.2V reference voltage. When the input voltage exceeds 4.2V,  $V_{UPPER}$  is greater than 0.2V, causing the output to assert low. When the input voltage falls below 3.0V,  $V_{LOWER}$  is less than 0.2V, causing the output also to assert low. With the input voltage between 3.0V and 4.2V, the output asserts high, indicating that the input voltage is within the desired range. Table 1 summarizes the operation of the MAX9065.

When  $V_{CC}$  goes to 0V, the n-channel FET is turned off, eliminating the resistor-divider as a leakage path for current.

**Table 1. MAX9065 Operation** 

| INPUT VOLTAGE                 | OUTPUT |
|-------------------------------|--------|
| V <sub>IN</sub> > 4.2V        | Low    |
| 3.0V < V <sub>IN</sub> < 4.2V | High   |
| V <sub>IN</sub> < 3.0V        | Low    |

### **Table 2. MAX9065A Operation**

| INPUT VOLTAGE                 | OUTPUT |
|-------------------------------|--------|
| V <sub>IN</sub> > 1.2V        | Low    |
| 0.6V < V <sub>IN</sub> < 1.2V | High   |
| V <sub>IN</sub> < 0.6V        | Low    |

#### **MAX9065A Operation**

The resistor-divider provides two voltages,  $V_{UPPER}$  and  $V_{LOWER}$ , for comparison with an internal 0.2V reference voltage. When the input voltage exceeds 1.2V,  $V_{UPPER}$  is greater than 0.2V, causing the output to assert low.

When the input voltage falls below 0.6V,  $V_{LOWER}$  is less than 0.2V, causing the output also to assert low. With the input voltage between 0.6V and 1.2V, the output asserts high, indicating that the input voltage is within the desired range. Table 2 summarizes the operation of the MAX9065A.

#### **Applications**

The MAX9065 is designed specifically to monitor the voltage on a single lithium battery. Keeping the voltage on a lithium battery within a tight range is important to prevent damage to the battery. Specifically, ensuring that the battery's voltage neither exceeds 4.2V nor falls below 3.0V lengthens the lifetime of the battery and avoids any hazardous battery conditions.

#### **Hysteresis**

There are four trip points for hysteresis. See Figure 1.

### **Power-Supply Considerations**

Bypass  $V_{CC}$  with a  $0.1\mu F$  capacitor to ground.

# **Chip Information**

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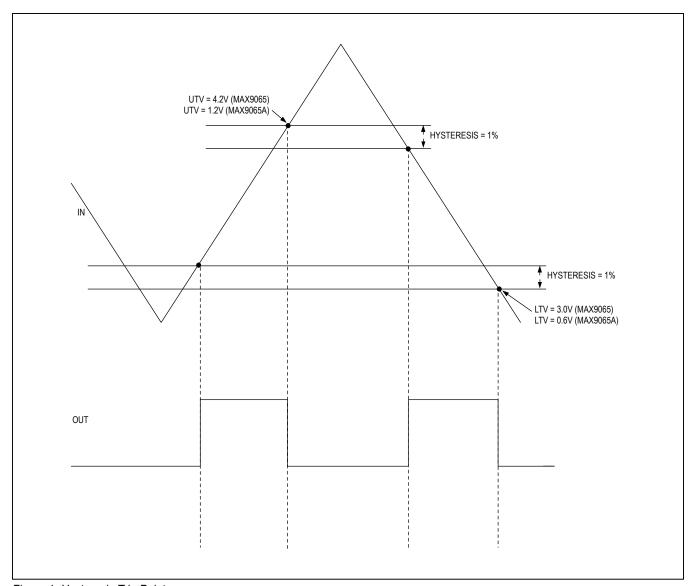
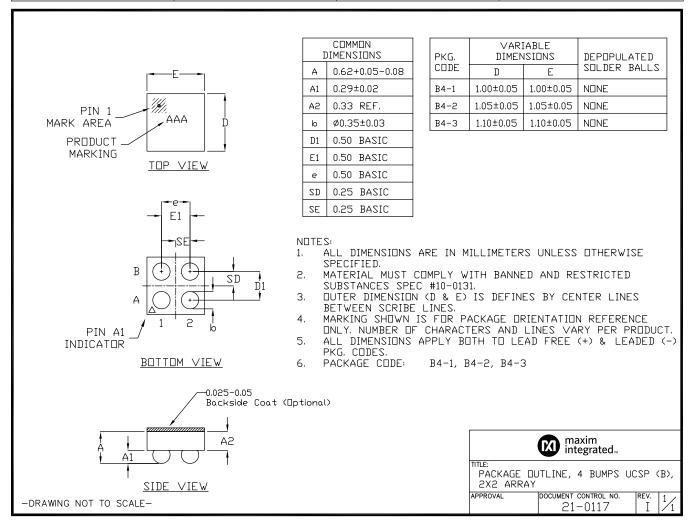


Figure 1. Hysteresis Trip Points

## **Package Information**

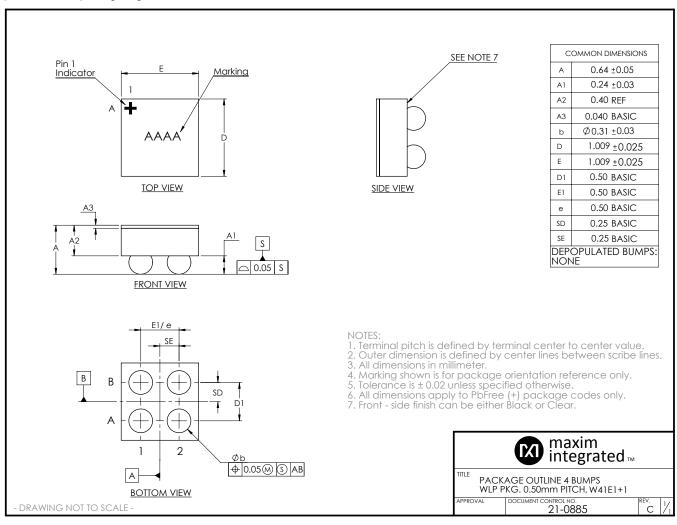
For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

| PACKAGE TYPE | PACKAGE CODE | OUTLINE NO.    | LAND<br>PATTERN NO.            |
|--------------|--------------|----------------|--------------------------------|
| 5 SOT23      | U5+2         | <u>21-0057</u> | <u>90-0174</u>                 |
| 4 UCSP       | B4+1         | <u>21-0117</u> | Refer to Application Note 1891 |
| 4 WLP        | W41E1+1      | <u>21-0885</u> | <u>90-0366</u>                 |



## **Package Information (continued)**

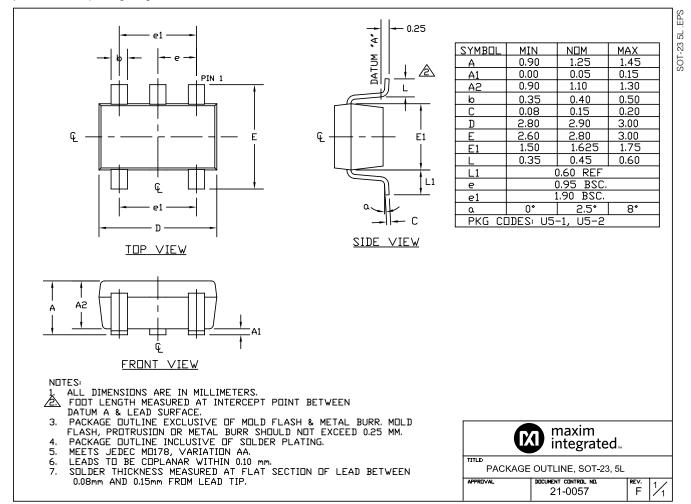
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# MAX9065

# Ultra-Small, Low-Power, Window Comparator in 4 UCSP and 5 SOT23

# **Revision History**

| REVISION<br>NUMBER | REVISION<br>DATE | DESCRIPTION  | PAGES<br>CHANGED |
|--------------------|------------------|--|------------------|
| 0                  | 7/08             | Initial release                                    | _                |
| 1                  | 10/08            | Removed future part reference from 5 SOT23 package | 1                |
| 2                  | 1/11             | Added G45 designation                              | 1                |
| 3                  | 8/15             | Corrected error in Typical Operating Circuit       | 1                |

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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