

#### **Preliminary**

## **GRF2052**

# Linear, Ultra-Low Noise Amplifier; 2.3 – 3.8 GHz

Package: 12-Pin QFN



#### **Features**

Broadband Tunable; 2300 - 3800 MHz

 Reference Frequency/Bias Condition: 2500 MHz; 5V/65 mA

Evaluation Board Gain: 19.2 dB

Evaluation Board NF: 0.49 dB

De-embedded NF: 0.35 dB

IIP3: +18.8 dBm

IP1dB: +1.5 dBm

Flexible Vdd: 3.0 – 6.0 volts

Flexible Iddq: 20 - 80 mA

### **Applications**

First Stage LNA for Infrastructure

Small Cells and Cellular Repeaters

Fast Switching TDD Systems

General Purpose Amplifier

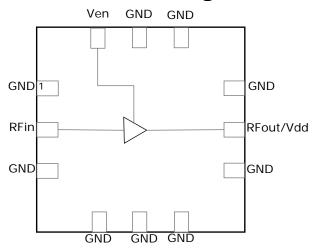
#### **Product Description**

The GRF2052 is a broadband, ultra-low noise linear amplifier designed for small cell, wireless infrastructure and other high performance RF applications requiring the absolute lowest possible NF along with high gain. The device delivers industry leading NF, exceptional gain and high linearity over 2300 - 2700 MHz and 3300 – 3800 MHz using a separate tune for each band.

Configured as a first stage LNA, linear driver or cascaded gain block, GRF2052 offers high levels of reuse both within a design and across platforms. For applications requiring lower gain over 1700 – 2700 MHz, the pin compatible GRF2051 should be used.

GRF2052 is housed in a 2.0 x 2.0 x 0.55 mm 12-pin plastic OFN.

#### **Functional Block Diagram**



2.0 mm x 2.0 mm QFN-12



### **Absolute Ratings**

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	Vdd	0	5.5	V
RF Input Power: (Load VSWR < 2:1; V <sub>D</sub> : 5.0 volts)	P <sub>IN MAX</sub>		+20	dBm
Operating Temperature (Package Heat Sink)	T <sub>AMB</sub>	-40	+105	°C
Storage Temperature	T <sub>STG</sub>	-40	+150	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Tmax		+160	°C
Maximum Disspated Power (Note: De-rate 8 mW/°C for T <sub>AMB</sub> > +85C.	PDISS MAX		500	mW
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	Class 4: 1000		V
Human Body Model: (TBD)	HBM	Class 1B: 500		V
Machine Model: (TBD)	MM	Class A: 50		V

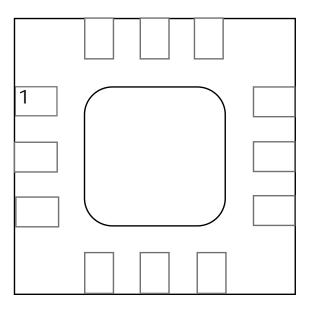


#### Caution! ESD Sensitive Device

Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.



### Pin Out (Top View)



### **Pin Assignments**

Pin	Name	Description	Note
1	GND	Ground	
2	RFin	RF Input	External match must provide DC block
3	GND	Ground	
4	GND	Ground	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	RFout/Vdd	RF Output	Provide device Vdd via external bias inductor
9	GND	Ground	
10	GND	Ground	
11	GND	Ground	
12	Ven	LNA Enable Control	Venable and series resistor set Iddq. Venable < 0.4 volts disables device.
PKG	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink.
BASE			



### **Nominal Operating Parameters**

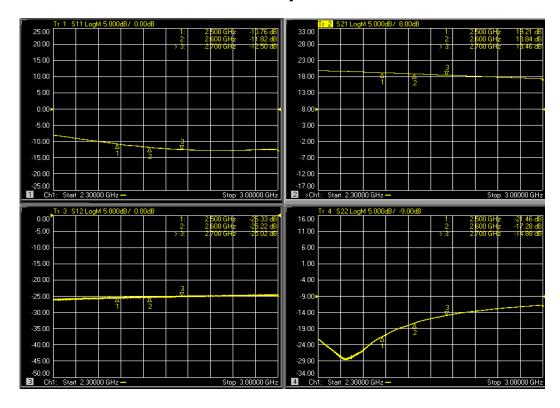
Parameter	Symbol Specification			on	Unit	Condition	
Faranietei	Syllibol	Min.	Тур.	Max.	Offic	Collation	
Gain Mode (Venable high)						Vdd = 5.0 V, T <sub>A</sub> = 25°C	
Test Frequency	F <sub>test</sub>		2500		MHz	2500 - 2700 MHz Tune	
Evaluation Board Gain	S21		19.2		dB		
Input Return Loss	S11		-11		dB		
Output Return Loss	S22		-18		dB		
Evaluation Board Noise Figure	NF		0.49		dB	Evaluation Board SMA to SMA	
De-embedded Noise Figure	NF		0.38		dB	Device Pin 2 to Pin 8	
Input 3rd Order Intercept Point	IIP3		+18.5		dBm	+2 dBm P <sub>OUT</sub> per tone at 2 MHz Spacing (2599 and 2601 MHz)	
Input 1dB Compression Point	IP1dB		+1.5		dBm		
Switching Rise Time	T <sub>RISE</sub>		300		ns		
Switching Fall Time	T <sub>FALL</sub>		300		ns		
Supply Current	ldd		63		mA	Adjustable for optimal IP3	
Enable Current	lenable		2		mA		
Thermal Data							
Thermal Resistance (measured via IR scan)	Θјс		118		°C/W	On standard evaluation board	
Channel Temperature @ +85 C Reference (Package Heat Sink)	Tchannel		+122		°C	Vdd: 5.0 V; Iddq: 63 mA; No RF; Pdiss: 315 mW	

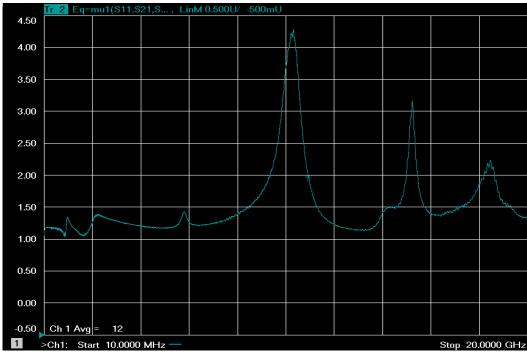
### GRF2052 Multi-Band, Measured Performance Summary Table:

Tune (MHz)	Reference Freq. (MHz)	Gain (dB)	Eval Board NF (dB)	De-embedded NF (dB)	OP1dB (dBm)	IIP3 (dBm)	S(1,1) (dB)	S(2,2) (dB)	Bias Condition (V/mA)
2300 - 2700	2500	19.2	0.49	0.35	+1.5	+18.5	-11	-18	5.0/63
2300 - 2700	2700	18.3	0.49	0.35	+3.0	+20.0	-12	-14	5.0/63
3300 - 3800	3600 (TBD)							-	

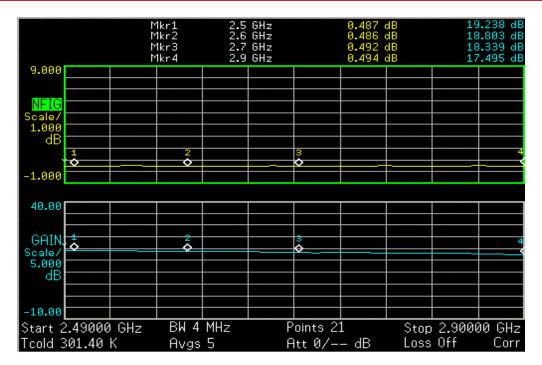


GRF2052 Evaluation Board S-Parameters and Stability Mu Factor: 2500 - 2700 MHz Tune

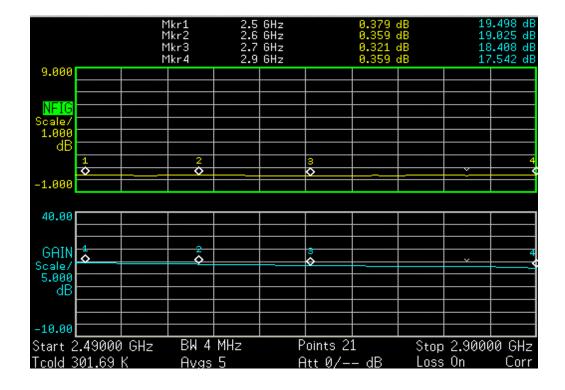






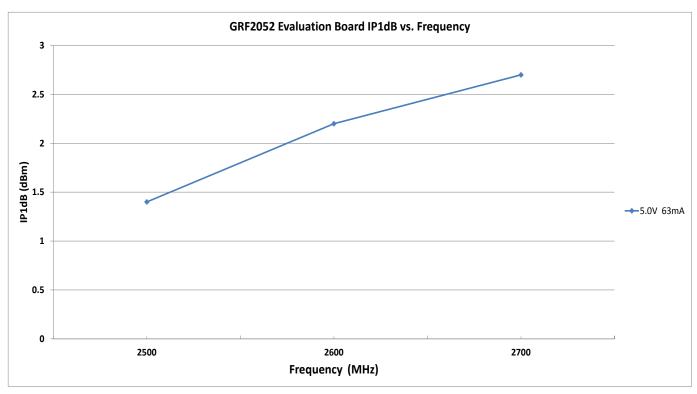


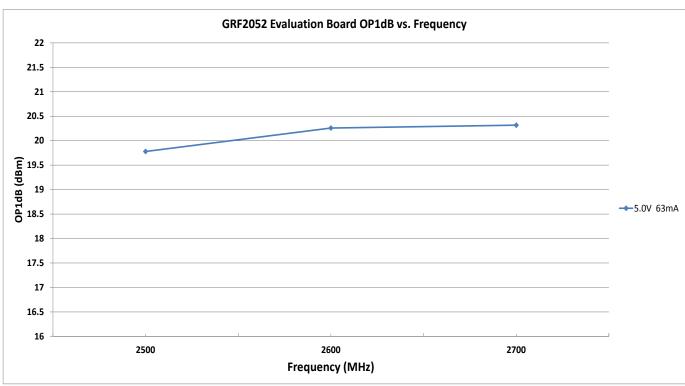
GRF2052 Evaluation Board Noise Figure (SMA to SMA): 2.5 - 2.7 GHz Tune



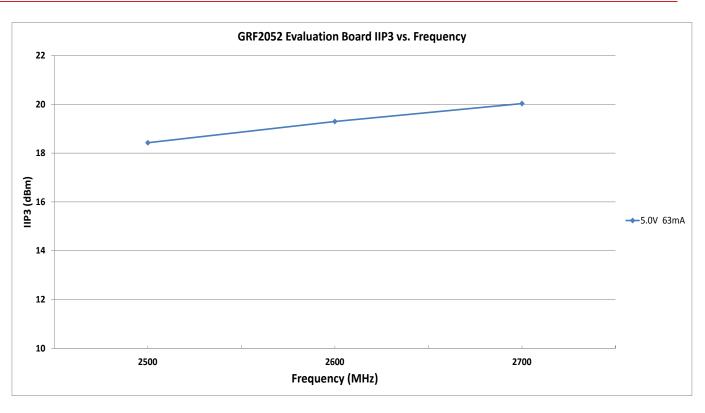
GRF2052 De-embedded Noise Figure (SMA to SMA): 2.5 - 2.7 GHz Tune

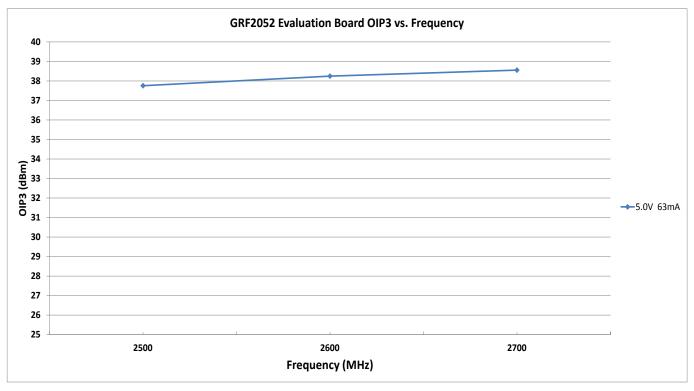




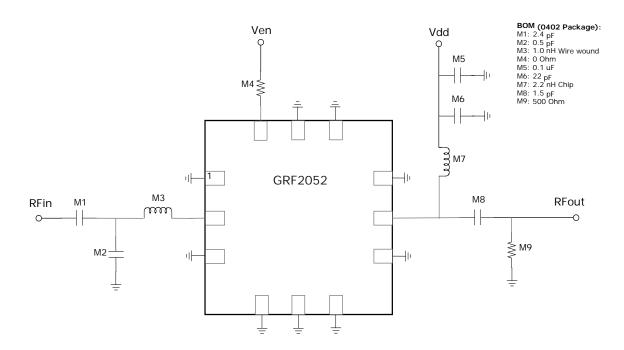












**GRF2052 Application Schematic** 



#### GRF2052 Theory of Operation:

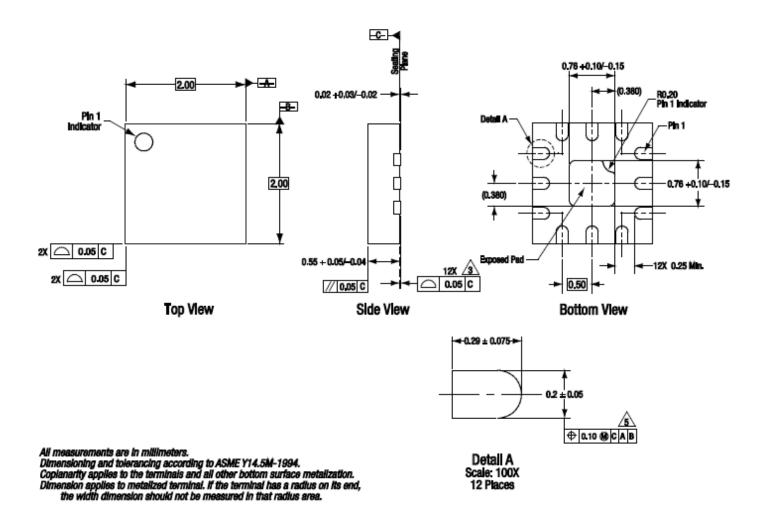
The GRF2052 is a single-stage, high-performance, ultra-low noise linear amplifier that is suitable for a wide range of high performance applications. With simple external matching on the output, the device can be tuned over 2300-3800 MHz with fractional bandwidths up to 30%. This device is the highest gain member of the GRF205X family of LNA optimized as follows:

GRF2050: Optimized for 700 – 960 MHz (Under Development) GRF2051: Optimized for 1700 – 2700 MHz (Single Tune)

GRF2052: Optimized for 2300 – 2700 MHz and 3300 – 3800 MHz (Separate Tunes)

The device Iddq can be set independently from the supply voltage Vdd via the bias resistor in series with Venable. This allows the device Iddq to be optimized to meet a given linearity requirement with the highest possible efficiency. For a given Venable, increasing this resistor will result in lower Iddq.





GRF2052 12-Pin QFN Package Dimensions



Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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