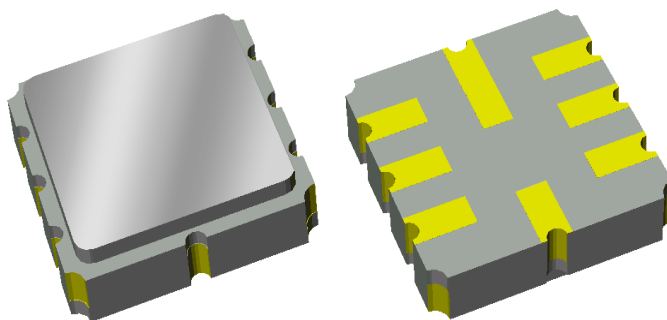


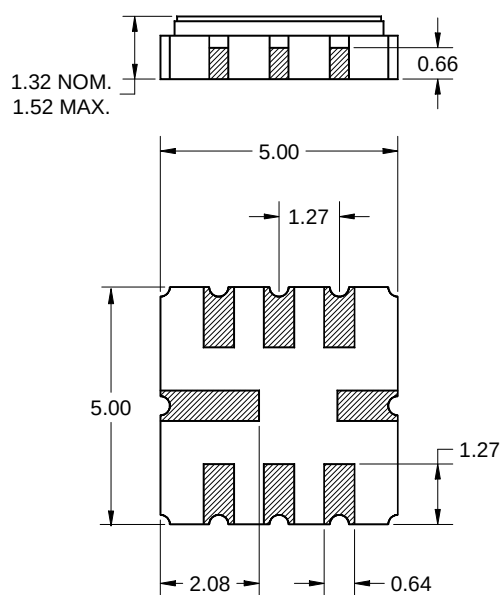
Features

- For WiMAX applications
- Usable bandwidth 10 MHz
- Low loss
- Balanced operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



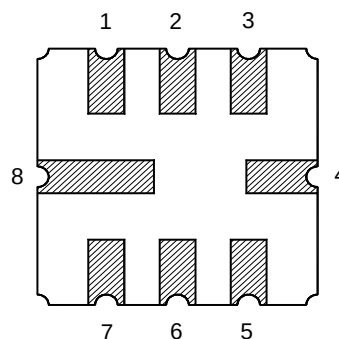
Package

Surface Mount 5.00 x 5.00 x 1.32 mm
SMP-20



Pin Configuration

Bottom View



Pin No.	Description
1	Input +
2	Input -
5	Output +
6	Output -
3,4,7,8	Case Ground

Dimensions shown are nominal in millimeters
All tolerances are ± 0.15 mm except overall
length and width $+0.15/-0.10$ mm

Body: Al_2O_3 ceramic
Lid: Kovar, Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ 0 to +70 °C

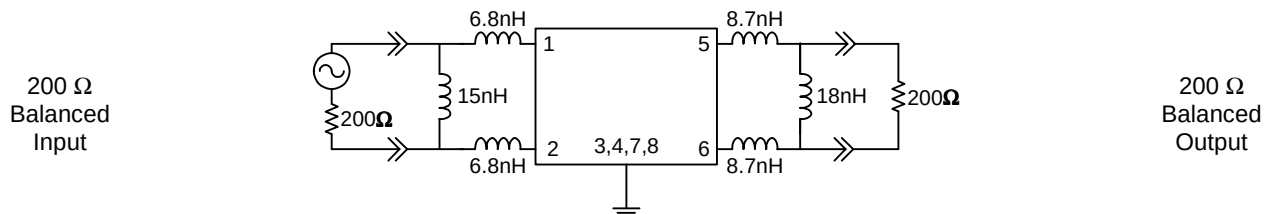
Parameter ⁽³⁾	Minimum	Typical ⁽⁴⁾	Maximum	Unit
Center Frequency	-	398	-	MHz
Insertion Loss at Center Frequency	-	8.7	12.5	dB
Lower 1.5 dB Band Edge ⁽⁵⁾	-	392.4	393	MHz
Upper 1.5 dB Band Edge ⁽⁵⁾	403	403.7	-	MHz
Lower 35 dB Band Edge ⁽⁵⁾	388	388.9	-	MHz
Upper 35 dB Band Edge ⁽⁵⁾	-	406.9	408	MHz
Amplitude Ripple 393 – 403 MHz	-	0.7	1.2	dB
Group Delay Variation 393 – 403 MHz	-	44	60	ns p-p
Ultimate Rejection ⁽⁵⁾				
10 – 348 MHz	40	49	-	dB
348 – 385 MHz	35	45	-	dB
385 – 388 MHz	35	46	-	dB
408 – 411 MHz	35	40	-	dB
411 – 448 MHz	35	41	-	dB
448 – 1000 MHz	33	35	-	dB
Source Impedance (balanced) ⁽⁶⁾	-	200	-	Ω
Load Impedance (balanced) ⁽⁶⁾	-	200	-	Ω

Notes:

1. All specifications are based on the TriQuint test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. Relative to insertion loss at center frequency
6. This is the optimum impedance in order to achieve the performance shown

Test Circuit:

Actual matching values may vary due to PCB layout and parasitics



Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

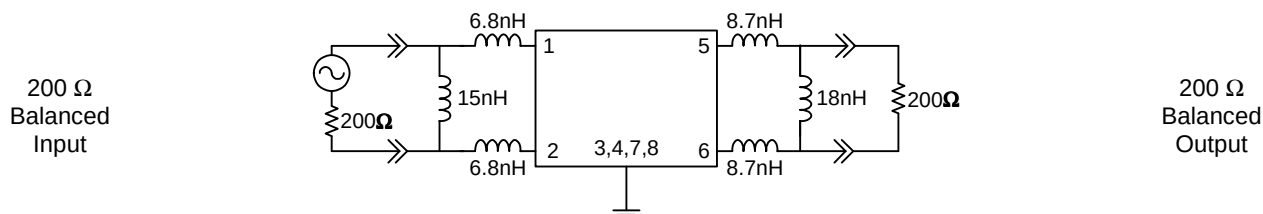
Parameter ⁽³⁾	Minimum	Typical ⁽⁴⁾	Maximum	Unit
Center Frequency	-	398	-	MHz
Insertion Loss at Center Frequency	-	8.7	12.5	dB
Lower 2 dB Band Edge ⁽⁵⁾	-	392.4	393	MHz
Upper 2 dB Band Edge ⁽⁵⁾	403	403.7	-	MHz
Lower 35 dB Band Edge ⁽⁵⁾	388	388.9	-	MHz
Upper 35 dB Band Edge ⁽⁵⁾	-	406.9	408	MHz
Amplitude Ripple 393 – 403 MHz	-	0.7	1.6	dB
Group Delay Variation 393 – 403 MHz	-	44	80	ns p-p
Ultimate Rejection ⁽⁵⁾				
10 – 348 MHz	40	49	-	dB
348 – 385 MHz	35	45	-	dB
385 – 388 MHz	35	46	-	dB
408 – 411 MHz	35	40	-	dB
411 – 448 MHz	35	41	-	dB
448 – 1000 MHz	33	35	-	dB
Source Impedance (balanced) ⁽⁶⁾	-	200	-	Ω
Load Impedance (balanced) ⁽⁶⁾	-	200	-	Ω

Notes:

1. All specifications are based on the TriQuint test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. Relative to insertion loss at center frequency
6. This is the optimum impedance in order to achieve the performance shown

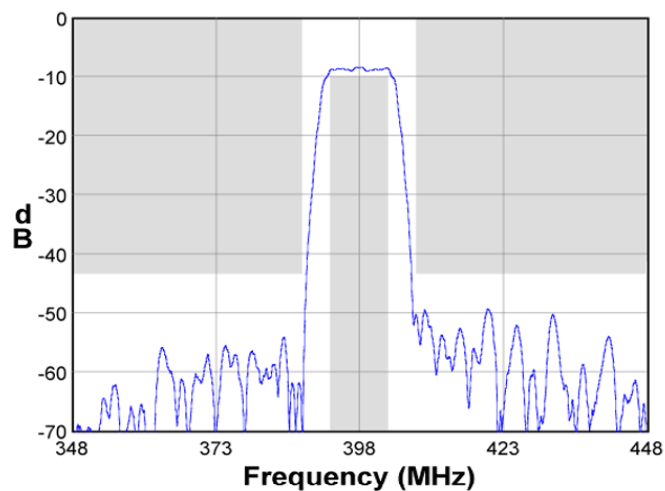
Test Circuit:

Actual matching values may vary due to PCB layout and parasitics

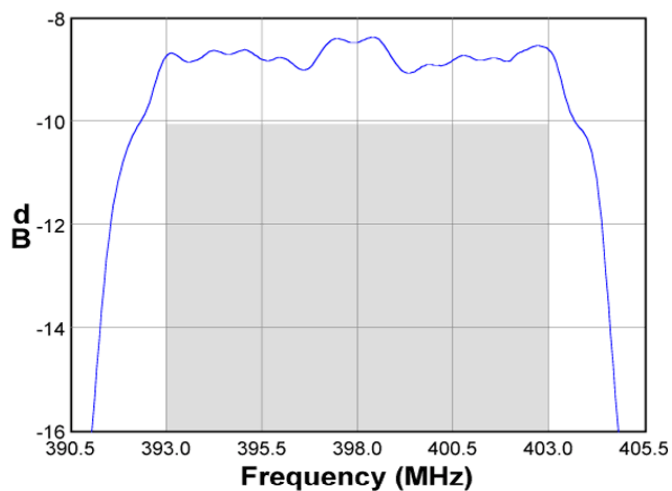


Typical Performance (at room temperature)

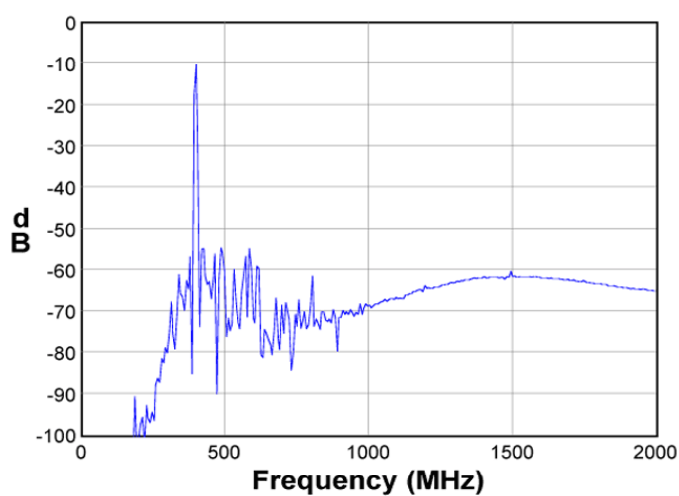
Frequency Response



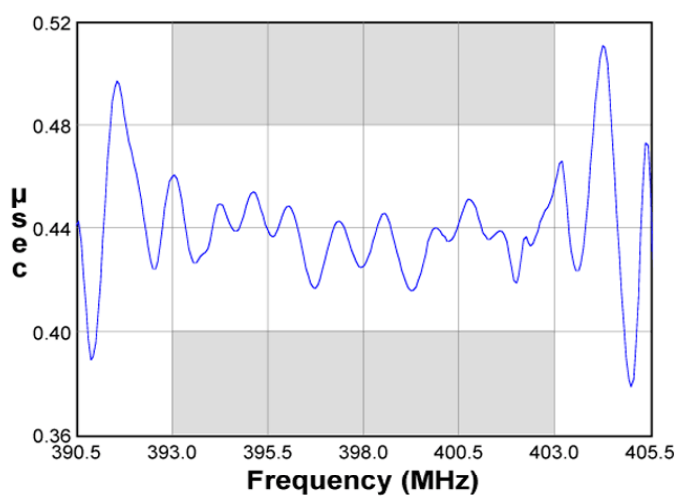
Passband Response



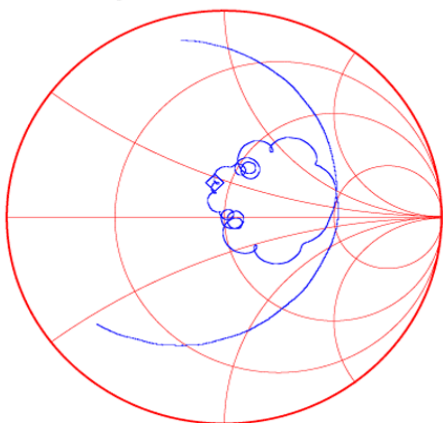
Wideband Response



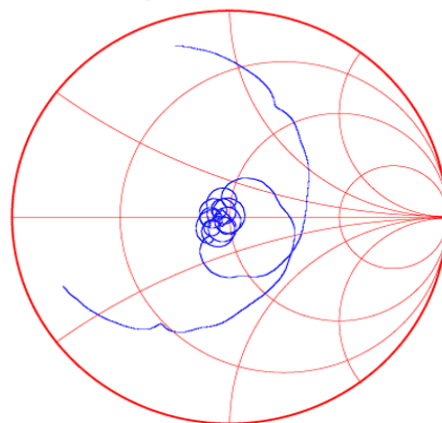
Group Delay



Input Smith Chart

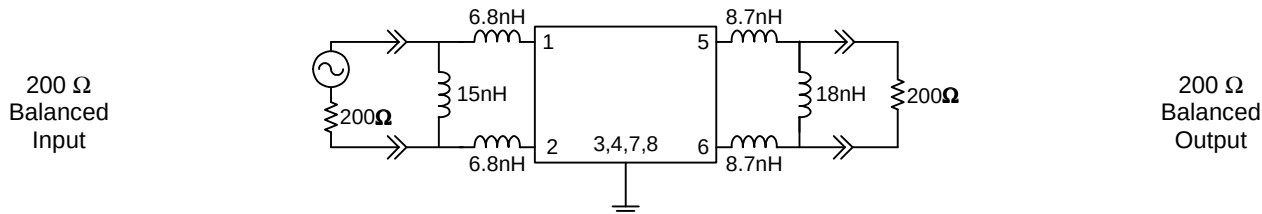


Output Smith Chart

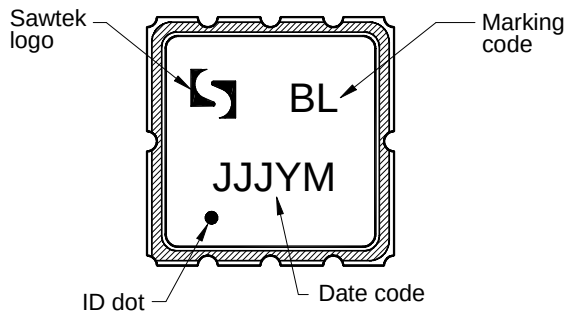


Matching Schematics

Actual matching values may vary due to PCB layout and parasitics

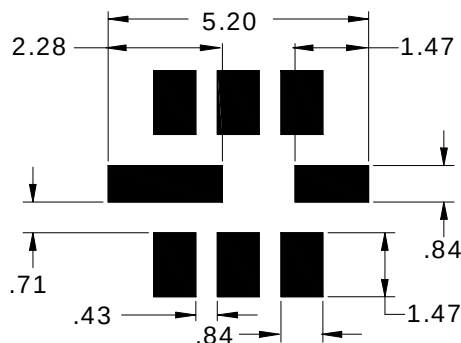


Marking



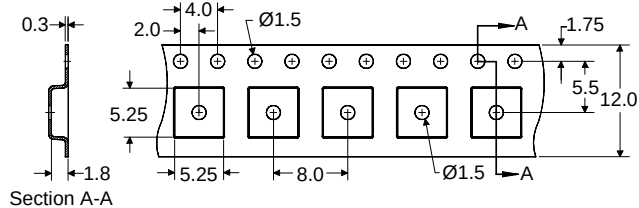
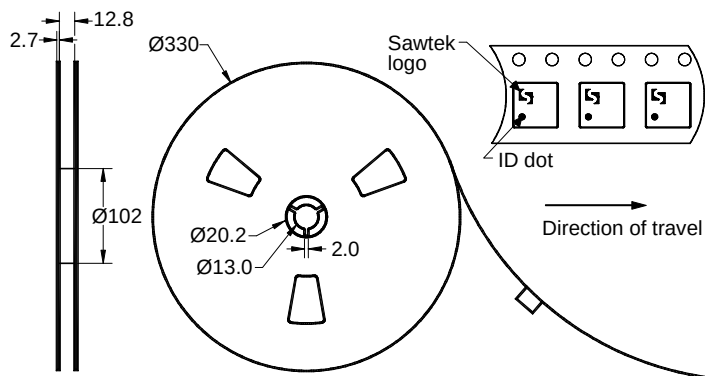
The date code consists of: day of the current year (Julian, 3 digits), Y = last digit of the year and M = manufacturing site code

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel



Dimensions shown are nominal in millimeters
Packaging quantity: 4000 units/reel

Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-40	+85	°C

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JESD22-B102, Pb-free process, 260C peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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[Representatives or distributors](#)