



**SUMMARY TEST DATA
ON
AMPLIFIER**

Job No : PE603026
Model No : PEC-4520-218-12-15-SFF-VVG
Serial No : PL2395

Tested By : Kevin Mason
Temperature : +25°C
Date : 06/02/06

TEST ITEM NO.	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
1	Frequency Range:	2.0 to 18.0GHz	Pass	
2	*Gain:	+45dB Typ.	Pass (See Plot)	
3	*Gain Flatness:	± 1.5dB Typ.	± 1.21dB	
4	*Noise Figure:	4.5dB Max.	Pass (See NF Plot)	
5	*Pout @ 1dB Compression:	+16dBm Min.	> +16dBm	
6	VSWR In/Out:	2.0:1 Max.	Pass (See plot)	
7	Attenuation Range: <i>V_{CTL} = 0 to 6VDC</i>	25dB Min.	Pass (See plot)	
8	DC Supply:	700mA @ +15V Max.	484mA @ +15V	

*Specifications valid with no attenuation selected.

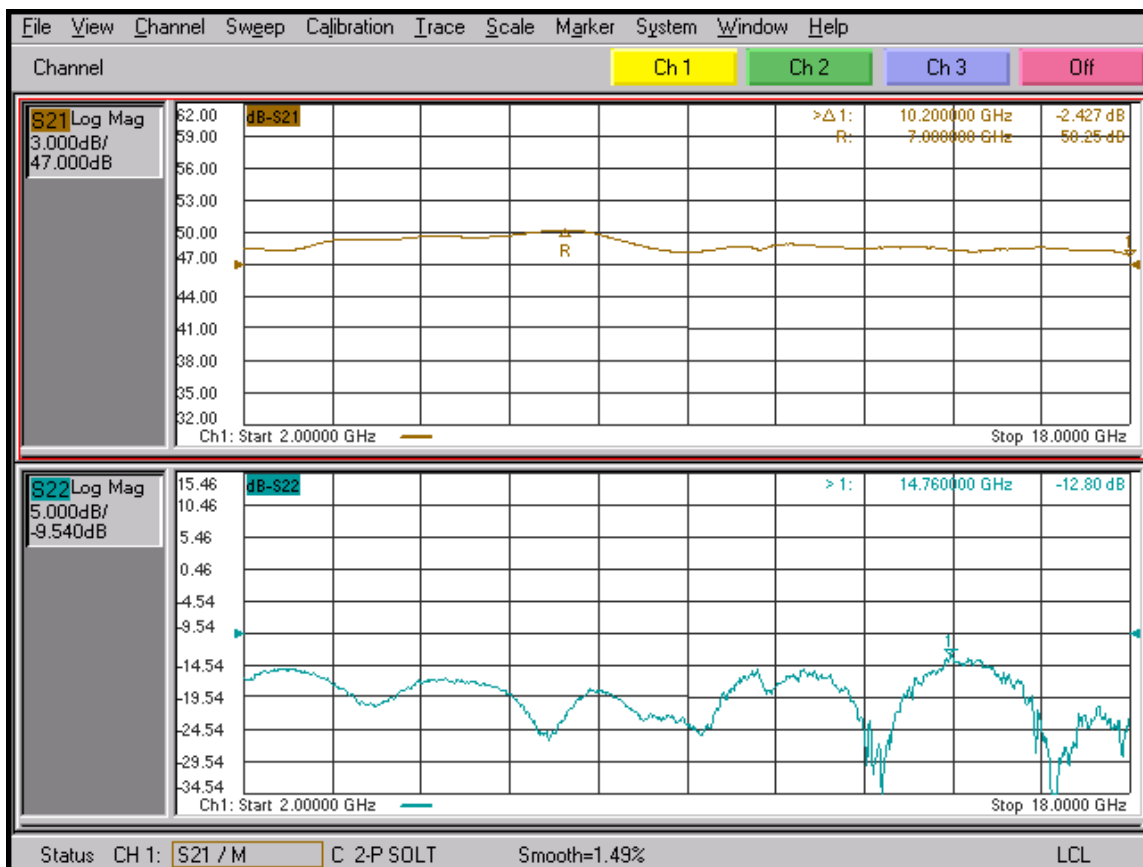
Production Manager Approval:  Date: 06/02/06

QA/QC Approval:  Date: 06/02/06

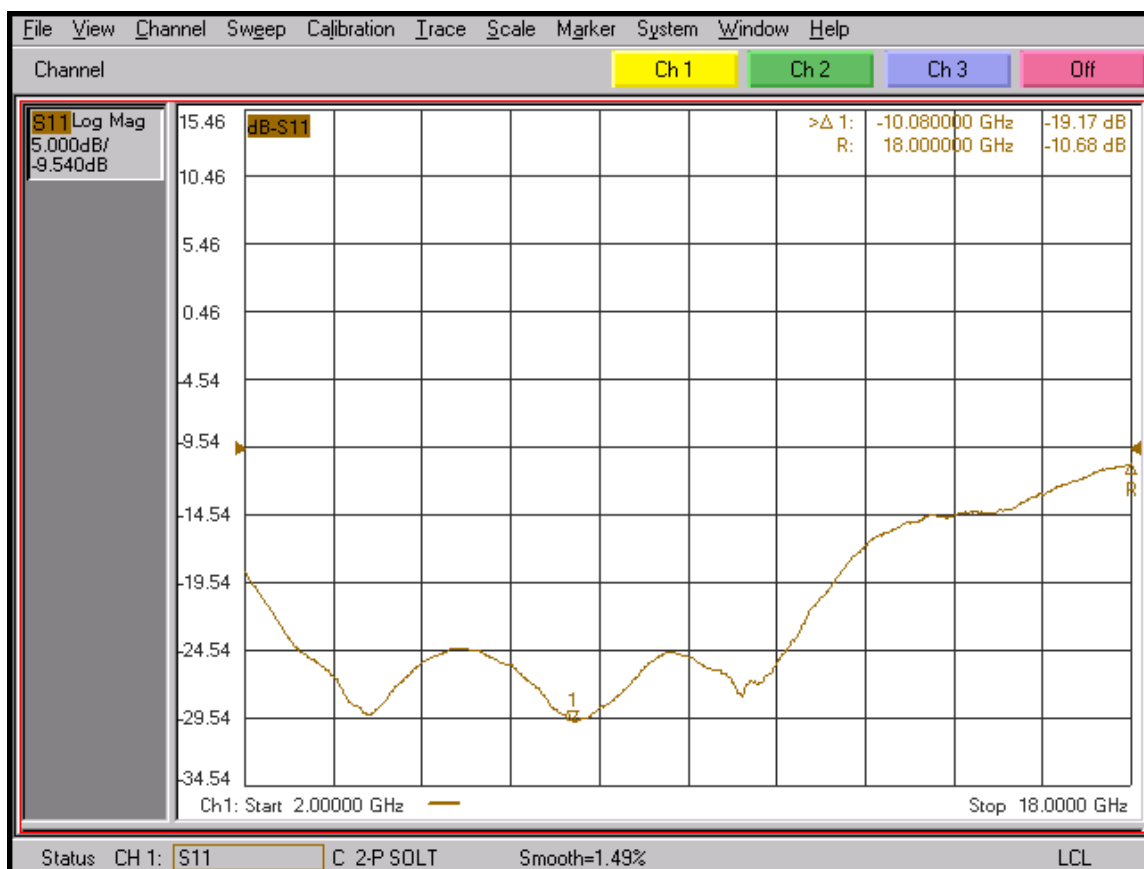
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email: sales@planarelec.com



Swept S21 and S22 Plot (Full Gain, $V_{CTL} = 0V$)

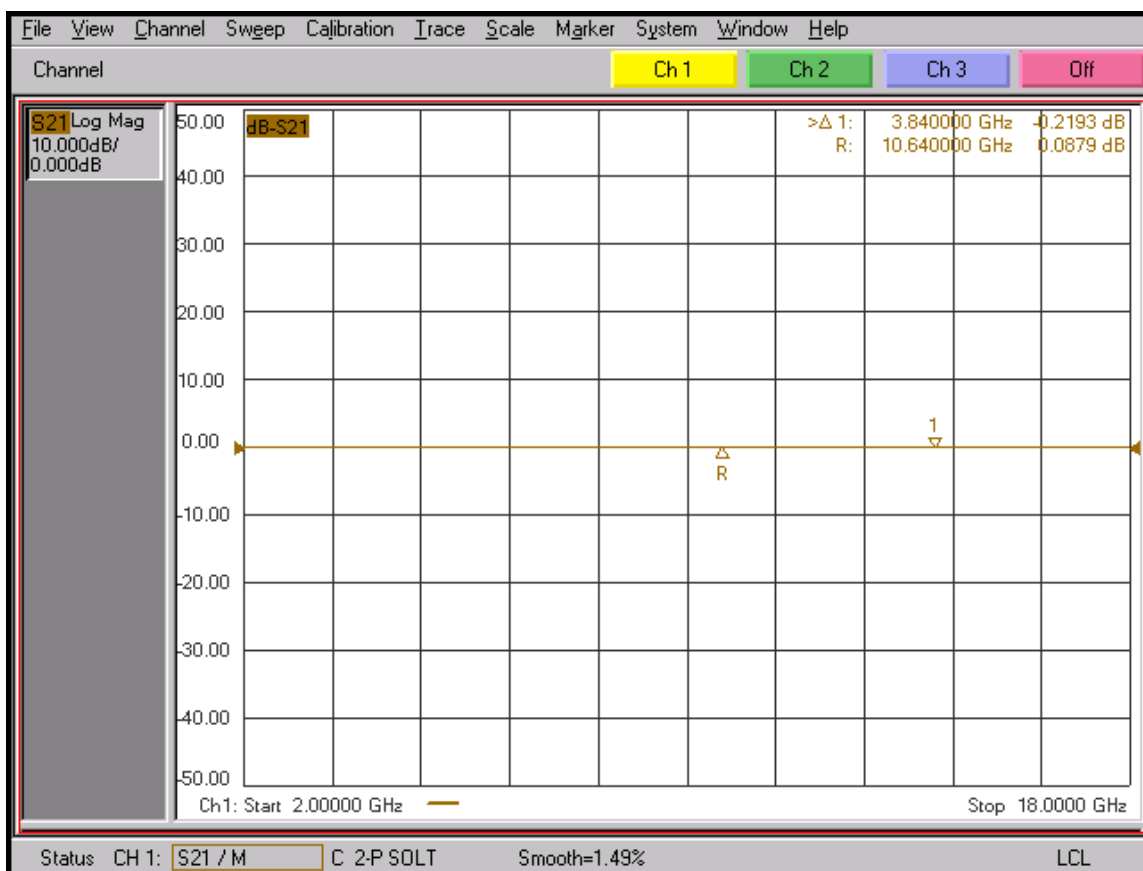


Swept S11 Plot



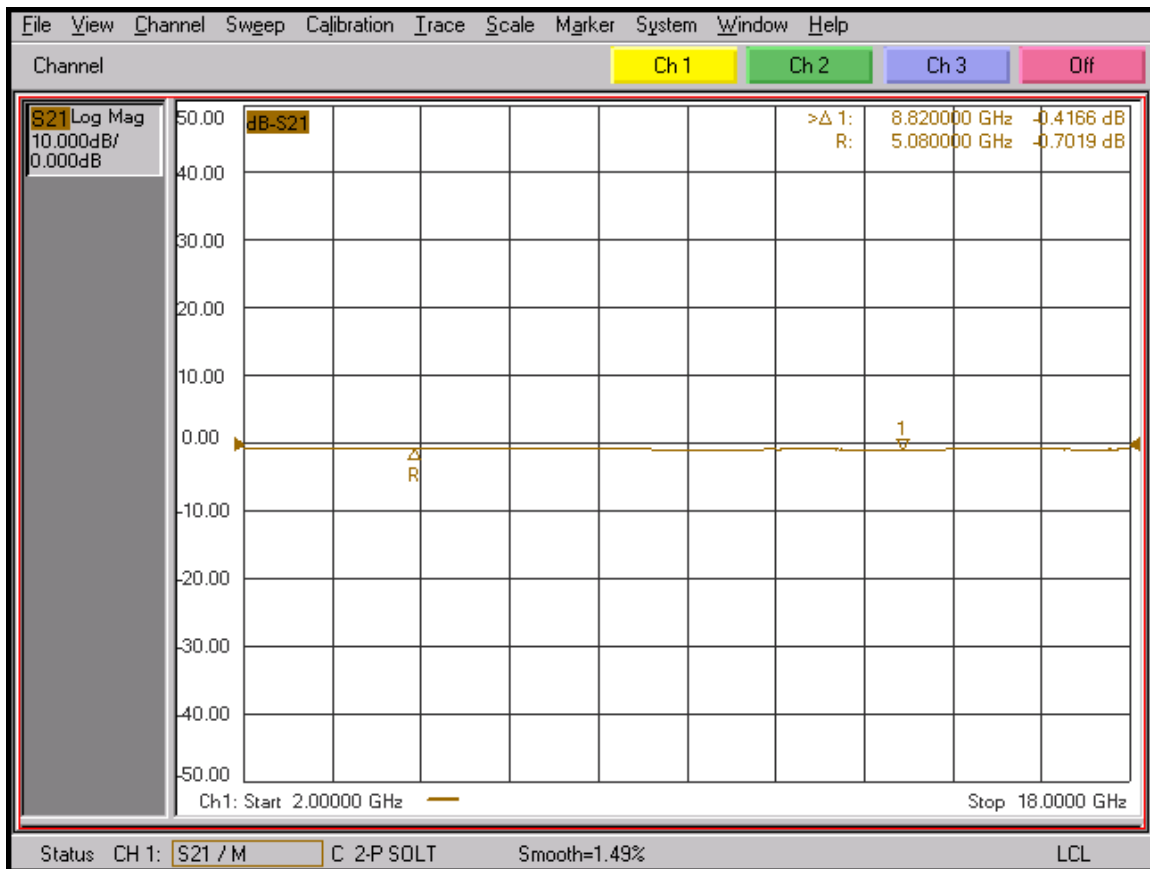


Swept S21 Plot (Gain Normalized, $V_{CTL} = 0V$)

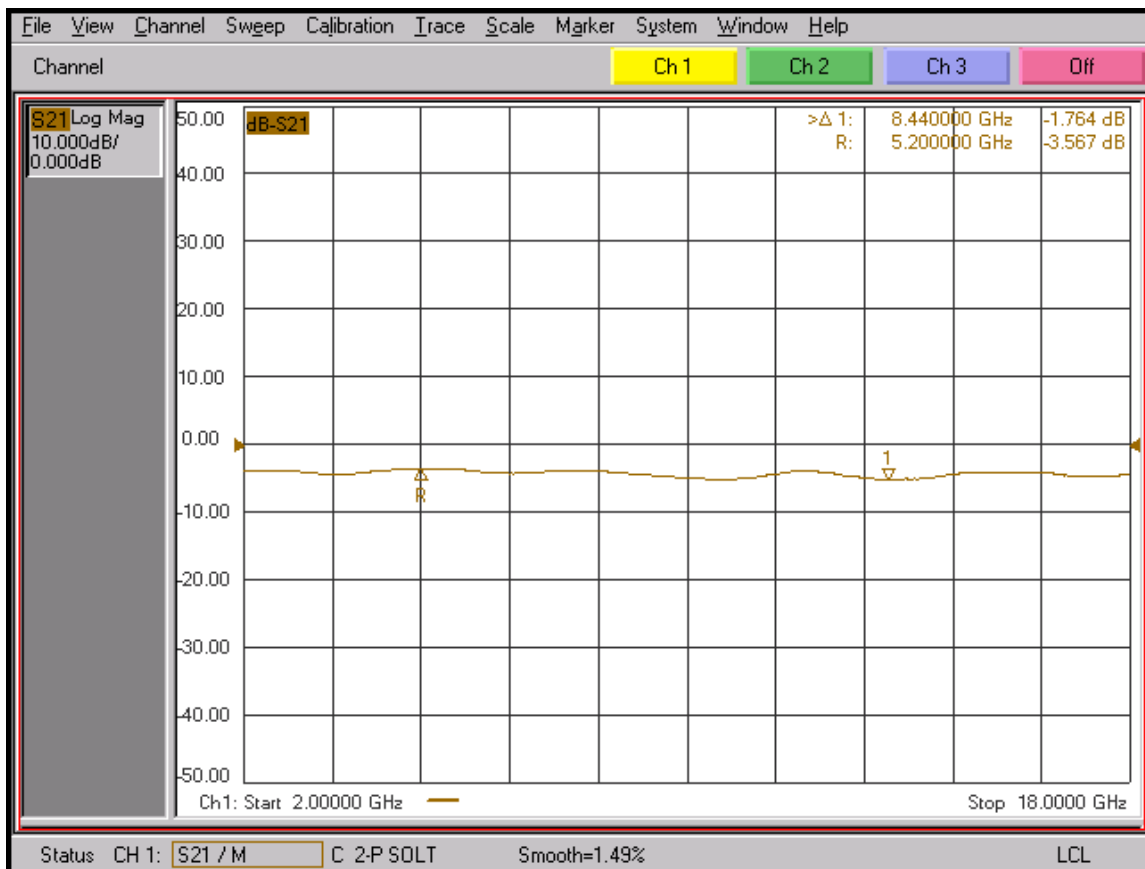




Swept S21 Plot (Attenuated Gain, $V_{CTL} = 1V$)

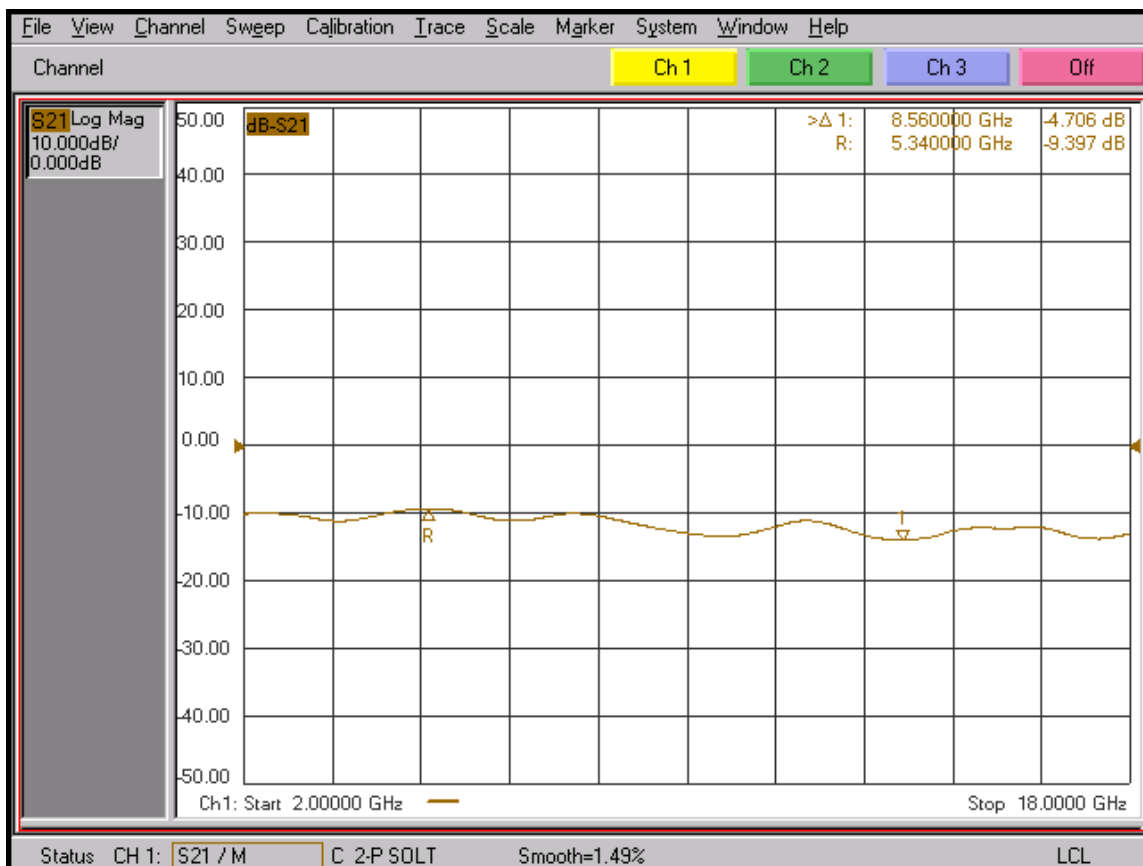


Swept S21 Plot (Attenuated Gain, $V_{CTL} = 2V$)



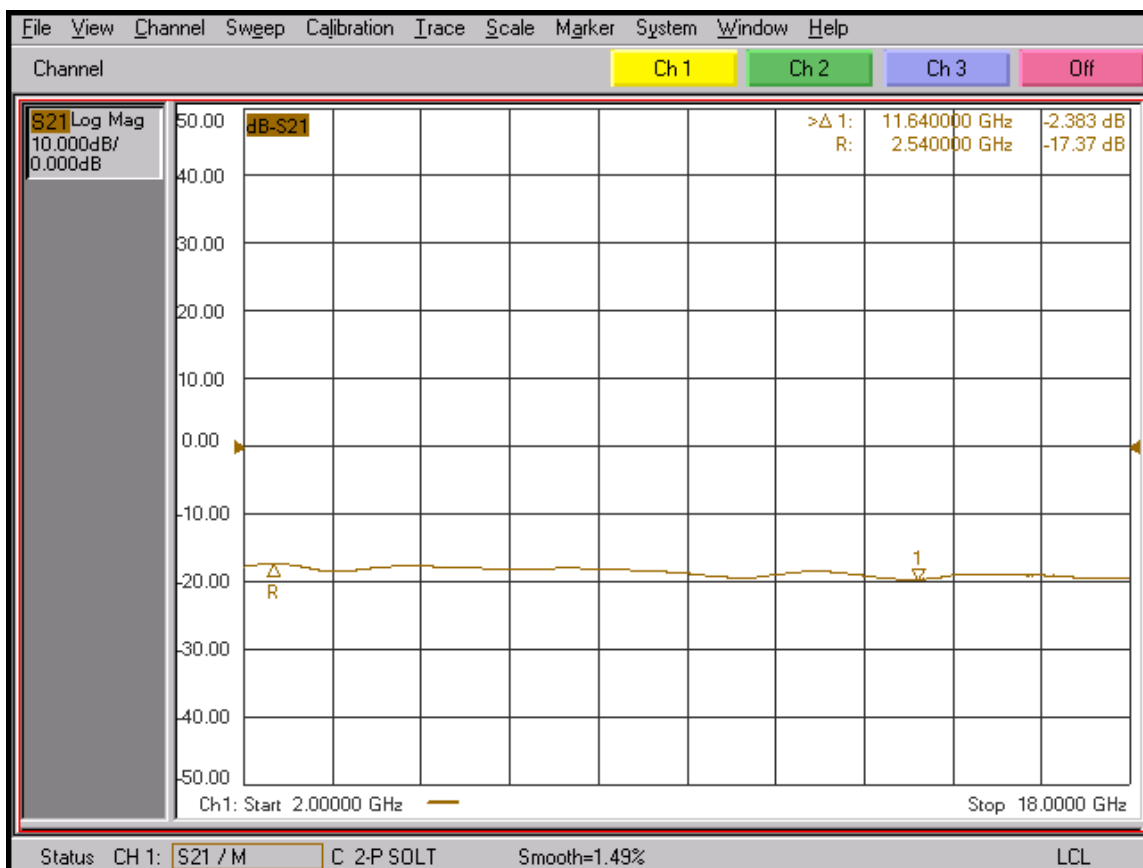


Swept S21 Plot (Attenuated Gain, $V_{CTL} = 3V$)

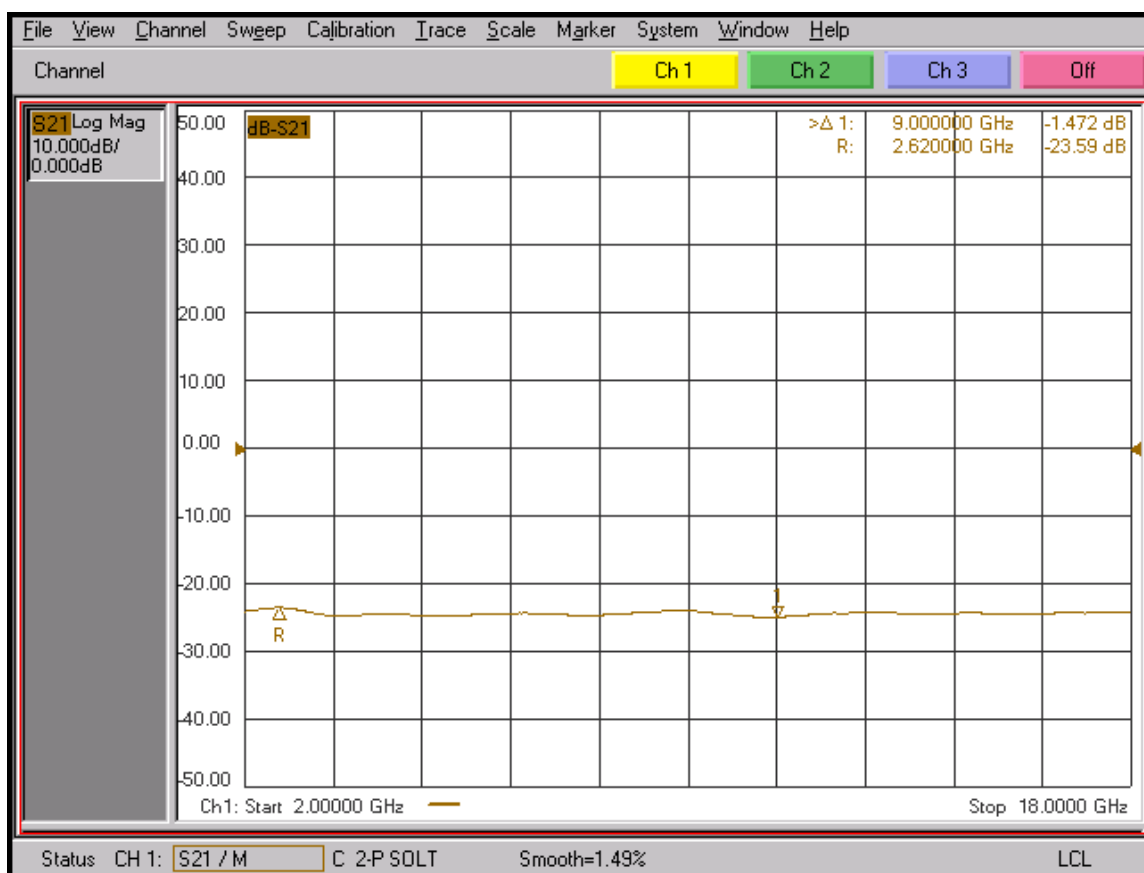




Swept S21 Plot (Attenuated Gain, $V_{CTL} = 4V$)

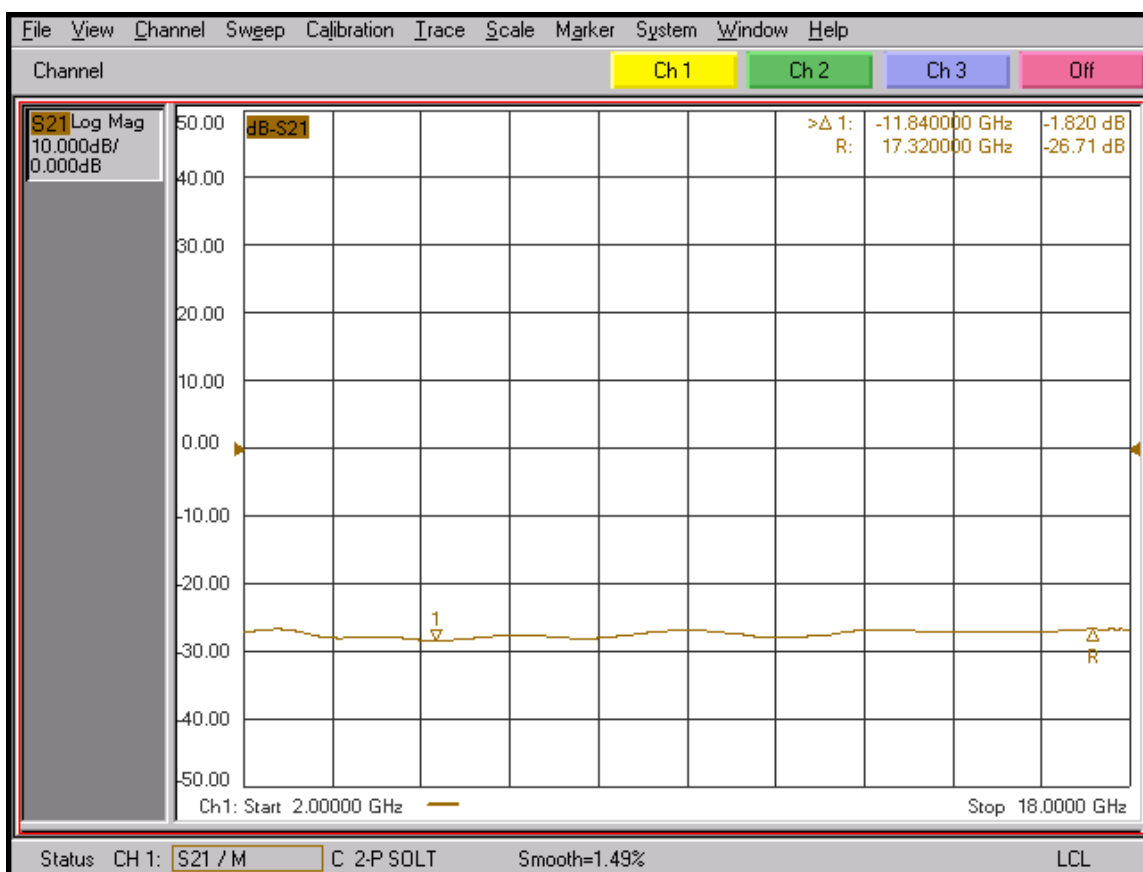


Swept S21 Plot (Attenuated Gain, $V_{CTL} = 5V$)

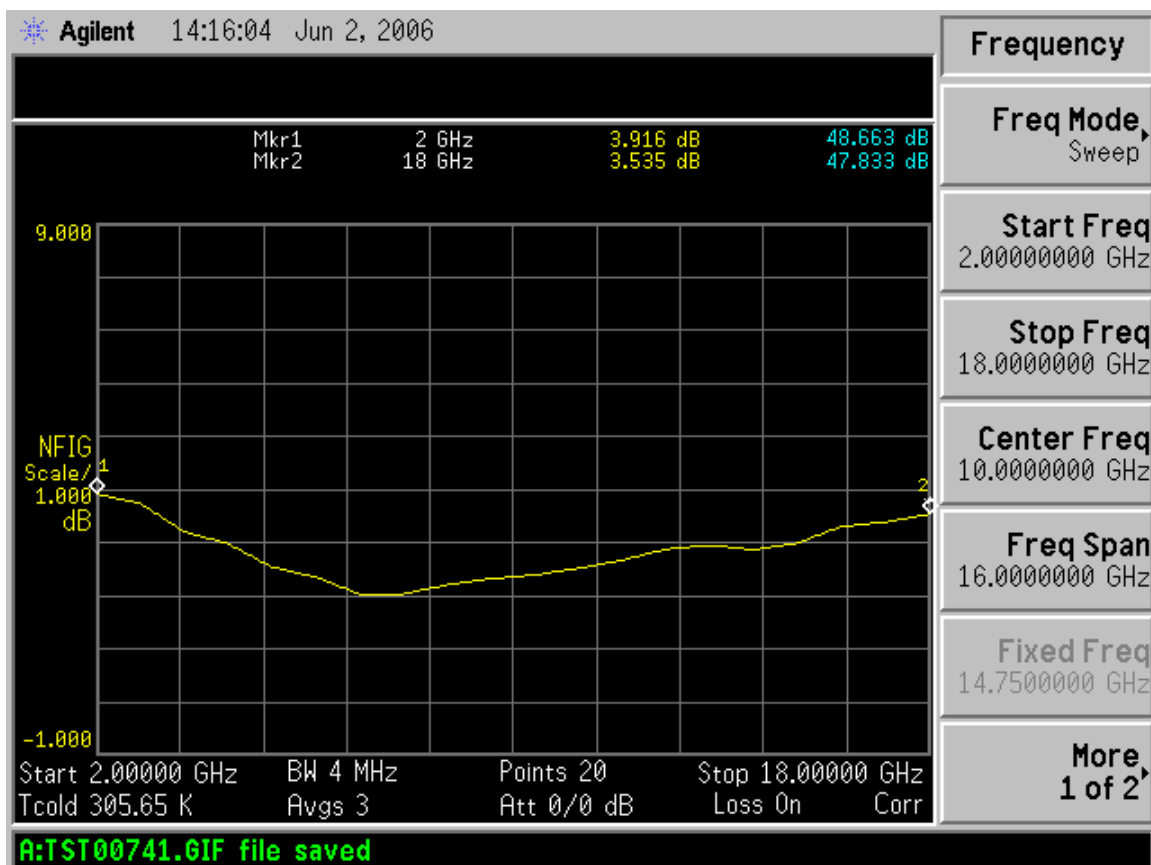




Swept S21 Plot (Attenuated Gain, $V_{CTL} = 6V$)



Noise Figure Plot (Full Gain, $V_{CTL} = 0V$)





Noise Figure Plot (Full Attenuation, $V_{CTL} = 6V$)

