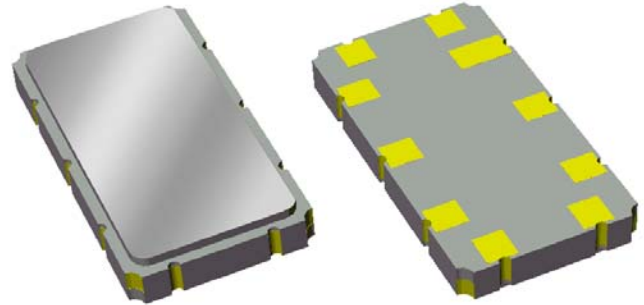


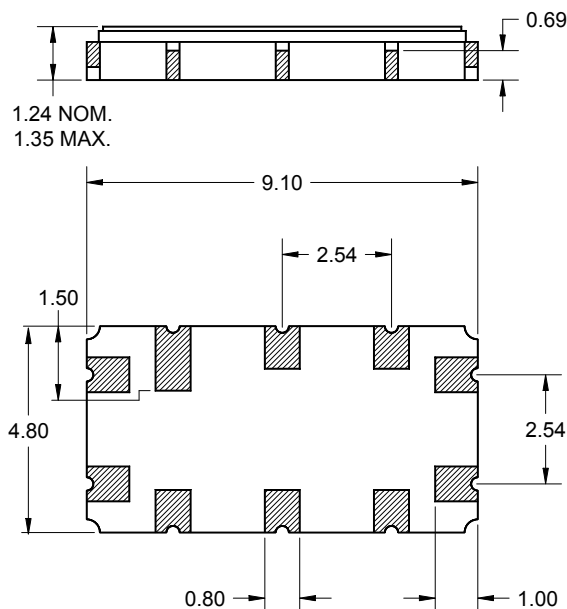
Features

- For multiple applications
- Usable bandwidth 6 MHz
- Low loss
- High attenuation
- Balanced operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



Package

Surface Mount 9.10 x 4.80 x 1.24 mm
SMP-35C

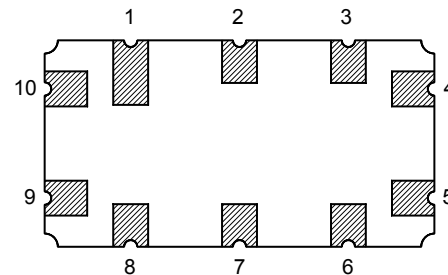


Dimensions shown are nominal in millimeters
All tolerances are ± 0.15 mm except overall
length and width ± 0.10 mm

Body: Al_2O_3 ceramic
Lid: Kovar, Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Pin Configuration

Bottom View



Pin No.	Description
9	Input +
10	Input -
4	Output +
5	Output -
1,2,3,6,7,8	Case Ground

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

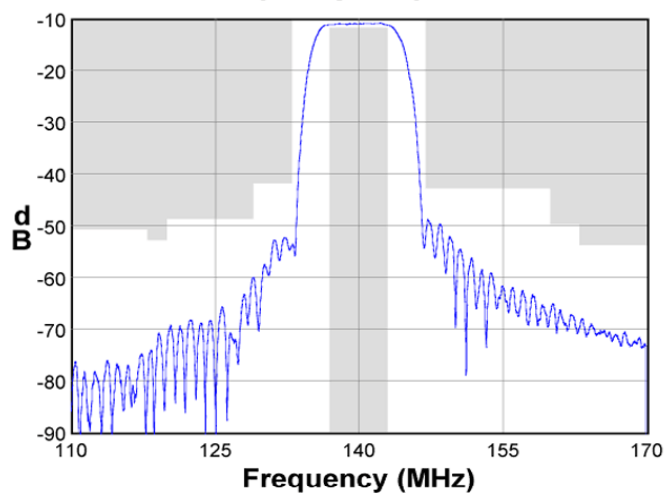
Parameter ⁽³⁾	Minimum	Typical ⁽⁵⁾	Maximum	Unit
Center Frequency	-	140	-	MHz
Minimum Insertion Loss	-	11	12.5	dB
Amplitude Variation 137.0 – 143.0 MHz	-	0.4	1.0	dB p-p
Phase Linearity 137.6 – 142.4 MHz 137.0 – 143.0 MHz	- -	1.6 2.1	4 6.5	° p-p ° p-p
Average Group Delay 137.0 – 143.0 MHz	0.88	0.93	0.98	µs
Relative Attenuation ⁽⁴⁾ 10 – 118 MHz 118 – 120 MHz 120 – 129 MHz 129 – 133 MHz 147 – 160 MHz 160 – 163 MHz 163 – 250 MHz	40 42 38 31 32 39 43	65 61 50 42 39 55 58	- - - - - - -	dB dB dB dB dB dB dB
Triple Transit Suppression	35	49	-	dB
Source Impedance (balanced) ⁽⁶⁾ Load Impedance (balanced) ⁽⁶⁾	- -	50 50	- -	Ω Ω

Notes:

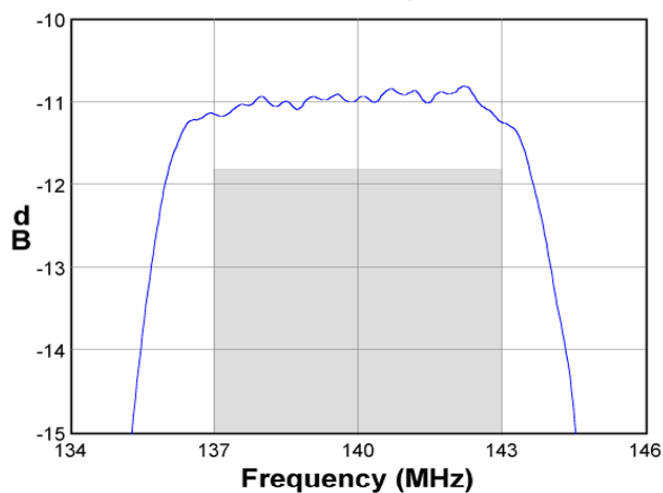
1. All specifications are based on the TriQuint matching schematic shown on page 4
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Relative to minimum insertion loss
5. Typical values are based on average measurements at room temperature
6. This is the optimum impedance in order to achieve the performance shown

Typical Performance (at room temperature)

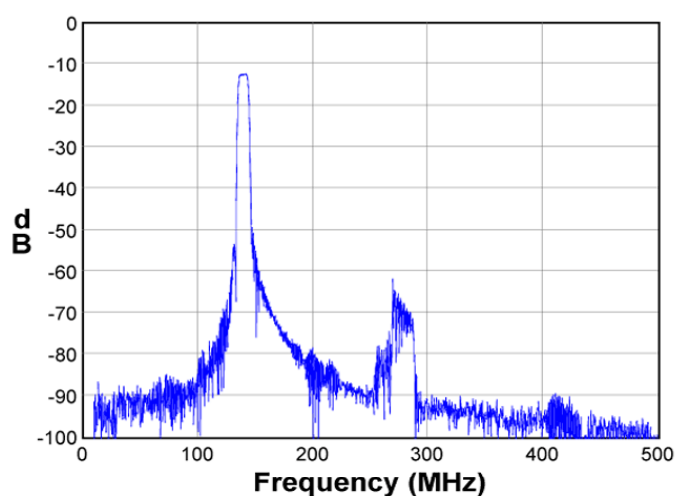
Frequency Response



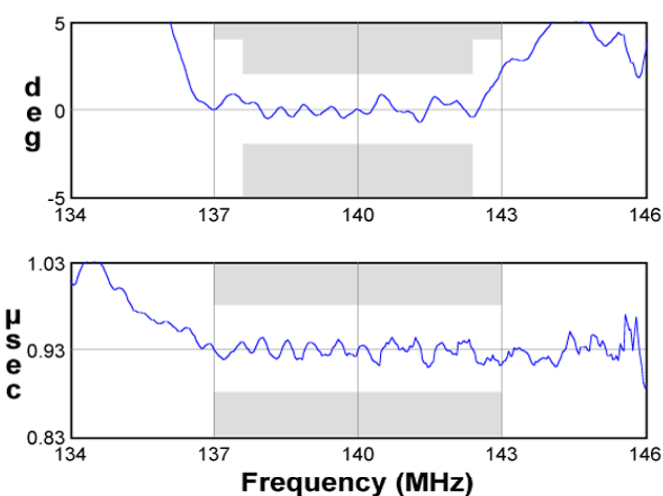
Passband Response



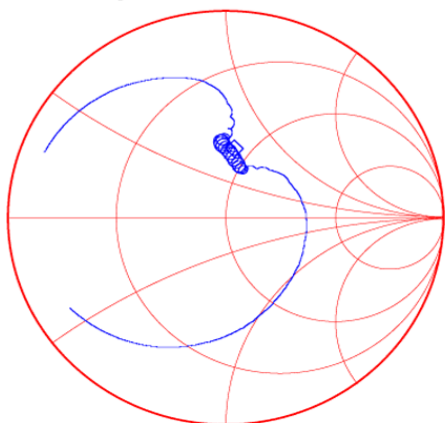
Wideband Response



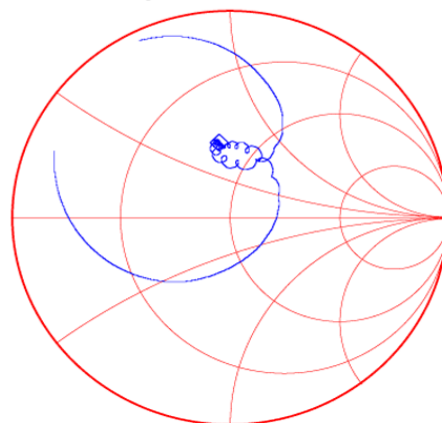
Phase / Group Delay



Input Smith Chart

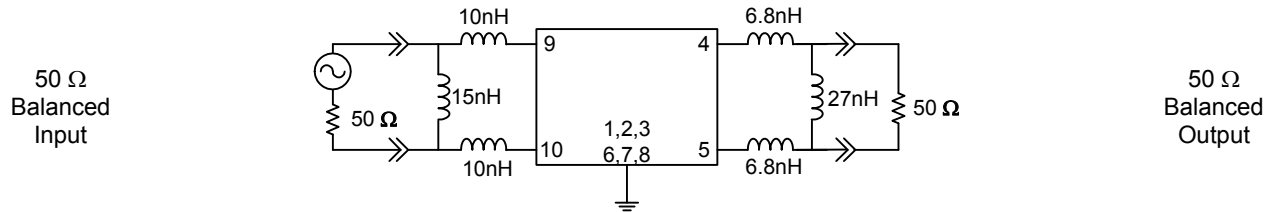


Output Smith Chart

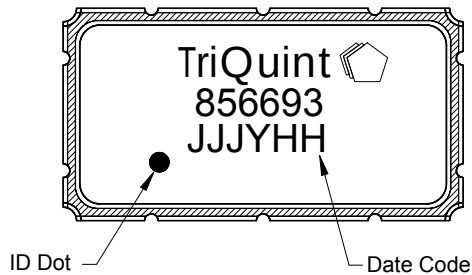


Matching Schematic

Actual matching values may vary due to PCB layout and parasitics

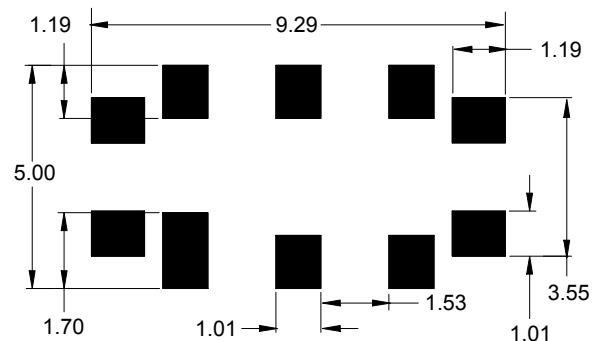


Marking



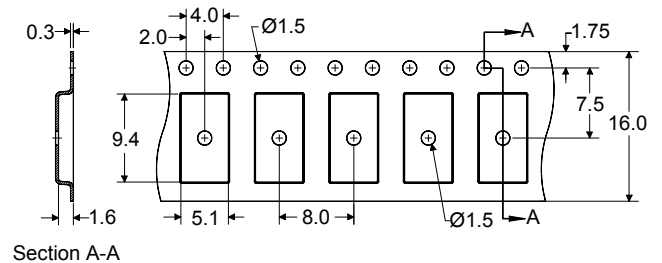
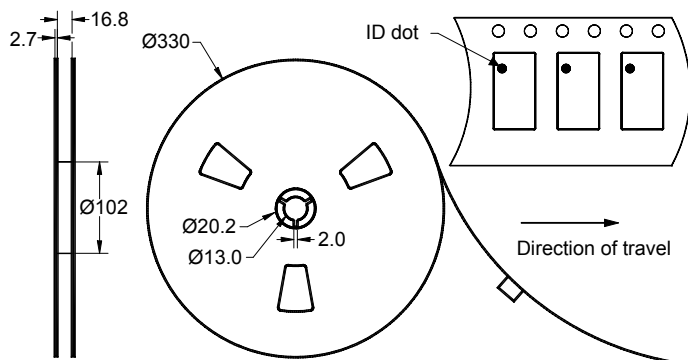
The date code consists of: day of the current year (Julian, 3 digits), last digit of the year (1 digit) and hour (2 digits)

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel



Dimensions shown are nominal in millimeters
Packaging quantity: 4000 units/reel

Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-55	+125	°C
Pyroelectric Voltage	V _{Pyro}	-	50	mV p-p
Input Power	P _{in}	-	+20	dBm

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JEDEC J-STD-020C **Pb**-free process, **260°C** peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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Contact Information



PO Box 609501
Orlando, FL 32860-9501
USA

Phone: +1 (407) 886-8860
Fax: +1 (407) 886-7061
Email: info-product@tqs.com
Web: www.triquint.com

Or contact one of our worldwide
Network of [sales offices](#),
[Representatives or distributors](#)