CMV & CMV3 Series

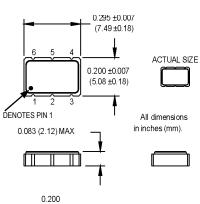
5x7 mm, 5.0 or 3.3 Volt, CMOS, VCXO

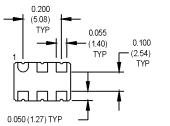


THIS PRODUCT IS NOT RECOMMENDED FOR NEW DESIGNS. PLEASE REFER TO THE MV3 or MV5 PRODUCT SERIES.

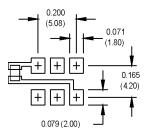








SUGGESTED SOLDER PAD LAYOUT



Pin Connections

PIN	FUNCTION
1	Control Voltage
2	Tristate
3	Ground & Gnd Plane
4	Output
5	N/C
6	+Vdd

	CMV/CMV3	Х	Х	00.000 MHz
Product S CMV = : CMV3 =				
Blank:	//Logic Compatibility — 40%/60% CMOS 45%/55% CMOS			
Blank:	ure Range 0°C to +70°C -40°C to +85°C			

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	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
	Frequency Range	F	2		52	MHz	
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆F/F					
	Overall		Inclusive of Calibration, Temperature, Voltage, Load, and Aging				
	0°C to +70°C		± 25 ppm				
	-40°C to +85°C			±40		ppm	
	Aging						
	1st Year		-3		+3	ppm	
	Thereafter (per year)		-1		+1	ppm	
Electrical Specifications	Pullability/APR						
	2.000 to 45.000 MHz			±120		ppm	
	45.001 to 52.000 MHz			±90		ppm	
	Control Voltage	Vc	0.5	2.5	4.5	٧	CMV
	-		0.3	1.65	3.0	V	CMV3
	Linearity				5	%	Positive Monotonic Slope
	Modulation Bandwidth	fm	20			kHz	±3dB
	Input Impedance	Zin	50k			Ohms	@ 10 kHz
	Input Voltage	Vdd	4.5	5.0	5.5	٧	CMV
			3.0	3.3	3.6	V	CMV3
	Input Current	ldd			30	mA	
	Output Type						HCMOS
	Load				15	pF	HCMOS
	Symmetry (Duty Cycle)		(See Ordering Information)				@ 50% Vdd
	Logic "1" Level	Voh	Vdd -0.5			٧	
	Logic "0" Level	Vol			0.5	٧	
	Output Current				20	mA	
	Rise Time	Tr			5	ns	20% to 80% Vdd, CL=15pF
	Fall Time	Tf			5	ns	80% to 20% Vdd, CL=15pF
	Tri-state Function		Input Logic "1" or floating: output active				
			Input Logic "0": output disables to high-Z				
	Start up Time				10	ms	
	Phase Jitter @ 26 MHz	φJ		4		ps RMS	Integrated 12 kHz - 20 MHz
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier
	@ 26 MHz	-65	-95	-115	-130	-140	dBc/Hz
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MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.



MtronPTI Lead Free Solder Profile

