# Low Noise, High IP3

# **Monolithic Amplifier**

## PMA-5455+

 $50\Omega$ 0.05 to 6 GHz





## **The Big Deal**

- Ultra Low Noise Figure, 0.8 dB
- High IP3/Low Current, 40mA
- Wideband, up to 6 GHz

3mm x 3mm MCLP (EIA: QFN) Pkg

### **Product Overview**

Mini-Circuits PMA-5455+ is a E-PHEMT based Ultra-Low Noise MMIC Amplifier operating from 50 MHz to 6 GHz with a unique combination of low noise and high IP3 making this amplifier ideal for sensitive receiver applications. This design operates on a single 5V supply at only 40 mA and is internally matched to 50 Ohms.

# **Key Features**

Feature	Advantages		
Ultra Low Noise, 0.8 dB	Outstanding Noise Figure, measured in a 50 Ohm environment without any external matching		
High IP3, 31 dBm	Combining Low Noise and High IP3 makes this MMIC amplifier ideal for Low Noise Receiver Front End (RFE) because it gives the user advantages at both ends of the dynamic range: sensitivity & two-tone spur-free dynamic range		
Low Current, 40 mA	At only 40mA, the PMA-5455+ is ideal for remote applications with limited available power or densely packed applications where thermal management is critical.		
Broad Band	Operating over a broadband the PMA-5455+ covers the primary wireless communications band Cellular, PCS, LTE, WiMAX		
Internally Matched	No external matching elements required to achieve the advertised noise and output power over the full band		
MCLP Package	Low Inductance, repeatable transitions, excellent thermal pad		
Max Input Power, +20 dBm	Ruggedized design operates up to input powers of +20 dBm without the need of an external lim		
High Reliability	Low, small signal operating current of 40mA nominal maintains junction temperatures typically below 110°C at 85°C ground lead temperature		

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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

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# **Monolithic Amplifier**

0.05-6 GHz

### **Product Features**

- Single Positive Supply Voltage, 5V, Id=40mA
- Ultra Low Noise Figure, 0.8 dB typ. at 1GHz
- High IP3, 31 dBm typ. 1GHz
- Gain, 19 dB typ. at 1 GHz
- Output Power, up to +19.5 dBm typ.
- Micro-miniature size 3mm x 3mm
- Aqueous washable

### Typical Applications

- Cellular
- ISM
- GSM
- WCDMA
- LTE
- WiMAX
- WLAN
- UNII and HIPERLAN



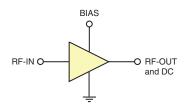
+RoHS Compliant

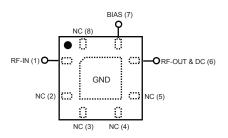
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

### **General Description**

PMA-5455+ is a high dynamic range, low noise, high IP3, high output power, monolithic amplifier. Manufactured using E-PHEMT\* technology enables it to work with a single positive supply voltage. Unconditionally stable over the operating frequency.

### simplified schematic and pad description





Function	Pad Number	Description (See Application Circuit, Fig. 3)	
RF-IN	1	RF input pad	
RF-OUT & DC	6	RF output pad (connected to RF-OUT via blocking external cap C2, and Supply voltage Vs via RF Choke L1)	
BIAS	7	Bias pad (connected to Vs via Rbias)	
GND	paddle in center of bottom	Connected to ground	
NOT USED	2,3,4,5,8	No internal connection; recommended use: per PCB Layout PL-299	

<sup>\*</sup>Enhancement mode Pseudomorphic High Electron Mobility Transistor.

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### Electrical Specifications<sup>(1)</sup> at 25°C, Zo=50 $\Omega$ (refer to characterization circuit, see Fig. 1)

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		0.05		6.0	GHz
DC Voltage (V <sub>d</sub> )			5.0		V
DC Current (I <sub>d</sub> ) <sup>(6)</sup>		30	40	60	mA
DC Current (I <sub>Rbias</sub> )			1.2		mA
- C C C C C C C C C C C C C C C C C C C	0.05		1.8	_	
	0.5		0.8	_	dB
	1.0		0.8	_	
	2.0		1.0	1.3	
Noise Figure	3.0		1.3	_	
	4.0		1.6	_	
	5.0		2.1	_	
	6.0		2.4	_	
	0.05	_	25.1	_	dB
	0.5	-	22.6	_	
	1.0	-	19.0	_	
Cain	2.0	12.6	14.0	15.4	
Gain	3.0	_	10.9	_	
	4.0	-	8.9	_	
	5.0	_	7.1	_	
	6.0	_	5.6	_	
Input Return Loss	0.05-0.5		9.0		dB
IIIput Hetuiii Loss	0.5-6		7.0		
Output Return Loss	0.05-0.3		15.0		dB
Output Hetain 2000	0.3-6		20.0		u u u
	0.05		29.1		dBm
	0.5		30.0		
	1.0		31.0		
Output IP3	2.0		32.7		
Output IF3	3.0		33.3		
	4.0		32.9		
	5.0		33.8		
	6.0		34.2		
	0.05		18.8		dBm
	0.5		19.3		
	1.0		19.6		
Output Power @ 1 dB compression (2)	2.0		19.3		
	3.0		19.1		
	4.0		19.1		
	5.0		19.9		
	6.0		19.8		-
DC Current Variation vs. Temperature (3)			-0.072		mA/°C
Thermal Resistance			128		°C/W

### Absolute Maximum Ratings(4)

- 110 G G 1 G 1 G 1 G 1 G 1 G 1 G 1 G 1 G				
Parameter	Ratings			
Operating Temperature (5)	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			
Channel Temperature	150°C			
DC Voltage (Pad 6)	6V			
Power Dissipation	500mW			
DC Current (Pad 6)	100mA			
Bias Current (Pad 7)	10mA			
Input Power (7)	20dBm			

- (1) Measured on Mini-Circuits Characterization test board TB-502+.
- See Characterization Test Circuit (Fig. 1) (2) P1dB specified with external current limiting of 50mA;
- Capable of higher P1dB at higher current (see Fig. 2)
- (3) (Current at 85°C Current at -45°C)/130
- (4) Permanent damage may occur if any of these limits are exceeded. These maximum ratings are not intended for continuous normal operation.
- (5) Defined with reference to ground pad temperature.
- (6) Specified DC current consumption is under small signal conditions. Current will increase with input RF Power. To maintain maximum current consumption, external DC current limiting circuits are required on Vd line.
- (7) Maximum input power is specified based upon external Vd current limiting of 60 mA. Maximum input power will degrade without external current limiting.

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### Characterization Test Circuit

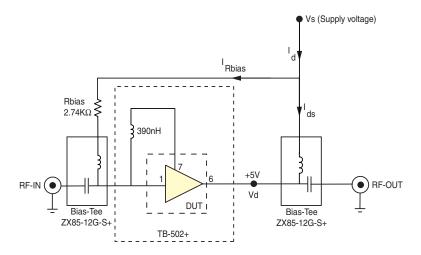
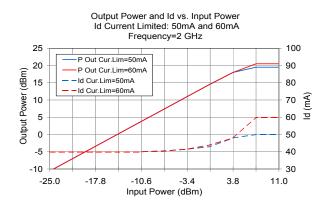


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-502+) Gain, Output power at 1dB compression (P1dB), Output IP3 (OIP3) and Noise Figure measured using Agilent's N5242A PNA-X Microwave network analyzer.

### Conditions:

- 1. Gain: Pin=-25 dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 5 dBm/tone at output.
- 3. Vs adjusted for 5V at device (Vd), compensating loss of bias tee.



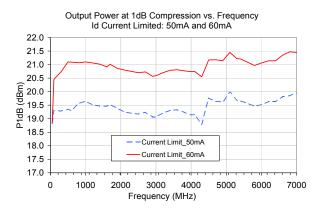


Fig 2. Output Power and Id vs. Input Power and Frequency. Performance measured on Mini-Circuits Characterization test board TB-502+. See Characterization Test Circuit (Fig. 1) Measurements performed with current (Id) limited as noted.

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### **Recommended Application Circuit**

(refer to evaluation board for PCB Layout and component values)

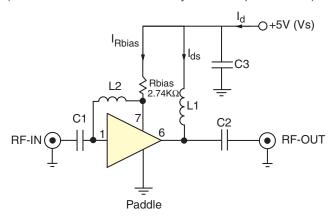


Fig 3. Recommended Application Circuit Note: Resistance of L1, 0.1- $0.2\Omega$  typically

### Typical Current (Id) as a function of Rbias (Vd = 5V)

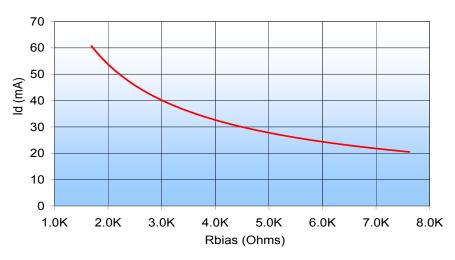
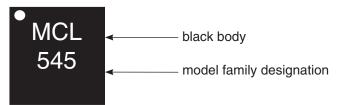


Fig 4. Id varies as a function of Rbias. The Id current range is defined based upon the specific Rbias value noted in the Application Circuit (Fig 3). Rbias may be adjusted to optimize Id for a customers' application. RF performance will vary accordingly.

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### **Product Marking**



### Additional Detailed Technical Information

Additional information is available on our web site www.minicircuits.com. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: DQ849

Plastic package, exposed paddle, lead finish: tin-silver over nickel

Tape & Reel: F104

Standard quantities availabe on reel: 7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices

Suggested Layout for PCB Design: PL-299

**Evaluation Board:** TB-501-5+ (50-5000 MHz)

**Environmental Ratings: ENV08T1** 

### **ESD Rating**

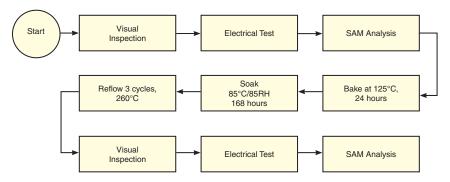
Human Body Model (HBM): Class 1A (250V to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 40V

### **MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

### **MSL Test Flow Chart**



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