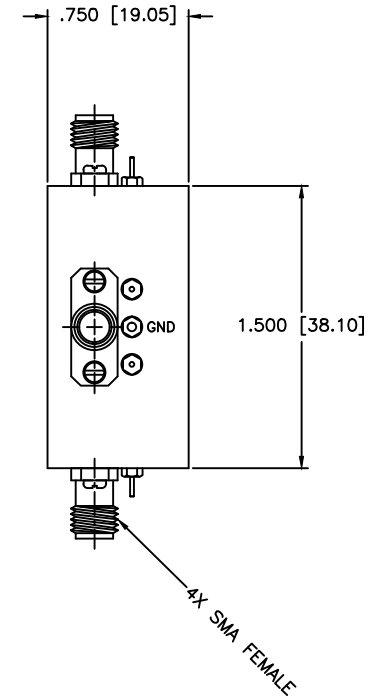
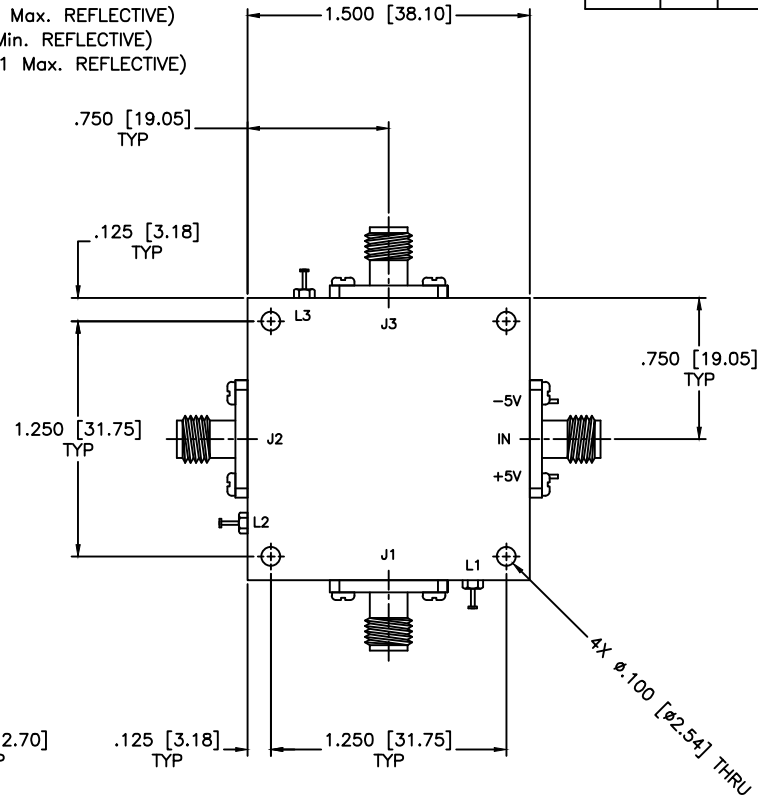
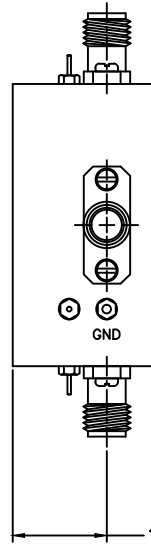


SPECIFICATIONS

FREQUENCY RANGE: 2–18 GHz
 INSERTION LOSS: 3.5dB Max. (3.0dB Max. REFLECTIVE)
 ISOLATION: 55dB Min. (60dB Min. REFLECTIVE)
 VSWR: 2.50:1 Max. (2.00:1 Max. REFLECTIVE)
 INPUT POWER mW/dBm: 200/23
 SWITCHING TIME: 100 nSEC



DRIVER SCHOTTKY TTL
 VOLTAGE / CURRENT REQUIRED:
 +5 Vdc @ +70 mA, Typical
 -5 Vdc @ -50 mA, Typical

INDIVIDUAL TTL CONTROL LOGIC:
 LOGIC "0" (-0.3V to +0.8V)=ON
 LOGIC "1" (+2.0V to +5.0V)=OFF

TTL Control logic

L1	J2	J3	IN-J1	IN-J2	IN-J3
0	1	1	INSERTION LOSS	ISOLATION	ISOLATION
1	0	1	ISOLATION	INSERTION LOSS	ISOLATION
1	1	0	ISOLATION	ISOLATION	INSERTION LOSS

REVISIONS				
ZONE	REV	DESCRIPTION	Date	APPROVED

MATERIAL:		MCLI MICROWAVE COMMUNICATIONS LABORATORIES INC. 7255 30TH AVE. N. SAINT PETERSBURG, FL 33710 TEL: (727) 344-6254 FAX: (727) 381-6116 http://www.MCLI.COM		SCALE: N/A
UNLESS OTHERWISE SPECIFIED: TOLERANCES IN (INCHES) OR [mm] (MILLIMETERS) FRACTIONS: $\pm 1/64$ DECIMALS: $\pm .02 \pm .009$ ANGLE: $\pm 1/2^\circ$		DRAWING TITLE: SP3T, PIN DIODE SWITCHES		
DRAWN: Paiboon Luekhamhan DATE: 02-03-2009		PART NO. D3-9 CAGE CODE: OD2L5 SIZE: A REV: N/A DO NOT SCALE THE DRAWING		
APPROVED: T. Nguyen DATE: 02-03-2009		DWG NO. 84127		