



## Ka-Band (29-31GHz) BUC with Integrated Linearizer (TSA-214144)



### Summary

This is a Ka-Band Block Up-Converter (BUC) with an integrated IF Linearizer that is designed to generate 29-30GHz or 30-31GHz from a 1-2GHz IF Band. The output band is selected via an RS-422 command, as is gain control, mute, and reference frequency selection. The RS-422 interface also allows reporting of the input power to the mixer, internal temperature, and local oscillator (LO) lock detect.

The unit uses a fully integrated IF Linearizer ASIC which can be used to linearize a Teledyne Microwave Solutions SSPA. As this uses an intelligent adaptive technique, SSPAs from other manufacturers may also be linearized. The achievable improvement in linearity depends on the type of SSPA, modulation scheme, and symbol rates used (measured improvement for a TMS amplifier is shown in Figure 2). Feedback of the RF signal is required to utilize the RF linearizer: approximately +10dBm is required at the RF feedback input.

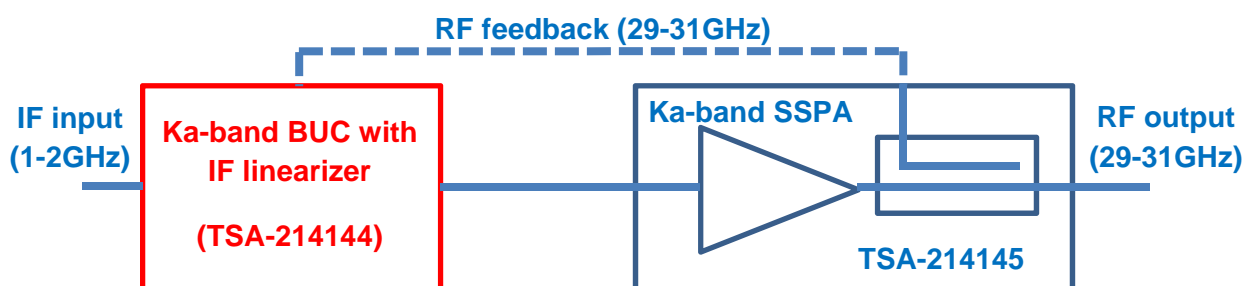


Figure 1: block diagram of the Ka-band dual band BUC implemented in TX lineup.

The unit has a wide operating voltage of +20.5V to +31.5V. The total weight is 1.5 lbs max and the size is 6" L x 4" W x 1" H. See the outline drawing attached



## Specifications

Parameter	Value
<b>Operating Frequency Range</b>	29 to 30 GHz and 30 to 31 GHz
<b>Operating Temperature Range</b>	-55 to +70 °C
<b>Max RF Input Power</b>	+5 dBm
<b>Small-Signal Gain</b> (Controlled Via RS-422)	30 dB typ.
<b>Gain Variation</b> Over any 40MHz IF BW	±0.75 dB
Over full IF BW	±2.0 dB
<b>Gain Variation Over Temperature</b>	±2.0 dB
<b>VSWR Input</b>	1.5:1
<b>VSWR Output</b>	1.5:1
<b>Output Spurious</b> 2 <sup>nd</sup> Harmonic <b>Pin=-23dBm</b> (2xIF)+LO & DC-DC Spurs	-60 dBc
<b>Phase Noise</b> 100 Hz Offset	-62 dBc/Hz
1 kHz Offset	-75 dBc/Hz
10 kHz Offset	-75 dBc/Hz
100 kHz Offset	-93 dBc/Hz
1 MHz Offset	-122 dBc/Hz
10 MHz Offset	-125 dBc/Hz
<b>RMS Noise (w/ 10 MHz Ext. Ref)</b>	<1.75 deg
<b>Spurious @ Pout = 42dBm</b> (29-31GHz) In Band	-60dBc
<b>Reference Clock Input Freq.</b>	10 or 50MHz (Selectable through RS-422)
<b>Reference Clock Input Level</b>	0dBm ± 3dB
<b>External Reference Clock Phase Noise Requirement</b>	10 Hz Offset -120dBc/Hz 100 Hz Offset -145dBc/Hz 1 kHz Offset -165dBc/Hz
<b>Lock Detect</b>	Indicator of Lock to Reference via RS-422
<b>Input Voltage</b>	+20.5V to +31.5V
<b>Detected Power @ Input of Mixer</b>	Read through RS-422
<b>Internal Temperature</b>	Read through RS-422
<b>Mute</b>	Control Via RS-422 or Discrete Pin
<b>Reference Input Connector</b>	SMA (F)
<b>IF Input Connector</b>	N (F)
<b>Feedback Input Connector</b>	2.92mm (F)
<b>Output Connector</b>	2.92mm (F)
<b>DC, Monitor, Control Connector</b>	15 Pin Micro-D
<b>Size</b>	6" L x 4" W x 1" H
<b>Weight</b>	1.5 lbs maximum



## Example of linearizer performance.

An example of the linearity improvement achievable using the IF linearizer is shown below in figure 2.

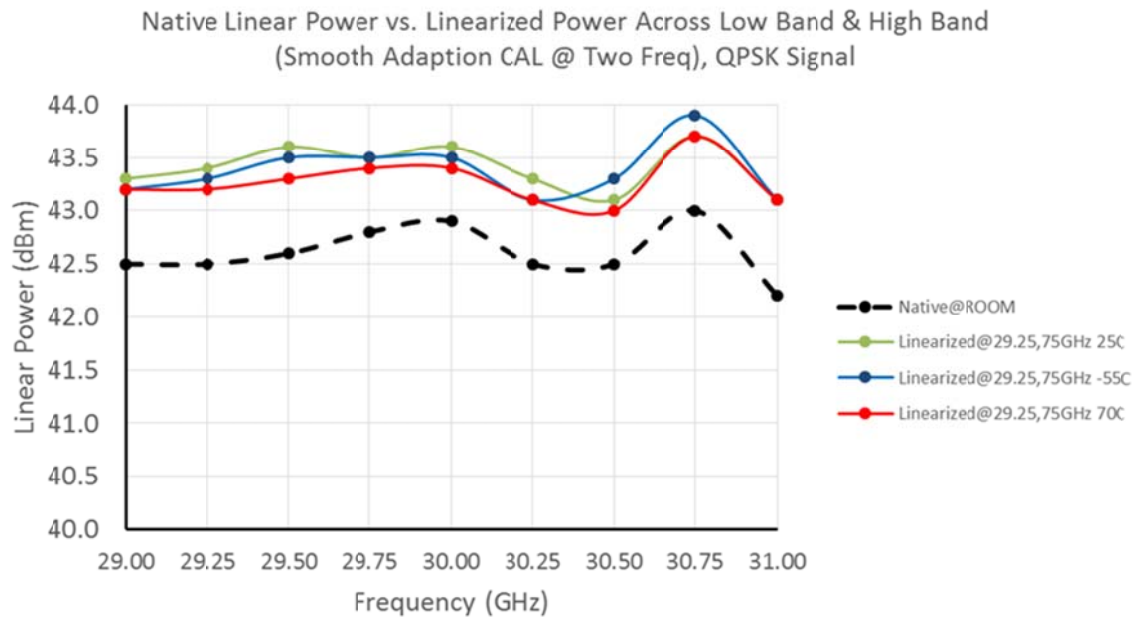


Figure 2. Performance enhancement gained through the use of the adaptive IF linearizer function. (Modulation parameters: QPSK, Symbol Rate = 10Msps, Filter = Root Raised Cosine, Filter Roll-off=0.2, Point of Linearity = 1 symbol rate offset from carrier to be -30dBc)

Mechanical Outline:

