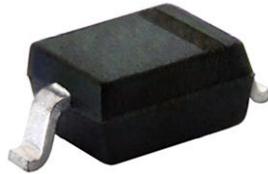




Small Signal Fast Switching Diode



FEATURES

- Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified available (part number on request)
- Base P/N-G3 - green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



MECHANICAL DATA

Case: SOD-323

Weight: approx. 4 mg

Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box

08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE				
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS
1N4448WS-G	1N4448WS-G3-08 or 1N4448WS-G3-18	Single diode	AJ	Tape and reel

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	75	V
Repetitive peak reverse voltage		V _{RRM}	100	V
Average rectified current half wave rectification with resistive load ⁽¹⁾	f ≥ 50 Hz	I _{F(AV)}	150	mA
Surge forward current	t < 1 s and T _j = 25 °C	I _{FSM}	350	mA
Power dissipation ⁽¹⁾		P _{tot}	200	mW

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	650	K/W
Junction temperature		T _j	150	°C
Storage temperature range		T _{stg}	-65 to +150	°C
Operating temperature range		T _{op}	-55 to +150	°C

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 100 mA	V _F			1	V
	I _F = 5 mA	V _F	0.620		0.720	V
Leakage current	V _R = 20 V	I _R			25	nA
	V _R = 75 V	I _R			5	μA
	V _R = 20 V, T _j = 150 °C	I _R			50	μA
Diode capacitance	V _F = V _R = 0 V	C _D			4	pF
Reverse recovery time	I _F = 10 mA, i _R = 1 mA, V _R = 6 V, R _L = 100 Ω	t _{rr}			4	ns

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

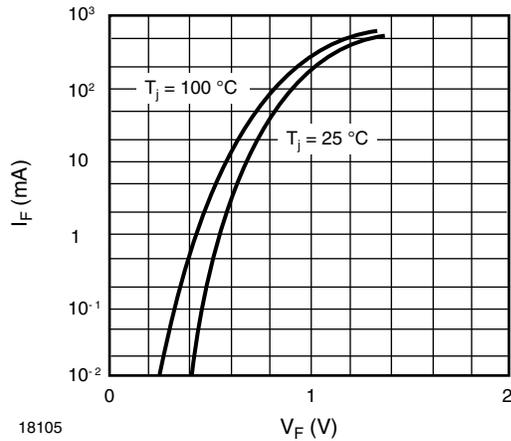


Fig. 1 - Forward Characteristics

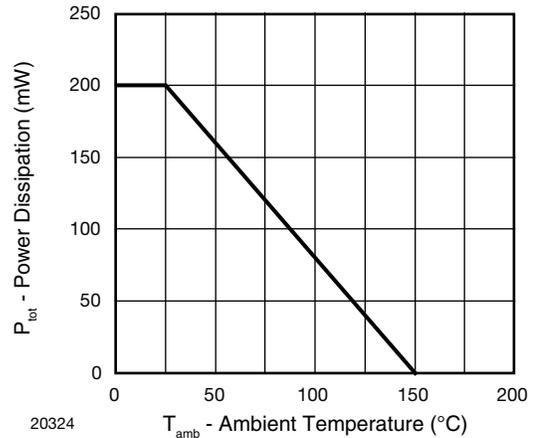


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

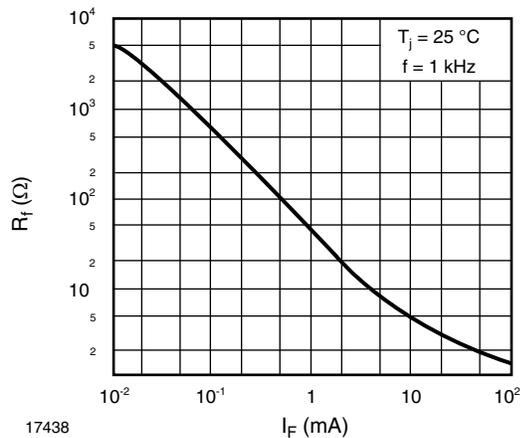


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

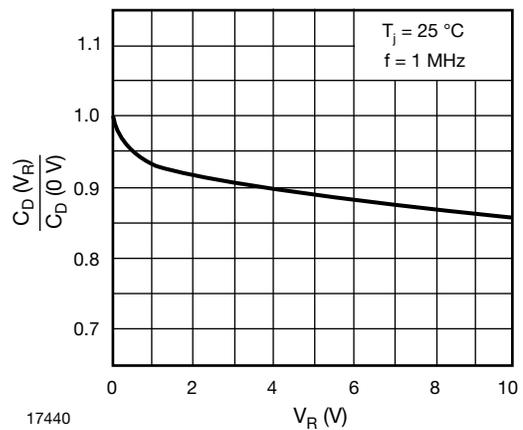


Fig. 4 - Relative Capacitance vs. Reverse Voltage

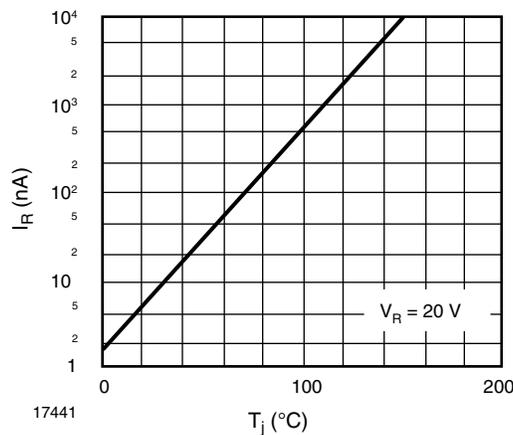


Fig. 5 - Leakage Current vs. Junction Temperature

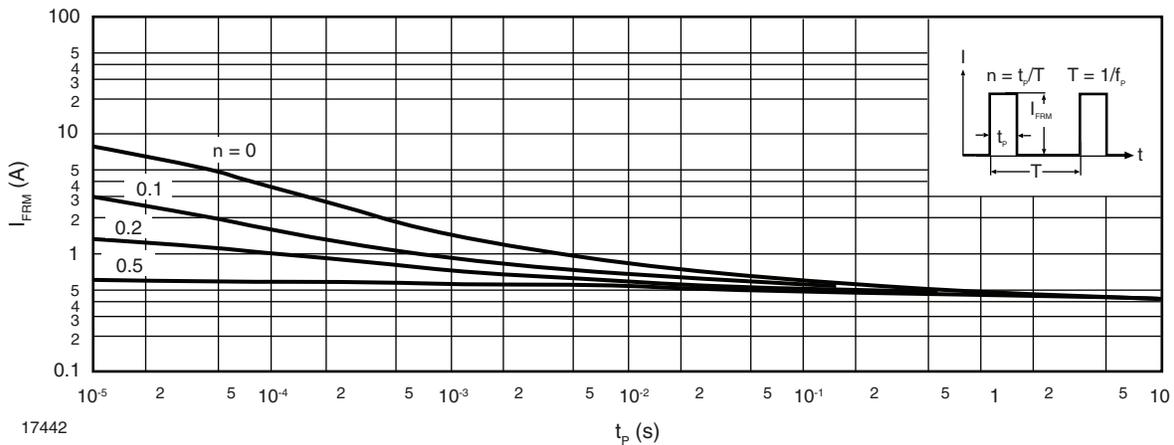
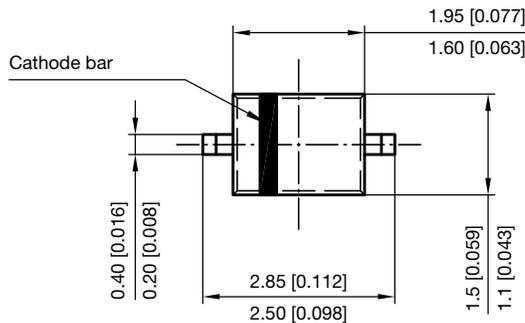
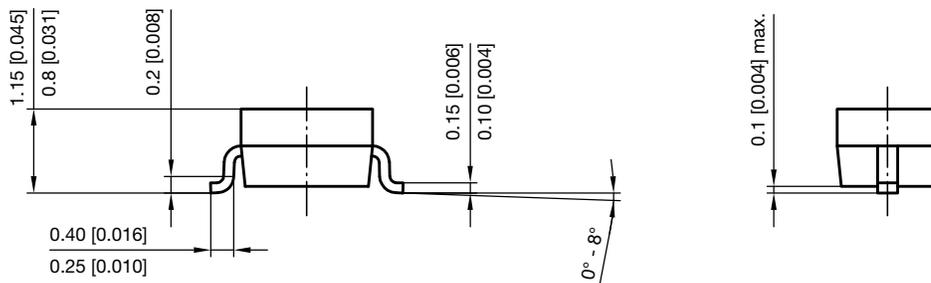
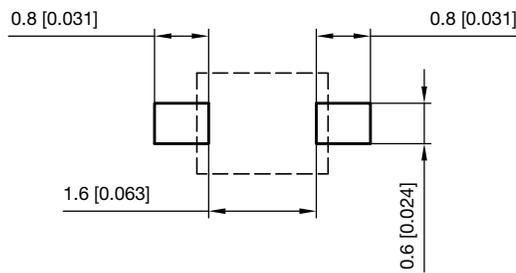


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

PACKAGE DIMENSIONS in millimeters (inches): **SOD-323**



Footprint recommendation:



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 Created - Date: 24.August.2004
 Rev. 6 - Date: 23.Sept.2016
 17443



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