Frequency Synthesizer

KSN-2130A-119+

50Ω 1850 to 2130 MHz

The Big Deal

- · Low phase noise and spurious
- · Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK801

Product Overview

The KSN-2130A-119+ is a Frequency Synthesizer, designed to operate from 1850 to 2130 MHz for UMTS application. The KSN-2130A-119+ is packaged in a metal case (size of 0.80° x 0.58° x 0.15°) to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -93 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -80 dBc typ. • Reference Spurious: -115 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-2130A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-2130A-119+ to be used in compact designs.







Frequency Synthesizer

KSN-2130A-119+

50Ω 1850 to 2130 MHz

Features

- Integrated VCO + PLL
- · Low phase noise and spurious
- · Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+5V)
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK801 PRICE: \$29.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

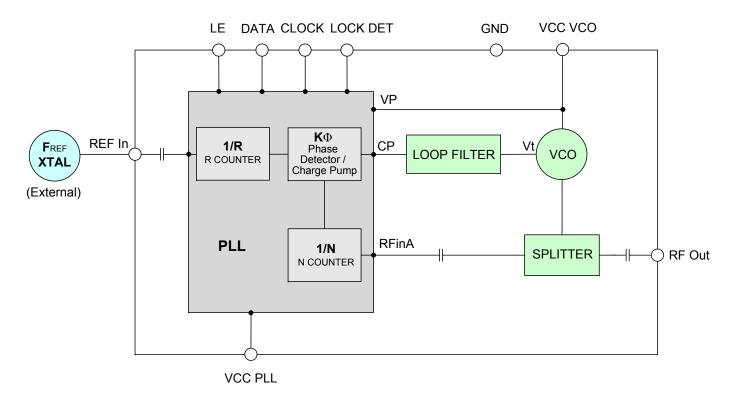
Applications

UMTS

General Description

The KSN-2130A-119+ is a Frequency Synthesizer, designed to operate from 1850 to 2130 MHz for UMTS application. The KSN-2130A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-2130A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic





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REV. A M129968 EDR-8436MPF1 KSN-2130A-1194 Category-A1 RAV 101221 Page 2 of 11

Notes: 1. Performance and quality attributes and conditions not expressly stated in this specification sheet are intended to be excluded and do not form a part of this specification sheet. 2. Electrical specifications and performance data contained herein are based on Mini-Circuits applicable established test performance criteria and measurement instructions. 3. The parts covered by this specification sheet are subject to Mini-Circui standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this rat en entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp.

Electrical Specifications (over operating temperature -30°C to +80°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units	
Frequency Range		-	1850	-	2130	MHz	
Step Size		-	-	50	-	kHz	
Settling Time		Within ± 1 kHz	-	30	-	mSec	
Output Power		-	+3.0	+6.0	+9.0	dBm	
		@ 100 Hz offset	-	-51	-		
		@ 1 kHz offset	-	-65	-57		
SSB Phase Noise		@ 10 kHz offset	-	-93	-84	dBc/Hz	
		@ 100 kHz offset	-	-114	-106		
		@ 1 MHz offset	-	-134	-127		
Reference Spurious Suppress	sion	Ref. Freq. 14.4 MHz	-	-115	-87		
Comparison Spurious Suppre	ssion	Step Size 50 kHz	-	-80	-55	-ID-	
Non - Harmonic Spurious Sup	pression	-	-	-90	-	dBc	
Harmonic Suppression		-	-	-26	-15		
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	- V	
PLL Supply Voltage		+5.00	+4.75	+5.00	+5.25] v	
VCO Supply Current		-	-	20	28	A	
PLL Supply Current		-	-	11	19	- mA	
	Frequency	14.4 (square wave)	-	14.4	-	MHz	
Reference Input	Amplitude	1	-	1	-	V _{P-P}	
(External)	Input impedance	-	-	100	-	ΚΩ	
	Phase Noise @ 1 kHz offset	-	-	-130	-	dBc/Hz	
RF Output port Impedance		-	-	50	-	Ω	
Input Logic Level	Input high voltage	-	3.55	-	-	V	
Imput Logic Level	Input low voltage	-	-	-	0.75	V	
Digital Lock Detect	Locked	-	3.55	-	4.45	V	
Digital Lock Detect	Unlocked	-	-	-	0.40	V	
Frequency Synthesizer PLL	-	ADF4113					
PLL Programming		-	3-wire serial 4.2V CMOS				
	F_Register	-	(MSB) 100	(MSB) 1001111111000000010010011 (LSB)			
Register Map @ 2130 MHz	N_Register	-	(MSB) 001001010011001100100001 (LSB)				
	R_Register	-	(MSB) 00010000000010010000000 (LSB)				

Absolute Maximum Ratings

- 100001010 11001110111 1 1001111 9 0						
Parameters	Ratings					
VCO Supply Voltage	6.3V					
PLL Supply Voltage	6.3V					
VCO Supply Voltage to PLL Supply Voltage	N.A					
Reference Frequency Voltage	-0.3Vmin, +4.25Vmax					
Data, Clock, LE Levels	-0.3Vmin, +4.25Vmax					
Operating Temperature	-40°C to +85°C					
Storage Temperature	-55°C to +100°C					

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	PO	POWER OUTPUT			VCO CURRENT			PLL CURENT		
(MHz)		(dBm)			(mA)			(mA)		
, ,	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C	
1850	4.67	5.20	6.15	17.40	19.02	20.83	9.50	11.05	13.13	
1882	4.23	5.44	5.96	17.44	19.19	20.93	9.52	11.07	13.16	
1918	4.72	4.82	6.31	17.77	19.29	21.29	9.53	11.08	13.18	
1954	4.61	5.53	6.32	17.89	19.70	21.47	9.54	11.09	13.20	
1990	4.20	5.46	6.06	18.04	19.82	21.64	9.55	11.11	13.22	
2026	4.92	4.92	6.52	18.44	20.03	22.10	9.56	11.12	13.22	
2062	4.25	5.83	6.20	18.43	20.52	22.10	9.57	11.14	13.24	
2098	4.31	5.45	5.93	18.83	20.56	22.48	9.58	11.15	13.26	
2130	4.71	4.98	6.49	19.04	20.83	22.85	9.59	11.16	13.27	

FREQUENCY	HARMONICS (dBc)								
(MHz)		F2			F3				
, ,	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C			
1850	-39.06	-42.19	-40.05	-28.20	-29.51	-27.80			
1882	-41.98	-45.26	-45.47	-25.64	-28.65	-27.52			
1918	-48.78	-48.97	-49.31	-26.45	-27.07	-26.70			
1954	-42.51	-44.73	-41.33	-25.55	-26.62	-24.98			
1990	-37.31	-39.26	-38.80	-25.45	-25.99	-24.12			
2026	-36.11	-34.89	-36.85	-25.56	-24.38	-23.08			
2062	-34.21	-35.10	-34.09	-23.88	-24.16	-21.61			
2098	-30.48	-34.87	-32.58	-21.82	-23.23	-20.29			
2130	-32.84	-32.01	-34.19	-19.44	-20.83	-19.82			



FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
(MHz)			+25°C					
, ,	100Hz	1kHz	10kHz	100kHz	1MHz			
1850	-58.14	-64.15	-91.75	-112.29	-133.12			
1882	-55.42	-67.92	-93.02	-113.84	-134.79			
1918	-53.86	-61.89	-90.03	-110.95	-131.54			
1954	-54.99	-64.44	-93.23	-114.46	-135.15			
1990	-54.36	-66.15	-93.22	-114.11	-134.83			
2026	-49.29	-61.76	-90.91	-112.44	-133.11			
2062	-54.91	-67.79	-95.26	-115.87	-137.29			
2098	-50.64	-67.59	-93.77	-115.10	-136.16			
2130	-46.78	-66.72	-93.24	-114.73	-135.58			

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS						
(MHz)			-35°C				
, ,	100Hz	1kHz	10kHz	100kHz	1MHz		
1850	-57.27	-63.13	-89.70	-111.41	-132.91		
1882	-56.31	-60.39	-86.90	-109.29	-130.78		
1918	-53.31	-64.26	-89.79	-112.43	-133.81		
1954	-57.93	-62.37	-89.34	-112.01	-133.29		
1990	-58.13	-59.94	-87.65	-110.30	-131.65		
2026	-55.14	-64.61	-91.72	-114.27	-135.56		
2062	-55.88	-61.94	-88.49	-110.85	-132.60		
2098	-54.30	-63.89	-90.25	-112.98	-134.34		
2130	-53.09	-66.11	-92.15	-115.10	-136.48		

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS									
(MHz)	+85°C									
	100Hz	1kHz	10kHz	100kHz	1MHz					
1850	-60.01	-66.73	-92.90	-113.55	-134.17					
1882	-61.20	-62.75	-91.34	-112.57	-132.82					
1918	-59.97	-66.56	-94.07	-114.62	-135.58					
1954	-58.74	-68.10	-93.50	-114.83	-135.35					
1990	-57.49	-63.51	-92.71	-113.28	-134.56					
2026	-54.84	-66.50	-95.64	-117.02	-137.65					
2062	-55.78	-64.71	-92.98	-114.19	-135.39					
2098	-55.05	-66.21	-93.62	-115.06	-136.13					
2130	-53.15	-66.16	-95.28	-117.17	-137.99					



COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @ Fcarrier 1850MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 1990MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 2130MHz+(n*Fcomparison) (dBc) note 1		
n	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C
-5	-91.66	-104.23	-101.66	-88.19	-96.18	-99.15	-101.17	-102.67	-103.15
-4	-87.33	-101.36	-97.18	-84.73	-93.63	-90.85	-97.43	-103.02	-103.85
-3	-87.12	-94.27	-93.64	-80.34	-95.87	-91.90	-86.99	-100.48	-96.71
-2	-83.37	-87.94	-88.99	-79.40	-92.01	-83.51	-82.91	-90.56	-89.73
-1	-79.60	-82.68	-82.51	-71.92	-78.34	-71.41	-81.07	-80.00	-79.38
o ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-80.42	-85.26	-81.64	-71.05	-78.11	-71.32	-79.49	-78.83	-80.55
+2	-78.02	-88.96	-88.61	-77.58	-89.81	-82.00	-85.50	-92.24	-89.41
+3	-86.28	-93.54	-94.26	-85.26	-95.03	-92.73	-88.52	-99.17	-98.70
+4	-89.10	-102.10	-99.14	-86.26	-94.49	-91.35	-90.83	-101.17	-106.31
+5	-98.35	-105.70	-102.22	-86.04	-95.77	-103.37	-98.46	-101.01	-104.81

Note 1: Comparison frequency 50 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 1850MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @ Fcarrier 1990MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @ Fcarrier 2130MHz+(n*Freference) (dBc) note 3		
n	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C	-35°C	+25°C	+85°C
-5	-119.52	-121.65	-122.26	-120.85	-123.45	-123.23	-124.89	-122.61	-124.99
-4	-111.69	-111.74	-114.39	-115.76	-114.28	-114.09	-117.28	-114.17	-116.33
-3	-123.04	-126.36	-125.56	-121.27	-127.45	-126.17	-124.55	-126.51	-128.66
-2	-113.84	-112.15	-112.99	-119.25	-112.08	-114.22	-116.79	-115.99	-117.68
-1	-113.36	-117.68	-113.84	-111.27	-121.49	-115.88	-103.99	-106.95	-103.83
o ^{note 4}	-	-	-	-	-	-	-	-	-
+1	-113.27	-117.55	-113.48	-110.99	-120.05	-116.68	-103.78	-105.97	-102.24
+2	-113.24	-111.99	-112.43	-114.23	-114.00	-113.80	-117.68	-116.35	-117.39
+3	-119.31	-125.73	-124.18	-122.00	-125.67	-126.23	-124.32	-128.20	-130.05
+4	-112.59	-111.88	-113.11	-114.84	-113.60	-114.99	-116.95	-116.43	-118.39
+5	-118.14	-123.70	-121.37	-118.14	-123.67	-124.20	-122.10	-127.61	-128.03

Note 3: Reference frequency 14.4 MHz

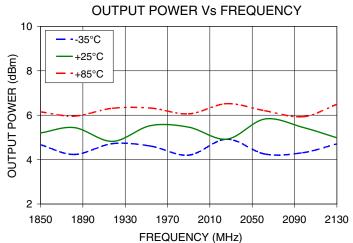
Note 4: All spurs are referenced to carrier signal (n=0).



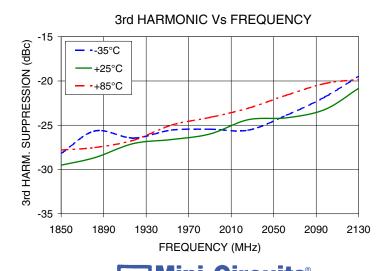
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Typical Performance Curves



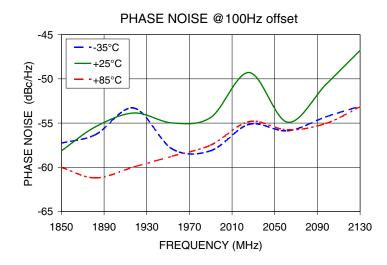
2nd HARMONIC Vs FREQUENCY -20 2nd HARM. SUPPRESSION (dBc) --35°C +25°C -30 +85°C -40 1850 1890 1930 1970 2010 2050 2090 2130 FREQUENCY (MHz)

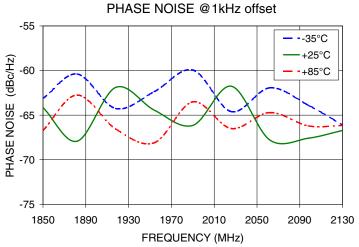


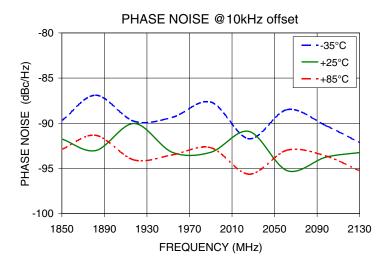
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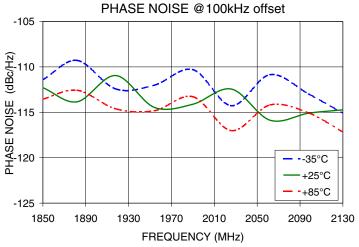
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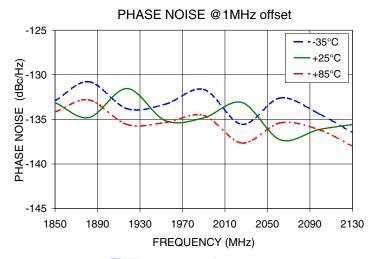
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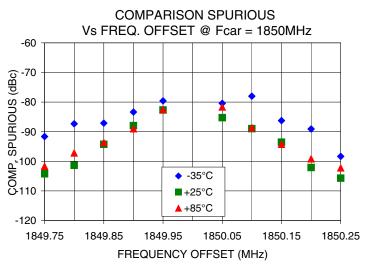
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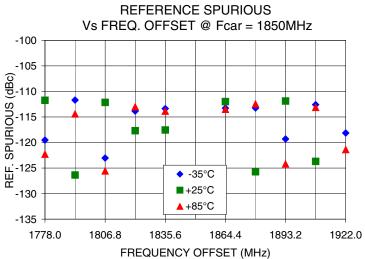
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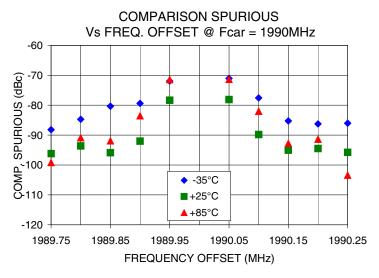
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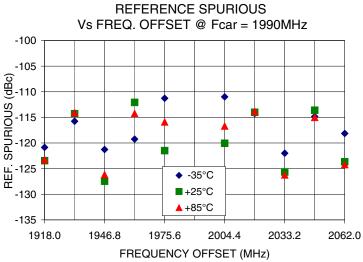
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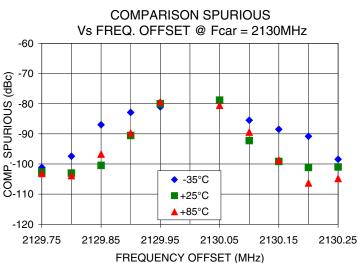


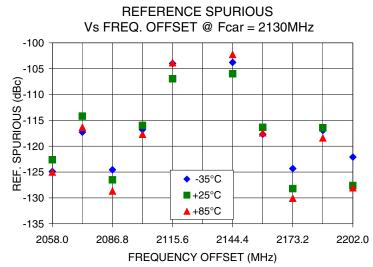












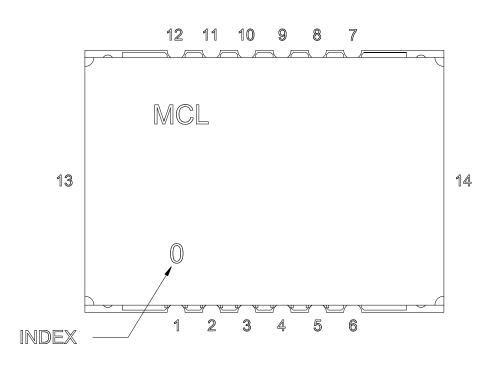
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Pin Configuration

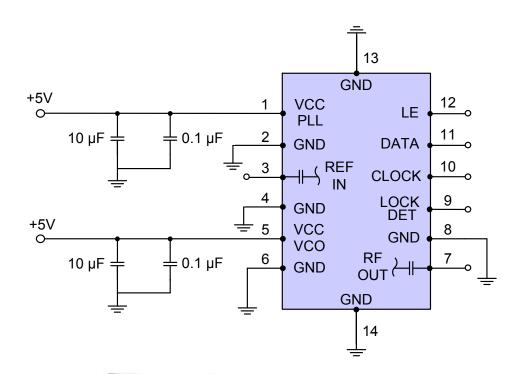


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

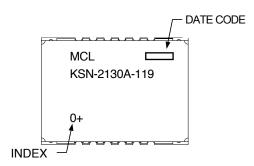




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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK801

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567+

Environment Ratings: ENV03T2



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