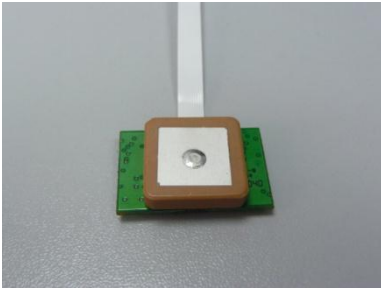


Fully Integrated GPS Module Including Antenna ORG4402 Data Sheet



1. Interface

1.1. Pin Assignment

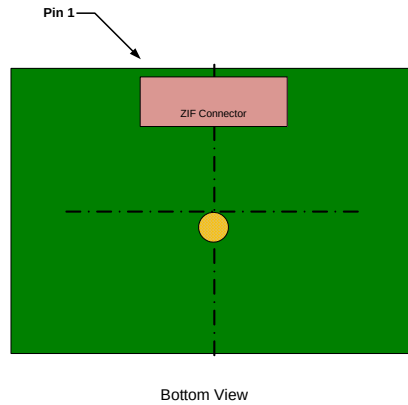


Figure 1-1: ORG4402

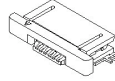
Pad Number	Pad Name	Pad Description	Direction	Default	Notes
1	GND	System Ground	Power		
2	1PPS	UTC Time Mark	Output	Low	1.8V compatible
3	GPS_nRESET	Asynchronous Reset	Input	High	
4	GPS_RX	UART Receive	Input	High	$V_{IH(max)} = 3.6V$
5	WAKEUP	Power State Indicator	Output		High for Full Power, Low for Hibernate
6	GPS_ON	Power State Control	Input	Low	Toggle Low-High-Low for power up
7	GPS_EN	Module Enable	Input	High	Drive low to inhibit the module
8	GPS_TX	UART Transmit	Output	Low	$V_{OH} = V_{CC} - 0.1V$
9	V _{CC}	System Power	Power		2.1- 5.5V
10	NC	Not Connected			

Table 1-1: ORG4402 pin-out

1.2. Connectivity

Interface connector

The ORG4402 interface connector is 10 position, 0.50mm (.020") Pitch, FFC/FPC ZIF Right Angle Molex p/n 527461071



Power

The ORG4402 series module requires only one power supply V_{CC} , which can be supplied directly from a battery since the module has internal regulators.

It is recommended to keep the power supply on all the time in order to maintain the non-volatile RTC and RAM active for fastest possible TTFF. When the V_{CC} is powered off settings are reset to factory default and the receiver performs Cold Start on next power up.

Power supply

V_{CC} range is 2.1 to 5.5V DC.

Typical I_{CC} current is 23mA@3.3V during acquisition. Peak I_{CC} current is 50 mA.

Power supply current consumption varies according to the processor load and satellite acquisition.

Typical I_{CC} current in Hibernate state is 50 μ A.

Voltage ripple below 50mV_{pp} allowed for frequency between 100KHz and 3MHz.

Voltage ripple below 15mV_{pp} allowed for frequency above 3MHz.

Higher voltage ripple may compromise the ORG4402 module performance.

The under voltage lockout (UVLO) circuit prevents the device from misoperation at low input voltages. The UVLO circuit prevents the integrated DC-DC switch-mode regulator from turning on the switch or rectifier MOSFET under undefined conditions. It has a UVLO threshold set to 1.8V.

Fully functional operation is permitted for input voltage down to the falling UVLO threshold level.

The converter starts operation again once the input voltage trips the rising UVLO threshold level.

Ground

Single Ground pad should be connected to the main Ground with shortest possible trace or via.

Enable input

The Enable control input can be used to fully inhibit the ORG4402 module.

Enable signal is active low and has internal 81k Ω pull-up resistor.

Do not connect to this input if the feature is not in use.

Host Control Interface

ON OFF input

The ON_OFF control input can be used to switch the receiver between Hibernate or Full Power states and also to generate interrupt in Push-to-Fix operation.

The ON_OFF interrupt is generated by a low-high-low toggle, which should be longer than 62 μ s and less than 1s (100ms pulse length recommended).

ON_OFF interrupts with less than 1 sec intervals are not recommended. Multiple switch bounce pulses are recommended to be filtered out.

Input levels are 1.8 to 3.6V.

ON_OFF input is rising edge triggered. Do not drive high permanently or pull-up this input.

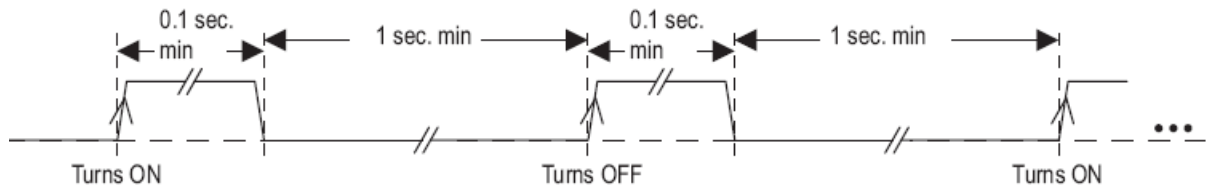


Figure 7-1: ON_OFF timing

WAKEUP output

The WAKEUP pad is an output from the ORG4402 used to flag for power state.

A low on this output indicates that the module is in one of its low-power states: Hibernate or Standby.

A high on this output indicates that the module is in Full Power state.

Wakeup output is LVCMOS 1.8V compatible.

Do not load this output if the feature is not in use.

nRESET input

The Power-on-Reset (POR) is generated internally in the ORG4402 module.

Additionally, manual reset option is available through nRESET pad.

Resetting the module clears the RTC block and configuration settings become default.

nRESET pad is active low and has internal pull-up resistor.

nRESET signal should be applied for at least 1 μ s.

nRESET input has internal pull-up of 86K Ω . Do not drive this input high.

Do not connect to this input if the feature is not in use.

1PPS output

The pulse-per-second (PPS) output provides a pulse signal for timing purposes.

Pulse length (high state) is 200ms about 1 μ s synchronized to full UTC second.

The UTC time message is generated and put into output FIFO 300ms after PPS.

The exact time between the PPS and UTC time message delivery depends on message rate, message queue and communication baud rate.

1PPS output is LVCMOS 1.8V compatible.

Do not connect to this output if the feature is not in use.

Host Data Interface

UART

TX used for GPS data reports. RX used for receiver control.

The default protocol is NMEA@4,800bps 8-N-1.

Baud rates are selectable from 1200bps to 1.25Mbps.

The configuration for baud rates and respective protocols can be changed by commands via NMEA or OSP (SiRF Binary) protocols.

Input levels are 1.8 to 3.6V.

Output level of the UART TX is $V_{CC} - 0.1V$

2. Software Functions

The ORG4402 series module supports NMEA-0183 ASCII protocol and One Socket Protocol (SiRF Binary).

2.1. NMEA

NMEA Output Messages

Message	Description
GGA	Time, position and fix type data
GLL ¹	Latitude, longitude, UTC time of position fix and status
GSA	GPS receiver operating mode, satellites used in the position solution and DOP values
GSV	The number of GPS satellites in view, satellite ID, elevation, azimuth and SNR values
RMC	Time, date, position, course and speed data
VTG ¹	Course and speed information relative to the ground
ZDA ²	PPS timing message (synchronized to PPS)
155	Extended Ephemeris Proprietary Message
156,0x20	ECLM ACK/NACK
156,0x21	ECLM EE Get Age response
156,0x22	ECLM Get SGEE Age response
156,0x23	ECLM Download Initiate Request
156,0x24	ECLM Erase Storage File
156,0x25	ECLM Update File Content
156,0x26	ECLM Request File Content
160 ²	Watchdog Timeout and Exception Condition

Table 2-1: NMEA protocol output messages

NMEA Input Messages

Message ID	Message	Description
100	Set Serial Port	Set UART parameters and protocol
101	Navigation Initialization	Parameters required for start using X/Y/Z
103	Query/Rate Control	Query standard NMEA message and/or set output rate
104	LLA Navigation Initialization	Parameters required for start using Lat/Lon/Alt
105	Development Data On/Off	Development Data messages On/Off
106	Select Datum	Selection of an alternative map datum
107	Extended ephemeris proprietary message	
108	Extended ephemeris proprietary message	
110	Extended ephemeris debug	
114,0x16	ECLM start download	
114,0x17	ECLM file size	
114,0x18	ECLM packet data	
114,0x19	ECLM Get EE Age	
114,0x1A	ECLM Get SGEE Age	
114,0x1B	ECLM Host File Content	
114,0x1C	ECLM Host ACK/NACK	
117 ²	System Turn Off	
120	Storage Configuration Setting	

Table 2-2: NMEA protocol input messages

Note:

1. Not transmitted by default, can be enabled by \$PSRF103 command
2. Not implemented in current firmware

2.2. OSP (SiRF) Binary

OSP Binary Output Messages

MID (hex)	MID (dec)	Definition	Sub ID (hex)	Sub ID (dec)	Definition
0 x 02	2	Measured Navigation Data			
0 x 03	3	True Tracker Data			
0 x 04	4	Measured Tracking Data			
0 x 06	6	SW Version			
0 x 07	7	Clock Status			
0 x 08	8	50 BPS Subframe Data			
0 x 09	9	Throughput			
0 x 0A	10	Error ID			
0 x 0B	11	Command Acknowledgement			
0 x 0C	12	Command No Acknowledgement			
0 x 0D	13	Visible List			
0 x 0E	14	Almanac Data			
0 x 0F	15	Ephemeris Data			
0 x 10	16	Test Mode 1			
0 x 12	18	Ok To Send			
0 x 13	19	Navigation Parameters			
0 x 14	20	Test Mode 2			
0 x 1B	27	DGPS Status			
0 x 1C	28	Nav. Lib. Measurement Data			
0 x 1E	30	Nav. Lib. SV State Data			
0 x 1F	31	Nav. Lib. Initialization Data			
0 x FF	255	Development Data			

Table 2-3: OSP binary output messages

OSP Binary Input Messages

MID (hex)	MID (dec)	Definition	Sub ID (hex)	Sub ID (dec)	Definition
0 x 35	53	Advanced Power Management			
0 x 80	128	Initialize Data Source			
0 x 81	129	Switch to NMEA Protocol			
0 x 82	130	Set Almanac (upload)			
0 x 84	132	Software Version (Poll)			
0 x 86	134	Set Main Serial Port			
0 x 87	135	Switch Protocol			
0 x 88	136	Mode Control			
0 x 89	137	DOP Mask			
0 x 8A	138	MID_SET_DGPS_MODE			
0 x 8B	139	Elevation Mask			
0 x 8C	140	Power Mask			
0 x 8D	141	Editing Residual			
0 x 8E	142	Steady-State Detection			
0 x 8F	143	Static Navigation			
0 x 90	144	Poll Clock Status			
0 x 92	146	Poll Almanac			
0 x 93	147	Poll Ephemeris			
0 x 95	149	Set Ephemeris (upload)			
0 x 96	150	Switch Operating Mode			
0 x 97	151	Set Trickle Power Parameters			
0 x 98	152	Poll Navigation Parameters			
0 x A5	165	Set UART Configuration			
0 x A6	166	Set Message Rate			
0 x A7	167	Low Power Acquisition Parameters			
0 x A8	168	MID_POLL_CMD_PARAM			
0 x A9	169	Set Datum			
0 x AA	170	Set SBAS Parameters			
0 x AC	172	MID_DrIn	0 x 01	1	Set DrNavInit
			0 x 02	2	Set DrNavMode
			0 x 03	3	Set GyrFactCal
			0 x 04	4	Set DrSensParam
			0 x 05	5	Poll DrValid
			0 x 06	6	Poll GyrFactCal
			0 x 07	7	Poll DrSensParam
			0 x 13	19	DR Debug Information
0 x AF	175	Send Command String			
0 x B2	178	SIRF_MSG_SSB_TRACKER_IC	0 x 14	20	Patch Storage Control
			0 x 22	34	Patch Memory Load Request
			0 x 26	38	Patch Memory Exit Request
			0 x 28	40	Patch Memory Start Request
			0 x 90	144	Patch Manager Prompt
			0 x 91	145	Patch Manager Ack.
0 x CD	205	Set Generic Software Control	0 x 10	16	Software Commanded OFF
0 x D1	209	MID_QUERY_REQ			
0 x D2	210	MID_POS_REQ			

Table 2-4: OSP binary input messages

MID (hex)	MID (dec)	Definition	Sub ID (hex)	Sub ID (dec)	Definition
0 x D3	211	MID_SET_AIDING	0 x 01	1	SET_IONO
			0 x 02	2	SET_EPH_CLOCK
			0 x 03	3	SET_ALM
			0 x 04	4	SET_ACQ_ASSIST
			0 x 05	5	SET_RT_INTEG
			0 x 06	6	SET_UTC_MODEL
			0 x 07	7	SET_GPS_TOW_ASSIST
			0 x 08	8	SET_AUX_NAV
			0 x 09	9	SET_AIDING_AVAIL
0 x D4	212	MID_STATUS_REQ	0 x 01	1	EPH_REQ
			0 x 02	2	ALM_REQ
			0 x 03	3	B_EPH_REQ
			0 x 04	4	TIME_FREQ_APPROX_POS_REQ
			0 x 05	5	CH_LOAD_REQ
			0 x 06	6	CLIENT_STATUS_REQ
			0 x 07	7	OSP_REV_REQ
			0 x 08	8	SERIAL_SETTINGS_REQ
0 x D5	213	MID_SESSION_CONTROL_REQ	0 x 01	1	SESSION_OPEN_REQ
0 x D6	214	MID_HW_CONFIG_RESP	0 x 02	2	SESSION_CLOSE_REQ
0xD7	215	MID_AIDING_RESP	0 x 01	1	APPROX_MS_POS_RESP
			0 x 02	2	TIME_TX_RESP
			0 x 03	3	FREQ_TX_RESP
			0 x 04	4	SET_NBA_SF1_2_3
			0 x 05	5	SET_NBA_SF4_5
0xD8	216	MID_MSG_ACK_IN	0 x 01	1	ACK_NACK_ERROR
			0 x 02	2	REJECT
0xD9	217		0 x 01	1	SENSOR_ON_OFF
0xDA	218	MID_PWR_MODE_REQ	0 x 00	0	FP_MODE_REQ
			0 x 01	1	APM_REQ
			0 x 02	2	MPM_REQ
			0 x 03	3	TP_REQ
			0 x 04	4	PTF_REQ
0xDB	219	MID_HW_CTRL_IN	0 x 01	1	VCTCXO
			0 x 02	2	ON_OFF_SIG_CONFIG
0xDC	220	MID_CW_CONTROLLER_REQ	0 x 01	1	CONFIG
			0 x 02	2	EVENT_REG
			0 x 03	3	COMMAND_SCAN
			0 x 04	4	CUSTOM_MON_CONFIG
			0 x 05	5	FFT_NOTCH_SETUP
0xE1	225	MID_SiRFOutput	0 x 06	6	STATISTICS
			0 x 07	7	Statistics with Aiding

Table 2-4: OSP binary input messages

MID (hex)	MID (dec)	Definition	Sub ID (hex)	Sub ID (dec)	Definition
0xE8	232	MID_EE_INPUT	0 x 01	1	SSB_EE_SEA_PROVIDE_EPH
			0 x 02	2	SSB_EE_POLL_STATE
			0 x 10	16	SSB_EE_FILE_DOWNLOAD
			0 x 11	17	SSB_EE_QUERY_AGE
			0 x 12	18	SSB_EE_FILE_PART
			0 x 13	19	SSB_EE_DOWNLOAD_TCP
			0 x 14	20	SSB_EE_SET_EPHEMERIS
			0 x 15	21	SSB_EE_FILE_STATUS
			0 x 16	22	ECLM Start Download
			0 x 17	23	ECLM File Size
			0 x 18	24	ECLM Packet Data
			0 x 19	25	Get EE Age
			0 x 1A	26	Get SGEE Age
			0 x 1B	27	ECLM Host File Content
			0 x 1C	28	ECLM Host ACK/NACK
			0 x 1D	29	ECLM Get NVM Header
			0 x FD	253	EE_STORAGE_CONTROL
			0 x FE	254	SSB_EE_DISABLE_EE_SECS

Table 2-4: OSP binary input messages

3. Handling Information

3.1. Product Packaging and Delivery

-TBA-

3.2. ESD Sensitivity

The ORG4402 modules are ESD sensitive devices and should be handled with care.



3.3. Compliances

The ORG4402 modules comply with the following standards:

- Pb-Free/RoHS (Directive 2002/95/EC on the restriction of the use of certain hazardous substances in electrical and electronic equipment)
- ISO 9001:2000 accredited manufacturing facility



3.4. Safety Information

Improper handling and use can cause permanent damage to the device.

There is also the possible risk of personal injury from mechanical trauma or shocking hazard.

3.5. Disposal Information

The product should not be treated as household waste.

For more detailed information about recycling electronic components, please contact your local waste management authority.



4. Mechanical Specifications

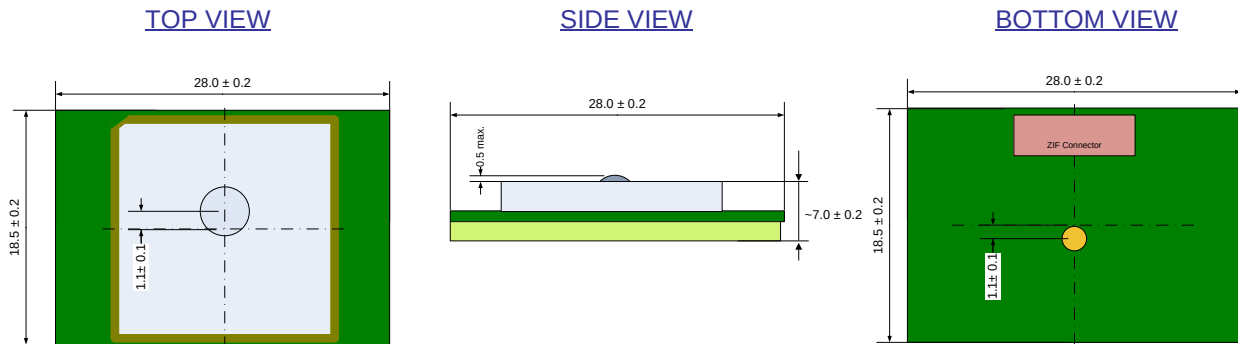


Figure 4-1: ORG4402 mechanical drawing

Dimensions	Length	Width	Height
mm	28.0 ± 0.2	18.5 ± 0.2	7.0 ± 0.2
inch	1.102 ± 0.008	0.728 ± 0.008	0.276 ± 0.008

Weight	
gr	8
oz	0.28

Table 4-1: ORG4402 mechanical information