Surface Mount **Monolithic Amplifier**

DC-1 GHz

Product Features

- Wideband, DC to 1 GHz
- Exact footprint substitute for Avago's MSA-0886
- Internally Matched to 50 Ohms
- Very high gain, 32.5 dB at 0.1GHz

Typical Applications

- Cellular
- PCN instrumentation





+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

ALTERNATE PART: MAR-8ASM+ App Note: AN-60-060

RF IN

General Description

MAR-8SM+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a Micro-X package. MAR-8SM+ uses Darlington configuration and is fabricated using silicon technology. Expected MTBF is 20,000 years at 85°C case temperature.

simplified schematic and pin description O RF-OUT and DC-IN **RF-OUT and DC-IN** 3 **BF IN** 0 GND 2

GROUND

Function	Pin Number	Description	
RF IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".	
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.	

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Electrical Specifications at 25°C and 36mA, unless noted

Parameter		Min.	Тур.	Max.	Units
Frequency Range*		DC		1	GHz
Gain	f=0.1 GHz		32.5		dB
	f=1 GHz	19 ²	22.5		
Input Return Loss			I	Į	1
		Input and output impedances are not 50 ohms, see S-parameter data. Conditionally stable,			stable,
Output Return Loss		source and load VSWR< 3:1 required.		ed.	
Output Power @ 1 dB compression	f=1 GHz		+12.5		dBm
Output IP3	f=1 GHz		+27		dBm
Noise Figure	f=1 GHz		3.3		dB
Recommended Device Operating Current		36		mA	
Device Operating Voltage		7.8		V	
Thermal Resistance, junction-to-case ¹		140		°C/W	

*Guaranteed specification DC-1 GHz. Low frequency cut off determined by external coupling capacitors.

Absolute Maximum Ratings

Parameter	Ratings	
Operating Temperature	-20°C to 85°C	
Storage Temperature	-55°C to 100°C	
Operating Current	65mA	
Power Dissipation	500mW	
Input Power	13dBm	

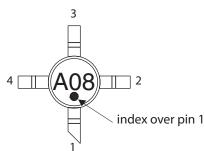
Note: Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation. ¹Case is defined as ground leads.

²Full temperature range.

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Product Marking



Markings in addition to model number designation may appear for internal quality control purposes.

Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: WW107-1 Plastic micro-x, .085 body diameter, lead finish: tin/silver/nickel

Tape & Reel: F4

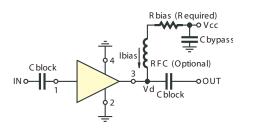
7" Reels with 20, 50, 100, 200, 500, 1K devices 13" Reels with 2K, 4K devices

Suggested Layout for PCB Design: PL-253

Evaluation Board: TB-411-8+

Environmental Ratings: ENV08

Recommended Application Circuit



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS					
Vcc	"1%" Res. Values (ohms) for Optimum Biasing				
10	63.4				
11	90.9				
12	115				
13	143				
14	169				
15	200				

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ESD Rating

Human Body Model (HBM): Class 1B (500v to < 1000v) in accordance with ANSI/ESD STM 5.1 - 2001

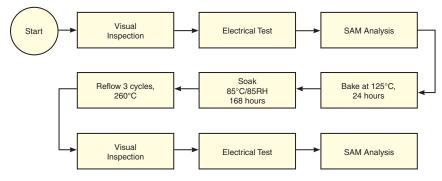
Machine Model (MM): Class M3 (200v to < 400v) in accordance with ANSI/ESD STM 5.2 - 1999

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020C

No.	Test Required	Condition	Standard	Quantity
1	Visual Inspection	Low Power Microscope Magnification 40x	MIP-IN-0003 (MCT spec)	45 units
2	Electrical Test	Room Temperature	SCD (MCL spec)	45 units
3	SAM Analysis	Less than 10% growth in term of delamination	J-Std-020C (Jedec Standard)	45 units
4	Moisture Sensitivity Level 1	Bake at 125°C for 24 hours Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-Std-020C (Jedec Standard)	45 units

MSL Test Flow Chart



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