675 MHz (fixed) 50Ω

The Big Deal

- · Low phase noise and spurious
- Fixed frequency without external programming
- Integrated microcontroller
- · Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

Product Overview

The KSN-675A-2C19+ is a Frequency Synthesizer, designed to operate 675MHz for wire-line broadband access application. The KSN-675A-2C19+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase noise: -110 dBc/Hz typ. @ 10 kHz offset • Comparison spurious: -90 dBc typ. • Reference spurious: -90 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-675A-2C19+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-675A-2C19+ to be used in compact designs.

& shopping online see web site

Frequency Synthesizer

KSN-675A-2C19+

 50Ω 675 MHz (fixed)

Features

- Fixed frequency without external programming
- Integrated microcontroller
- · High reliability over temperature changes
- · Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3V)
- Small size 0.80" x 0.58" x 0.15"

Applications

· Wire-line broadband access



CASE STYLE: DK1042 PRICE: \$29.95 ea. QTY (1-9)

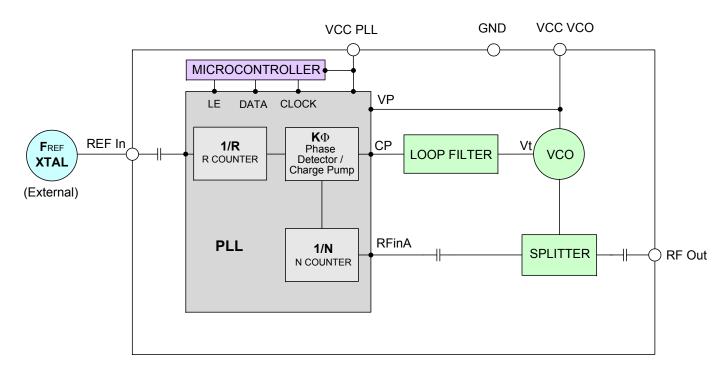
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

General Description

The KSN-675A-2C19+ is a Frequency Synthesizer, designed to operate 675MHz for wire-line broadband access application. The KSN-675A-2C19+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-675A-2C19+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic



Mini-Circuits

For detailed performance specs & shopping online see web site

Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units	
Frequency Range (fixed)		-	675	-	675	MHz	
Step size		-	-	500	-	kHz	
Settling Time (Power on to	o lock)	Within ± 1 kHz	-	30	-	mSec	
Output Power		-	0	+3	+6	dBm	
		@ 100 Hz offset	-	-95	-		
		@ 1 kHz offset	-	-92	-83	1	
SSB Phase Noise		@ 10 kHz offset	-	-110	-105	dBc/Hz	
		@ 100 kHz offset	-	-134	-128	1	
		@ 1 MHz offset	-	-154	-148	1	
Integrated SSB Phase Noise		@ 10 kHz to 3 MHz	-	-70	-		
Reference Spurious Suppression		Ref. Freq. 27 MHz	-	-90	-75]	
Comparison Spurious Sup	pression	Step Size 500 kHz	-	-90	-75	dBc	
Non - Harmonic Spurious	Suppression	-	-	-90	-		
Harmonic Suppression		-	-	-20	-10		
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	V	
PLL Supply Voltage		+3.00	+2.85	+3.00	+3.15	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
VCO Supply Current		-	-	28	40	mA	
PLL Supply Current		-	-	9	20	IIIA	
	Frequency	27 (square wave)	-	27	-	MHz	
Reference Input	Amplitude	1	-	1	-	V _{P-P}	
(External)	Input impedance	-	-	100	-	ΚΩ	
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz	
RF Output port Impedance	RF Output port Impedance		-	50	-	Ω	
Digital Look Datast	Locked	-	2.45	-	3.15	V	
Digital Lock Detect	Unlocked	-	-	-	0.40	V	

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	5.8V
PLL Supply Voltage	3.6V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin,VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin,VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	POWER OUTPUT		Y POWER OUTPUT VCO CURRENT		PLL CURENT		т		
(MHz)		(dBm)			(mA)			(mA)	
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
675	2.75	3.06	3.08	27.27	28.75	29.73	8.45	9.58	11.17

FREQUENCY	HARMONICS (dBc)					
(MHz)		F2			F3	
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
675	-21.11	-20.20	-19.99	-24.47	-22.95	-22.42

FREQUENCY @TEMP.		PHASE NOISE (dBc/Hz)				
		@OFFSETS				
		100Hz	1kHz	10kHz	100kHz	1MHz
	-45°C	-94.18	-95.36	-111.19	-135.58	-155.58
675	+25°C	-96.70	-92.40	-110.99	-134.32	-154.34
	+85°C	-95.03	-92.26	-109.57	-132.89	-152.98

COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 675MHz+(n*Fcomparison) (dBc) note 1				
n	-45°C	+25°C	+85°C		
-5	-99.01	-104.16	-98.20		
-4	-97.22	-102.78	-96.72		
-3	-94.83	-100.68	-94.16		
-2	-91.71	-97.56	-91.05		
-1	-85.93	-93.46	-84.95		
0 ^{note 2}	-	-	-		
+1	-86.36	-94.54	-85.27		
+2	-92.22	-101.51	-91.25		
+3	-95.91	-107.35	-94.56		
+4	-98.82	-112.62	-97.02		
+5	-100.92	-113.35	-98.46		

Note 1: Comparison frequency 500 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 675MHz+(n*Freference) (dBc) note 3				
n	-45°C	+25°C	+85°C		
-5	-117.12	-124.19	-117.35		
-4	-97.97	-103.13	-117.25		
-3	-106.66	-106.60	-104.49		
-2	-102.42	-112.53	-105.33		
-1	-95.36	-91.33	-90.84		
o ^{note 4}	-	-	-		
+1	-92.60	-92.29	-92.60		
+2	-109.10	-110.05	-105.44		
+3	-106.74	-109.83	-106.99		
+4	-102.36	-105.07	-124.61		
+5	-132.12	-128.47	-115.90		

Note 3: Reference frequency 27 MHz

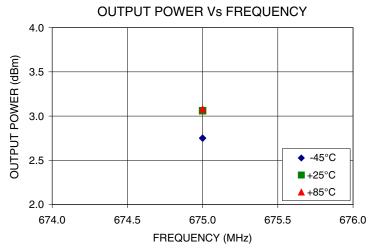
Note 4: All spurs are referenced to carrier signal (n=0).

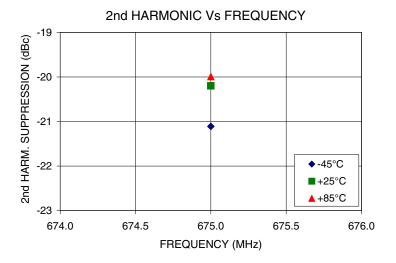


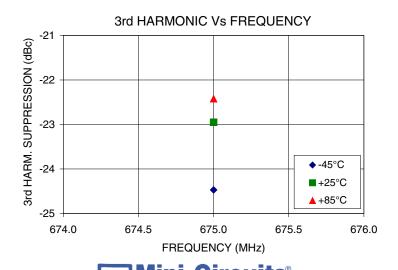
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Typical Performance Curves





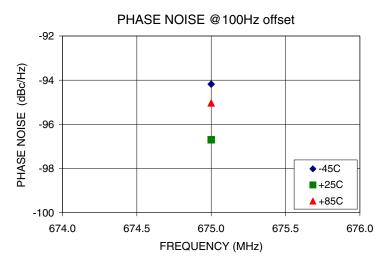


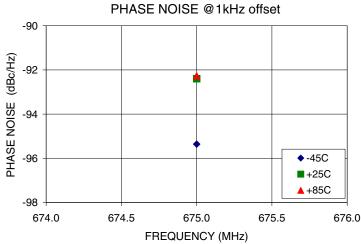
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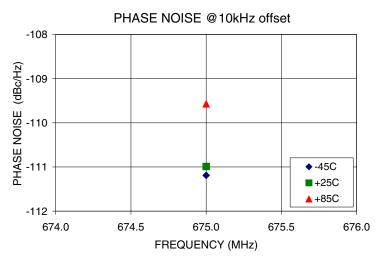
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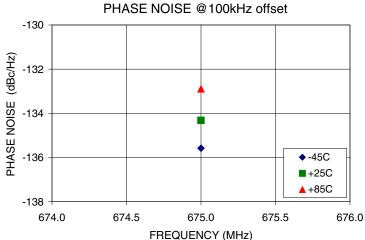
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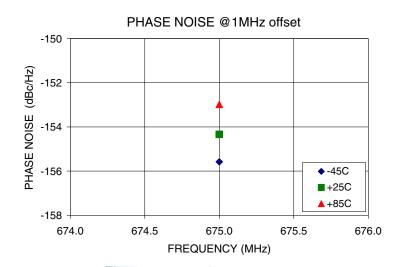












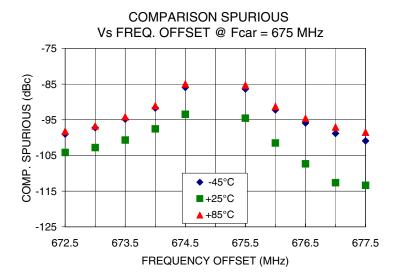
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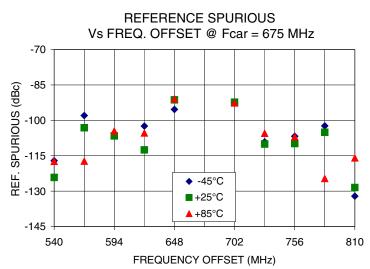
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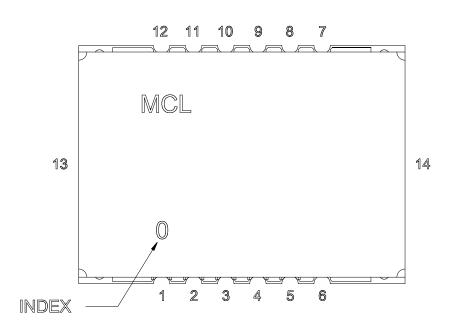








Pin Configuration

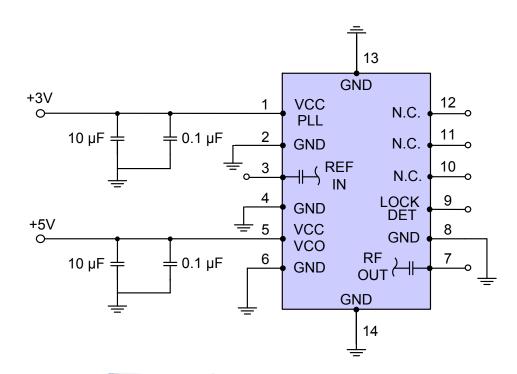


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	NOT CONNECTED
11	NOT CONNECTED
12	NOT CONNECTED
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

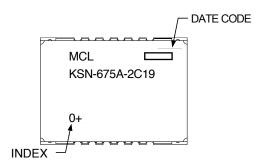




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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-2+F

Environment Ratings: ENV03T2

