# Pur Surface Mount LVDS Clock Oscillator



2111 Comprehensive Drive

Aurora, Illinois 60505 Phone: 630-851-4722

Fax: 630-851-5040

www.conwin.com

US Headquarters: 630-851-4722 European Headquarters: +353-61-472221

# **Description:**

The Connor Winfield Lxxx - Series are 5.0x7.0mm Surface Mount, LVDS, Fixed Frequency Crystal Controlled Oscillator (XO) designed for applications requiring tight frequency stability, wide temperature range and low jitter. Operating at 2.5V or 3.3V supply voltage, the Lxxx - Series provides an LVDS Differential Outputs with enable / disable function. The surface mount package is designed for high-density mounting and is optimum for mass production.



Model Lxxx - Series
5.0 x7.0mm Surface Mount Package
2.5V or 3.3V Operation
LVDS Output Logic
Frequency Stabilities Available:
+/-20ppm,+/-25ppm, +/-50ppm, +/-100ppm
Temperature Ranges Available:
0 to 70°C, -40 to 85°C, 0 to 85°C, -20 to 70°C
Low Jitter <1ps RMS
Tri-State Enable/Disable on Pad 1 or 2
Tape and Reel Packaging
RoHS Compliant / Lead Free

# **Absolute Maximum Ratings**

	Absolute N	/laximum Ha	tings		
Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage (Vcc)	-0.5	-	5.0	Vdc	
Input Voltage (Vc)	-0.5	-	Vcc + 0.5	Vdc	
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Parameter	Minimum	Nominal	Maximum	Units	Notes
Output Frequency (Fo)	19.44	-	312.5	MHz	
Total Frequency Tolerance (Se	ee Ordering Infor	mation for full pa	art number)		
Model Lx4x	-20	-	20	ppm	1
Model Lx1x	-25	-	25	ppm	1
Model Lx2x	-50	-	50	ppm	1
Model Lx3x	-100	-	100	ppm	1
Operating Temperature Range					
Model L1xx	0	-	70	°C	
Model L2xx	-40	-	85	°C	2
Model L3xx	0	-	85	°C	
Model L4xx	-20	-	70	°C	
Supply Voltage (Vcc)					
Model Lxx2 E/D Pad 1	2.375	2.5	2.625	Vdc	
Model Lxx3 E/D Pad 1	3.135	3.3	3.465	Vdc	
Model Lxx4 E/D Pad 2	2.375	2.5	2.625	Vdc	
Model Lxx5 E/D Pad 2	3.135	3.3	3.465	Vdc	
Supply Current (Icc)	-	45	65	mA	
Jitter:					
Period Jitter	-	2.5	3.5	ps RMS	
Integrated Phase Jitter (BW = 12 KHz to 20 MHz)					
Fo ≥ 100 MHz	-	0.5	0.7	ps RMS	
80 MHz $\leq$ Fo $\leq$ 100 MHz	-	0.8	1.0	ps RMS	
Fo <80 MHz	-	1.5	2.5	ps RMS	
SSB Phase Noise					
@ 10 Hz offset	-	-60	-	dBc/Hz	
@ 100 Hz offset	-	-90	-	dBc/Hz	
@ 1 KHz offset	-	-115	-	dBc/Hz	
@ 10 KHz offset	-	-140	-	dBc/Hz	
@ 100 KHz offset		-145		dBc/Hz	
Start-Up Time	-	5	10		ms
Input Characteristics					
Parameter	Minimum	Nominal	Maximum	Units	Notes
Frable / Disable Outland					

Parameter	Minimum	Nominal	Maximum	Units	Notes	
Enable / Disable Option:						
Models Lxx2, Lxx3 E/D Pad 1. N/	C Pad 1					
Models Lxx4, Lxx5 E/D Pad 2. N/	C Pad 2					
Enable Input Voltage - (High) -(Vih)	70%Vcc	-	-	Vdc	3	
Disable Input Voltage - (Low) - (Vil)	-	-	30%Vcc	Vdc	3	
Enable Time	-	-	500	us		
Disable Time	-	-	200	ns		
Standby Current (When Osc. is disal	oled) -	-	30	uA		

# **LVDS Output Characteristics**

Minimum	Nominal	Maximum	Units	Notes
-	100	-	Ohm	
250	-	450	mV	4
500	700	900	mV	
45	50	55	%	5
0% -	0.3	0.7	ns	
	250 500 45	- 100 250 - 500 700 45 50	- 100 - 250 - 450 500 700 900 45 50 55	-         100         -         Ohm           250         -         450         mV           500         700         900         mV           45         50         55         %



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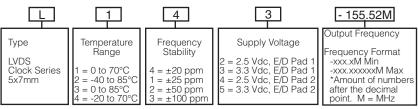
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#### Notes:

- ole S. Includes calibration @ 25°C, frequency stability vs. change in temperature, supply voltage and load variations, shock and vibration and 20 years aging. Models L242, L243, L244 and L245 are not available above 260 MHz. 2. Models L242, L243, L244 and L245 are not available above 260 MHz.
- When the oscillator is disabled the outputs are at high impedance. Outputs are enabled with no connection on E/D pad.
- 4. Vod is measured with a 100 ohm resistor between the true and the complementary outputs. 5. Duty cycle measured at 50% of output voltage swing..

#### **Ordering Information**



Example: Part Number L143-155.52M = LVDS Output,

0 to 70, +/-20ppm, 3.3Vdc, E/D Pad 1, Output Frequency 155.52MHz

To order an L143 with an output frequency of: 25 MHz = L143-025.0M 44.736 MHz = L143-044.736M 155.52 MHz = L143-155.52M

Attention: Models L242, L243, L244 and L235 are not available above 260 MHz.

# Package Characteristics

Hermetically sealed ceramic package and metal cover Package

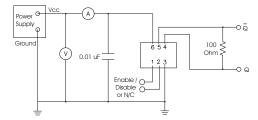
#### **Environmental Characteristics**

Vibration:	Vibration per Mil Std 883E Method 2007.3 Test Condition A.
Shock:	Mechanical Shock per Mil Std 883E Method 2002.4 Test Condition B.
Soldering P	rocess: RoHS compliant lead free See soldering profile on page 2

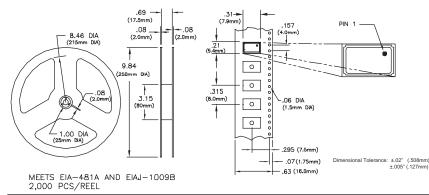
#### **Enable / Disable Function**

Function:	Output
Low:	Disabled (High Impedance)
High or Open:	Enabled

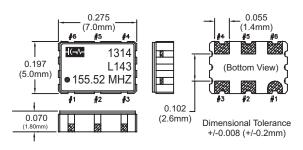
# **Test Circuit**



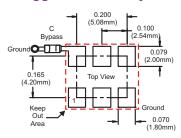
### Tape and Reel Dimensions



# **Package Outline**



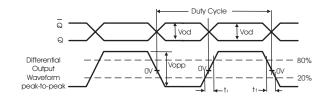
# Suggested Pad Layout



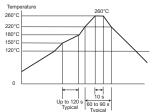
#### **Pad Connections**

Models: Lxx2, Lxx3	Models: Lxx4, Lxx5		
1: Enable / Disable	_1: N/C		
_2 N/C	2 Enable / Disable		
3: Ground	3: Ground		
4: Output Q	4: Output Q		
5: Complementary Output Q	5: Complementary Output Q		
6: Supply Voltage (Vcc)	6: Supply Voltage (Vcc)		

# **Output Waveform**



#### Solder Profile



Meets IPC/JEDEC J-STD-020C

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